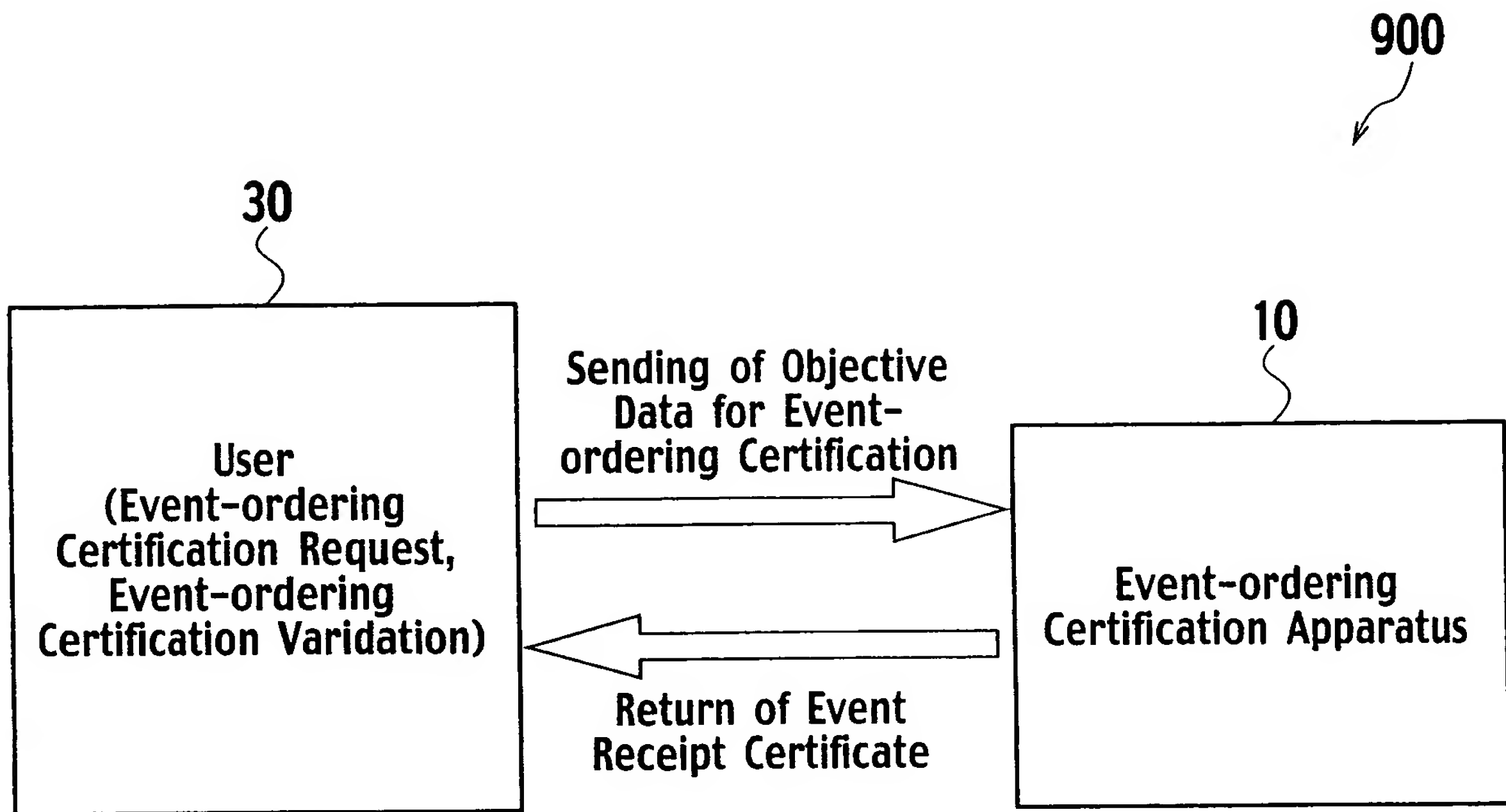


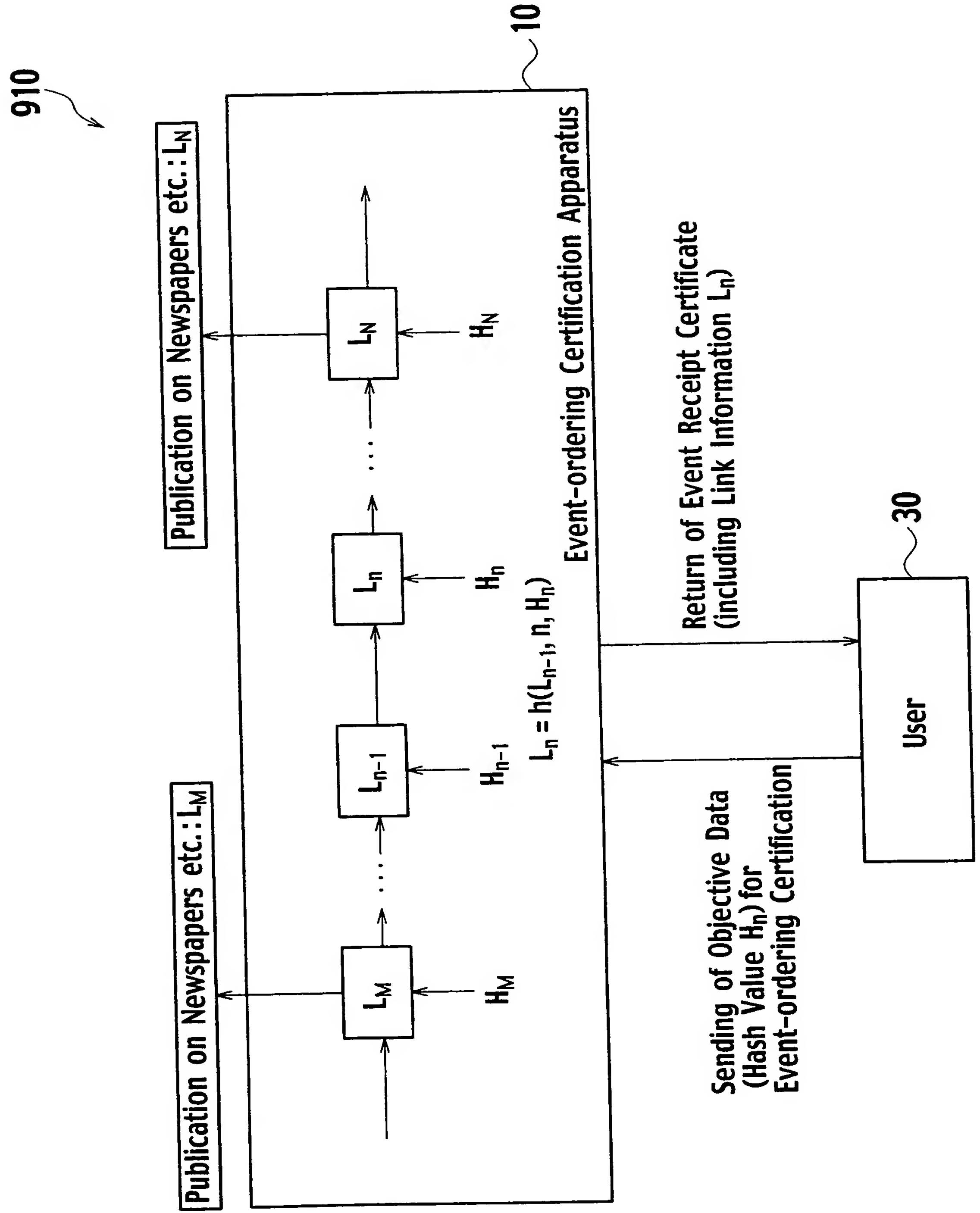
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FIG. 1



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FIG. 2



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FIG. 3

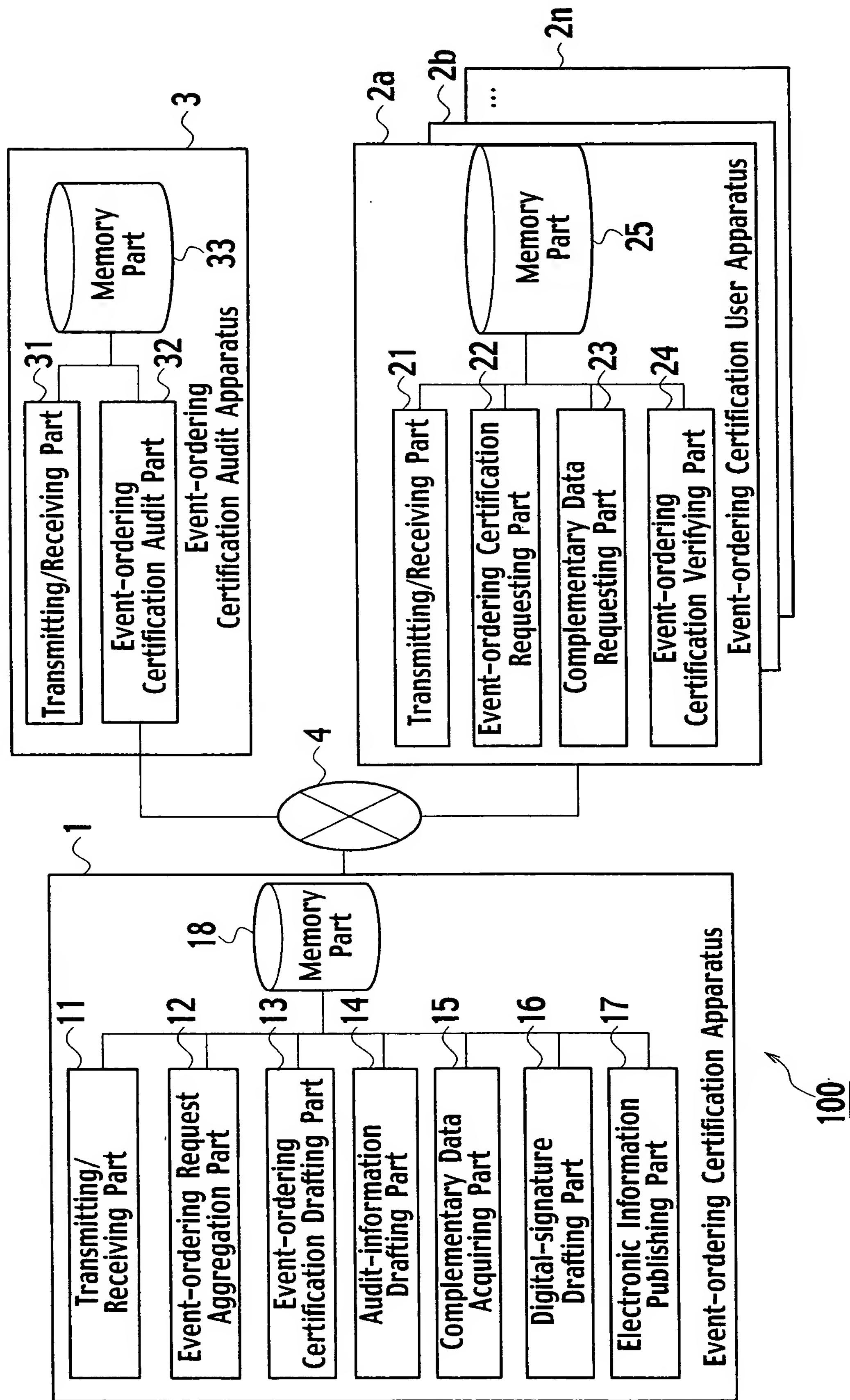


FIG. 4

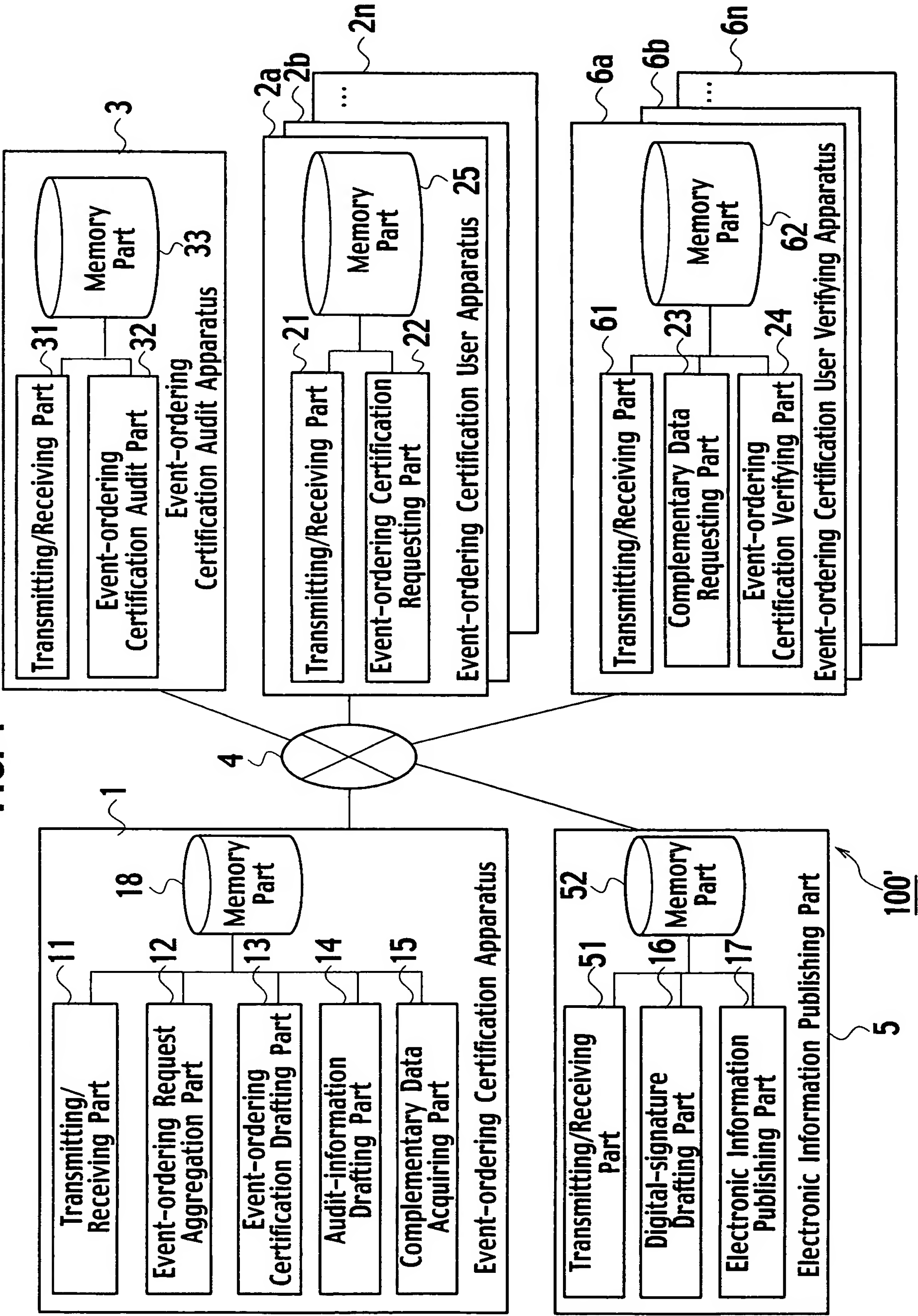


FIG. 5

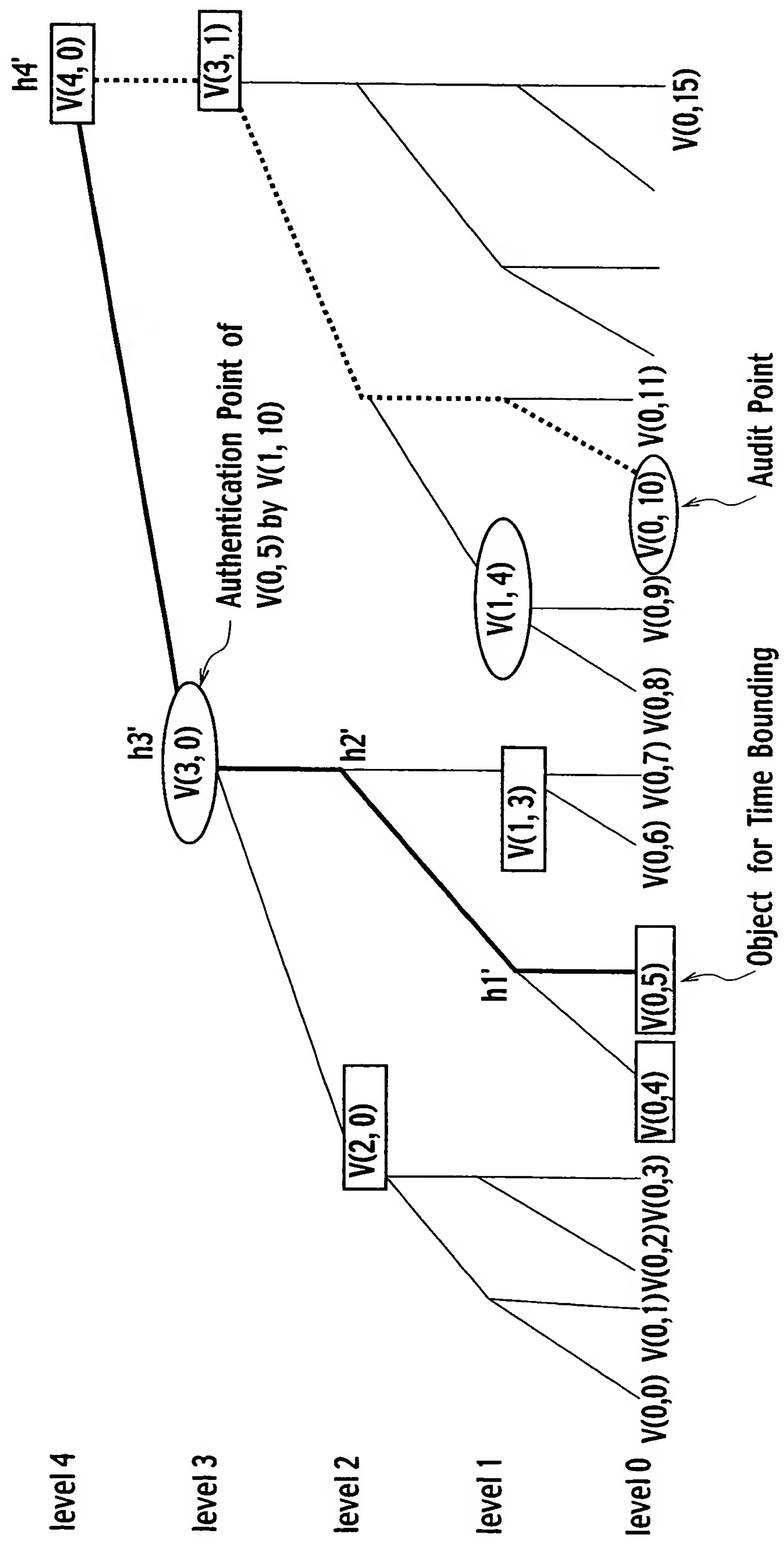


FIG. 6

ITEM	SIGN	REQUIRED
Original Data	y	<input type="radio"/>
Sequentially Assigned Data-item	z	<input type="radio"/>
Sequential Aggregation Tree No.	n	<input type="radio"/>
Sequential Aggregation Tree Leaf No.	i	<input type="radio"/>
Immediate Complementary Data (Positional Info. Assigned Value)	HK	
Digital Signature	DS	

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FIG. 7

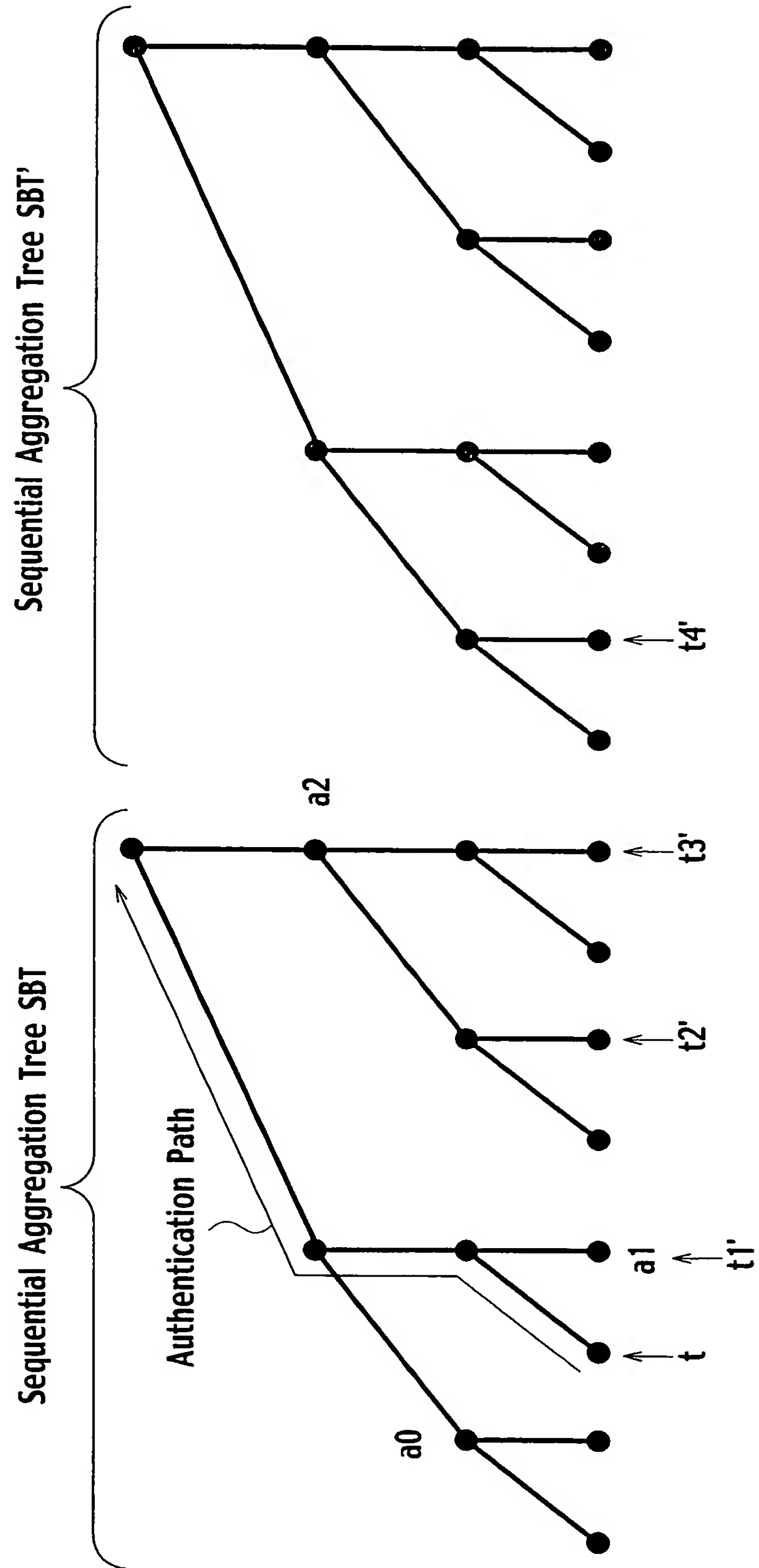
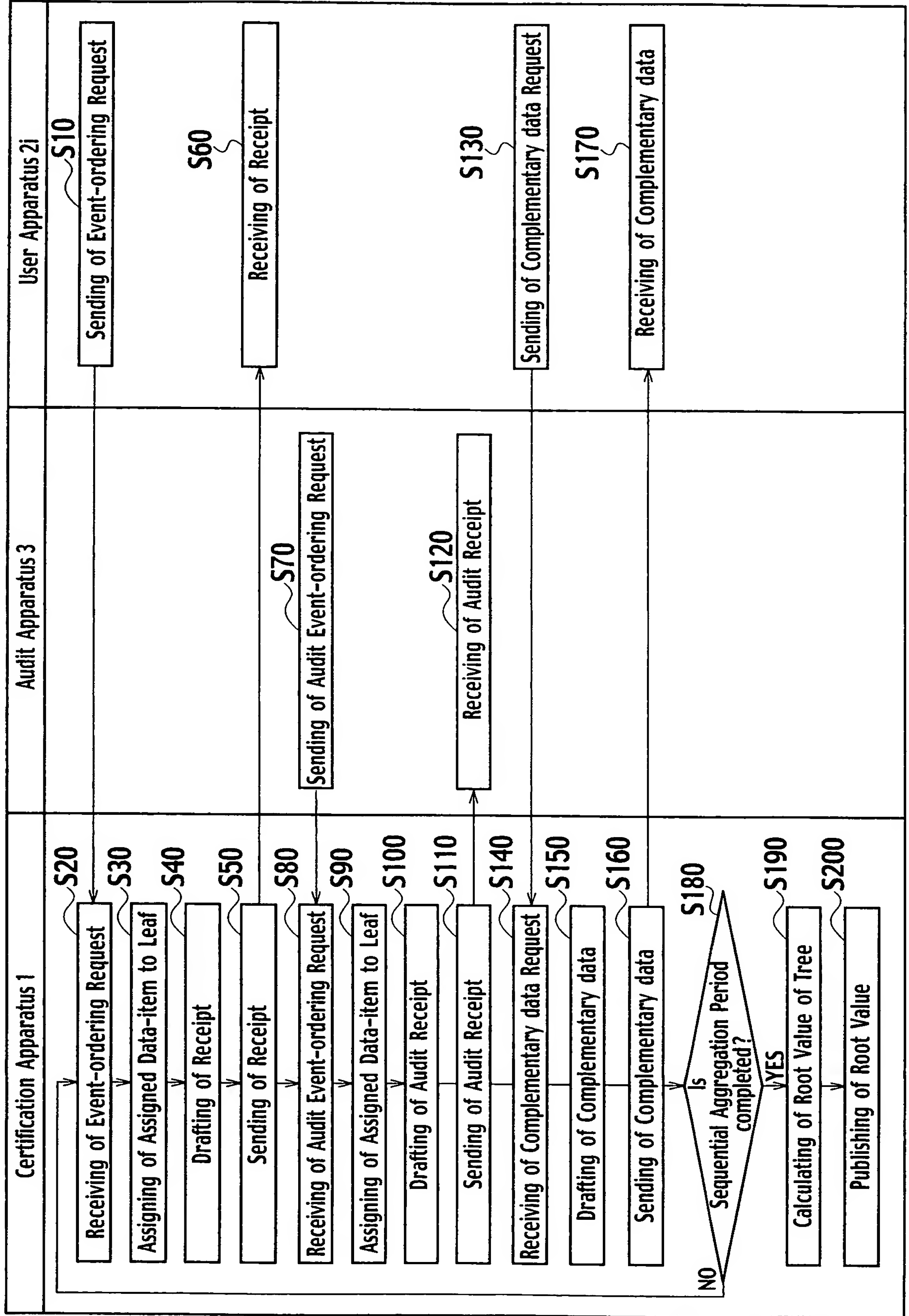


FIG. 8



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FIG. 9

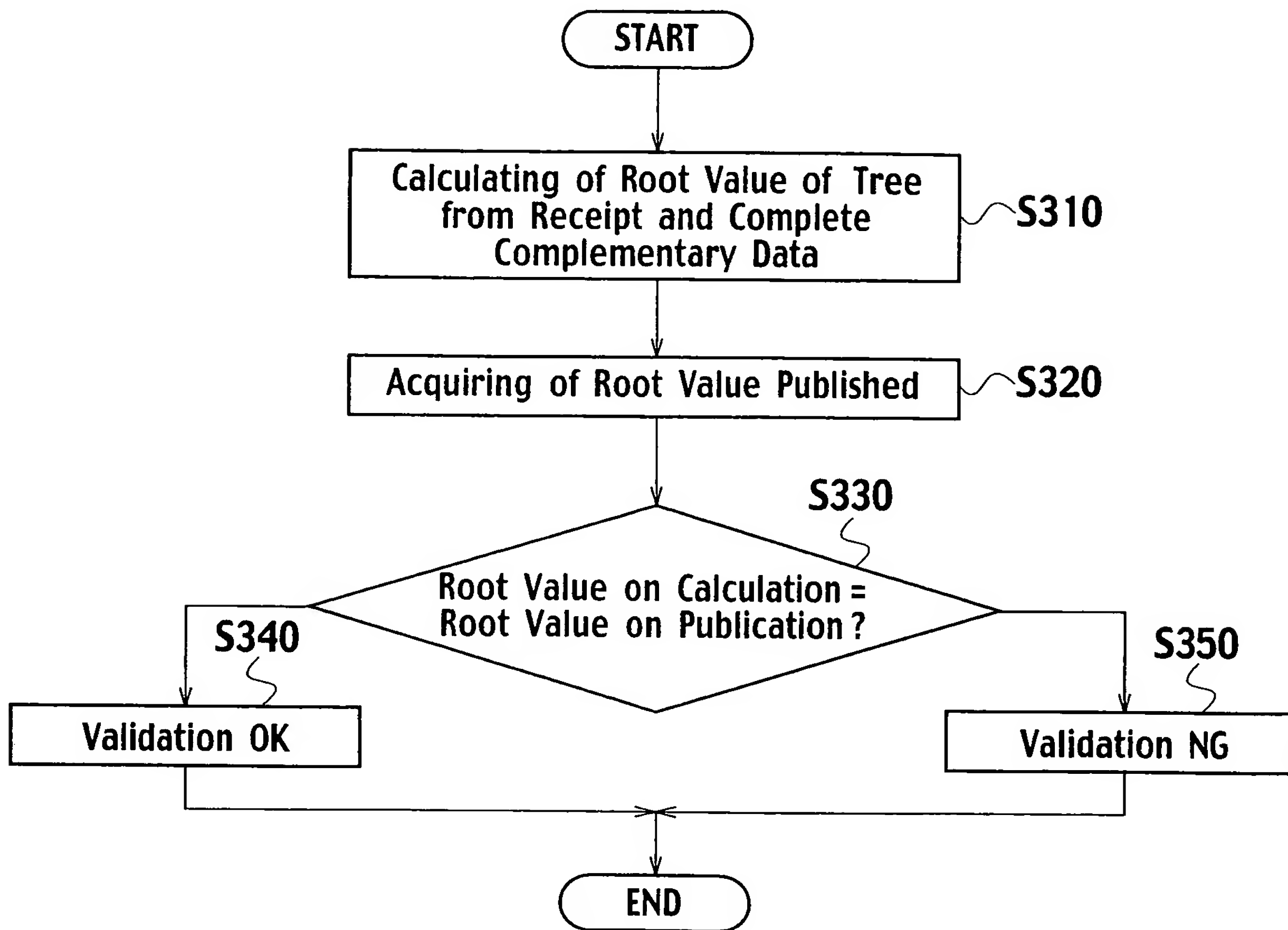


FIG. 10

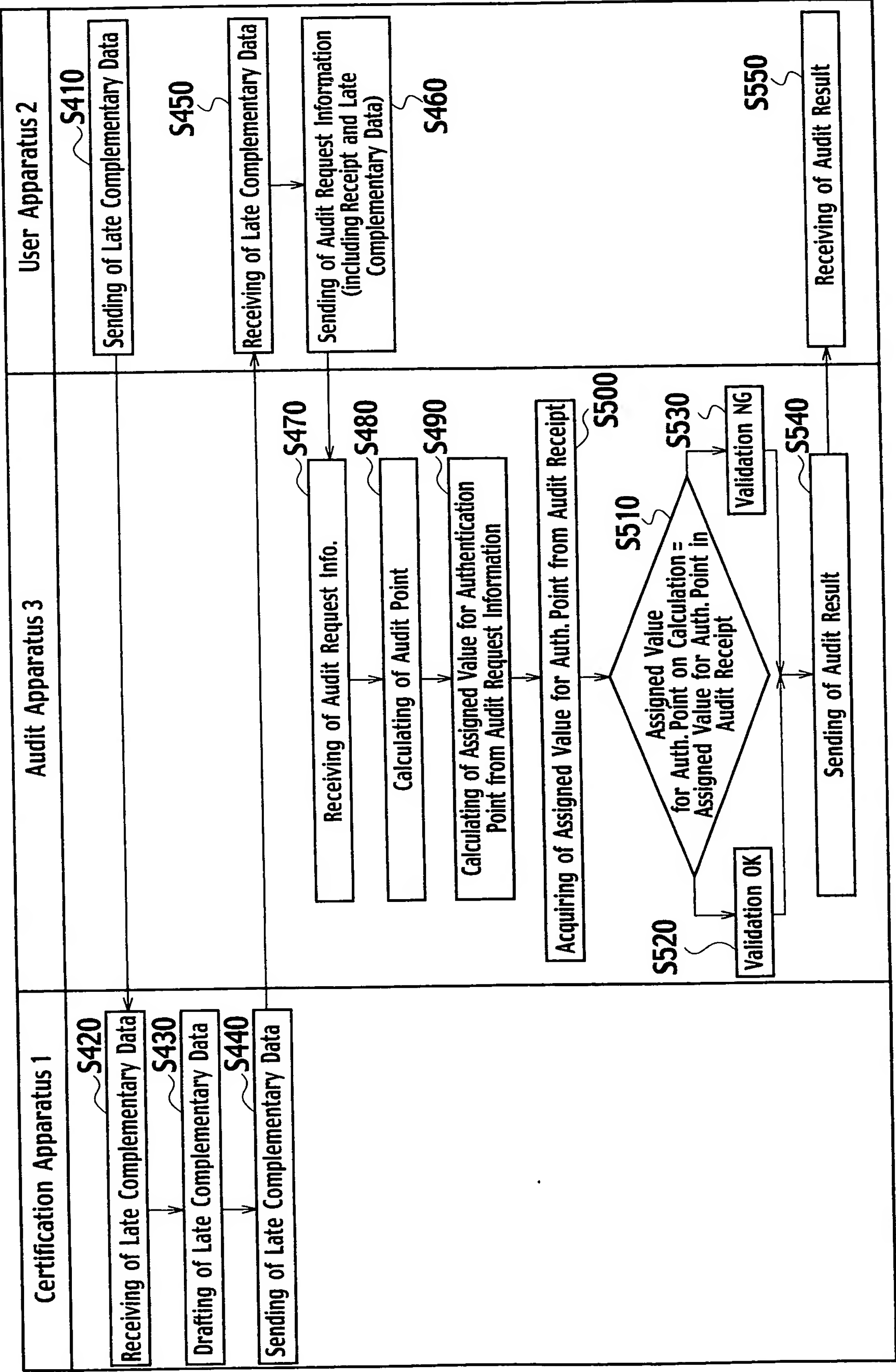


FIG. 11

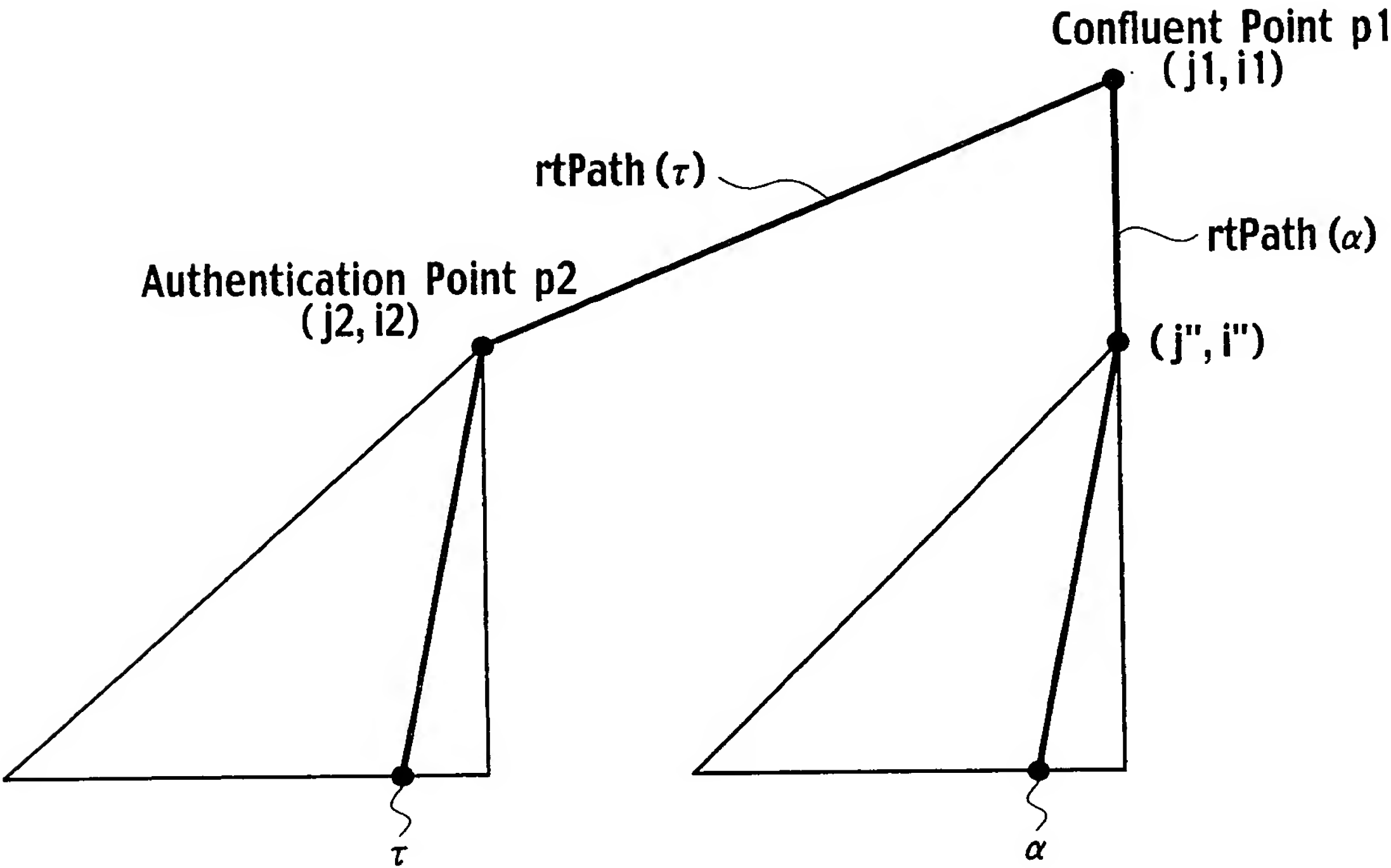


FIG. 12

Validation Result 1	<p>Certification Apparatus 1</p> <p>Receiving Point of Event-ordering Request (corres. user point τ)</p> <p>Sending Point of Audit Receipt (corres. user point α)</p> <p>Time t</p>
Validation Result 2	<p>Certification Apparatus 1</p> <p>Receiving Point of Event-ordering Request (corres. user point τ)</p> <p>Receiving Point of Acceptance of Audit Receipt (corres. user point α)</p> <p>Time t</p>

FIG. 13

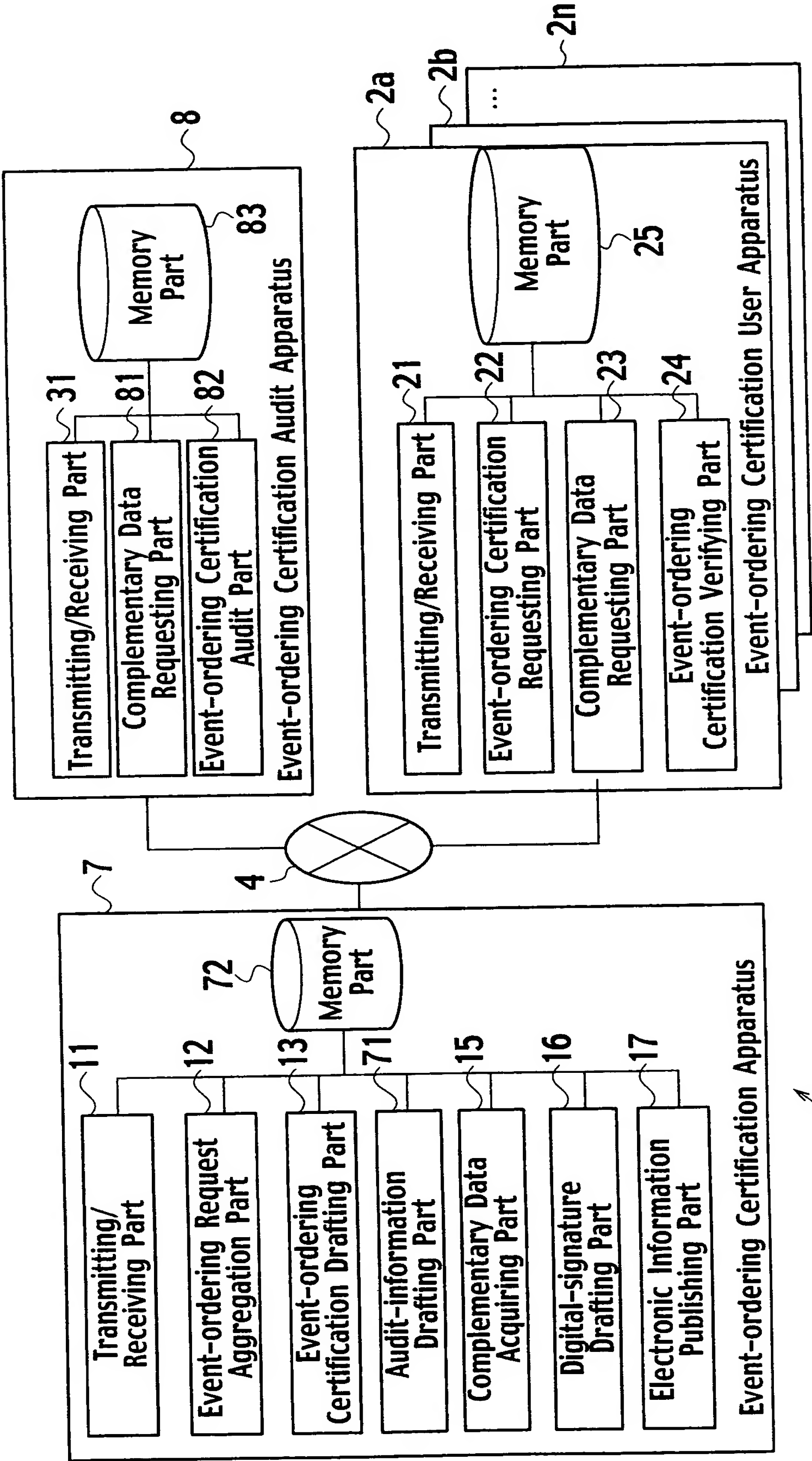


FIG. 14

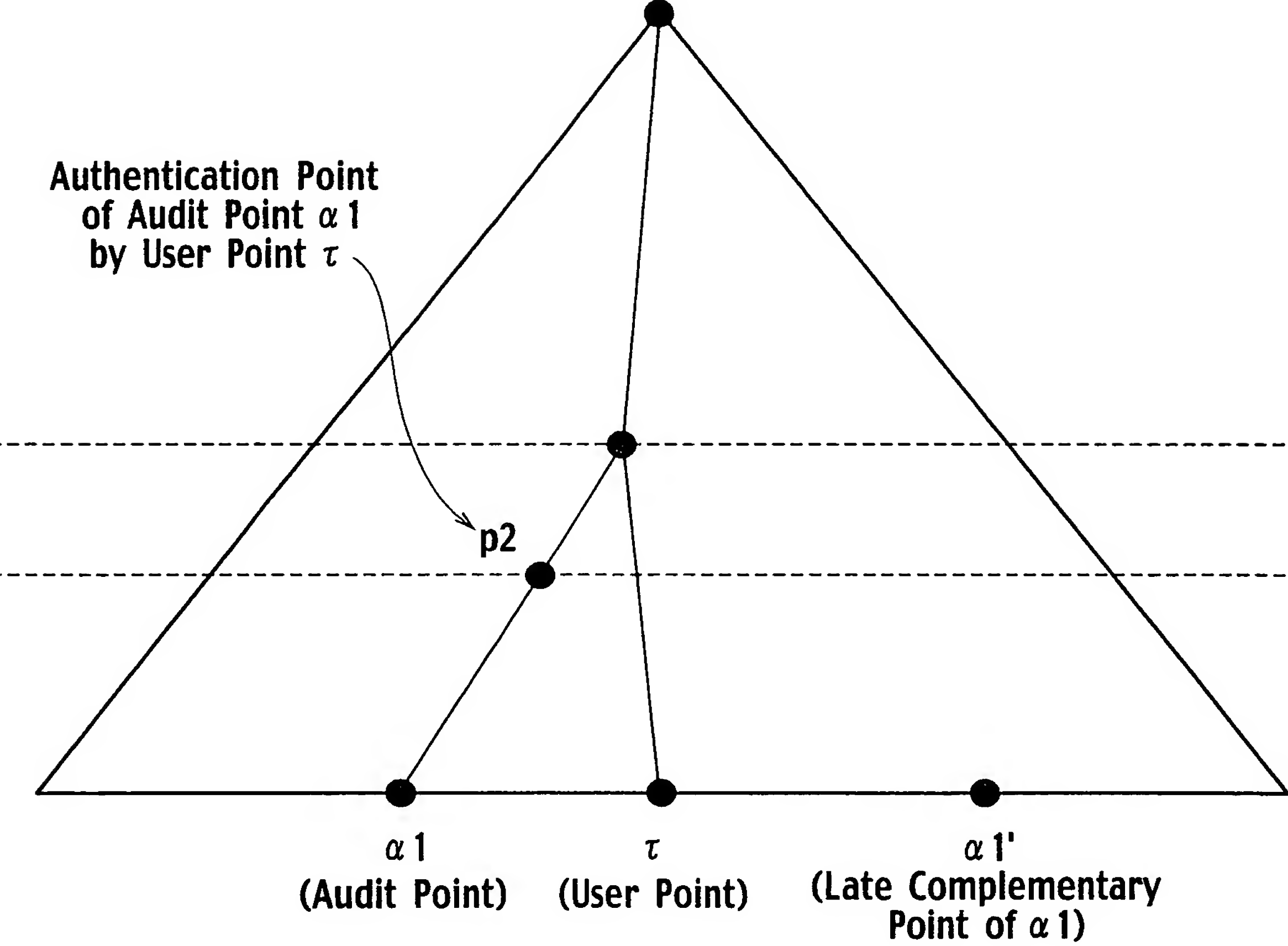


FIG. 15

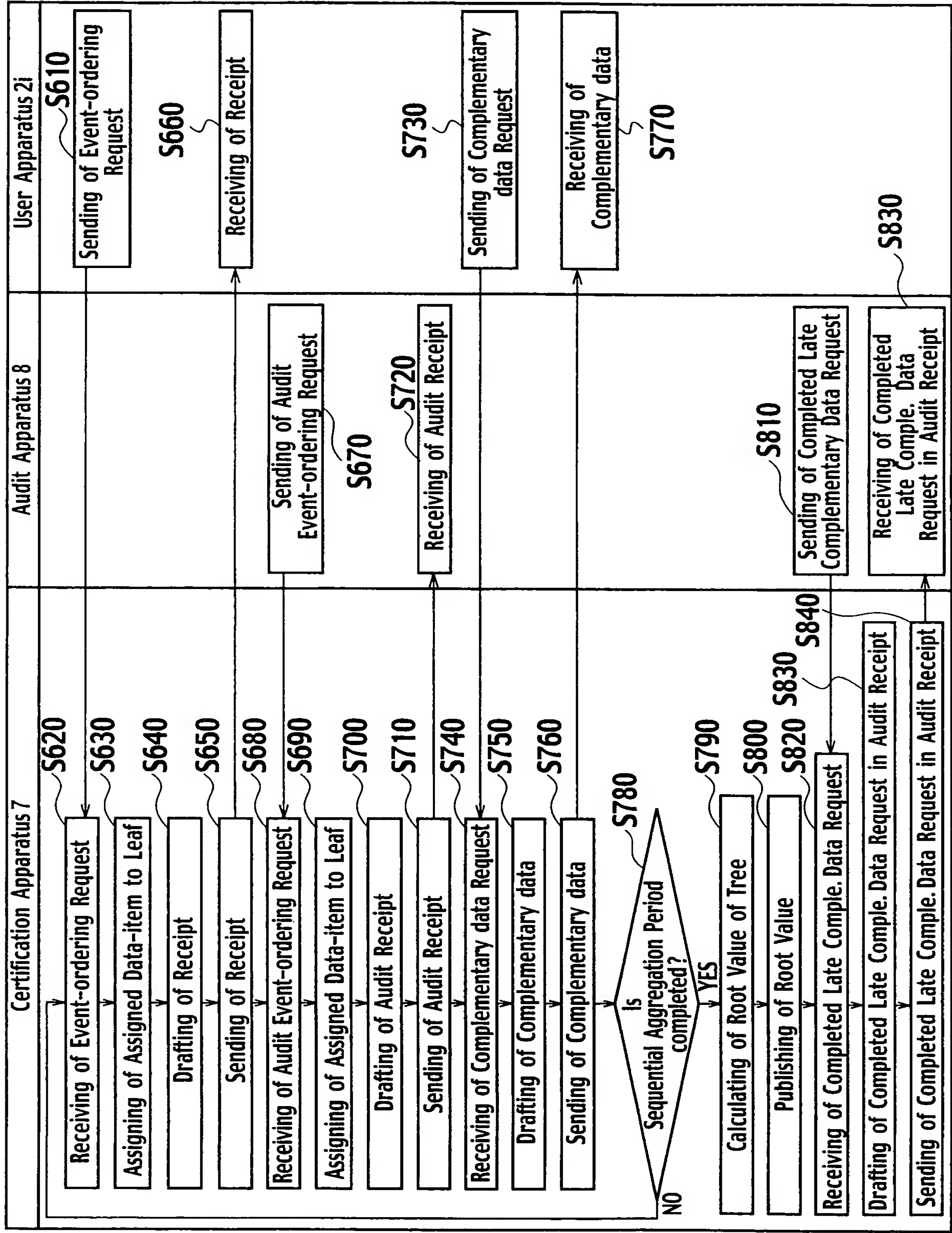


FIG. 16

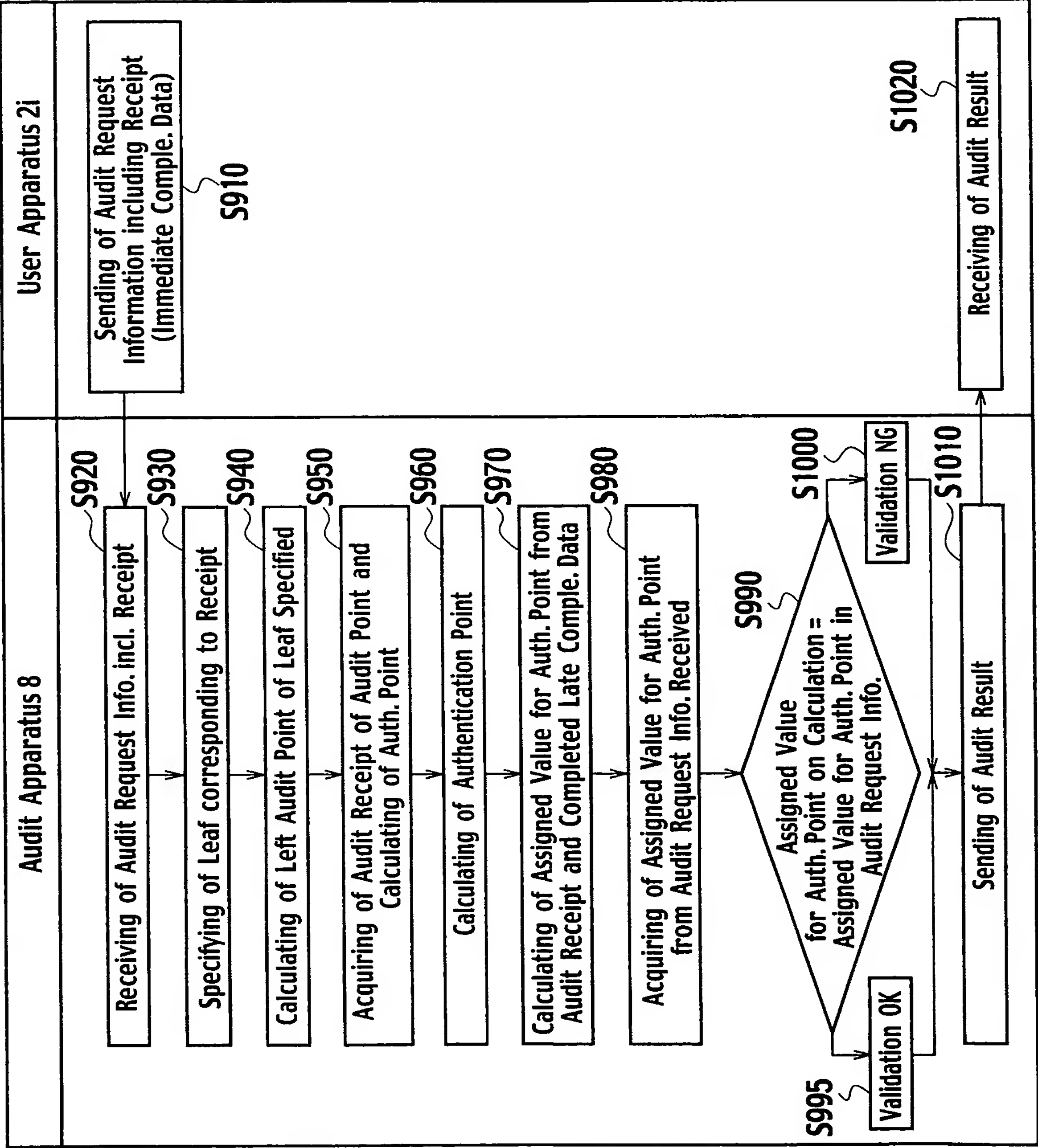
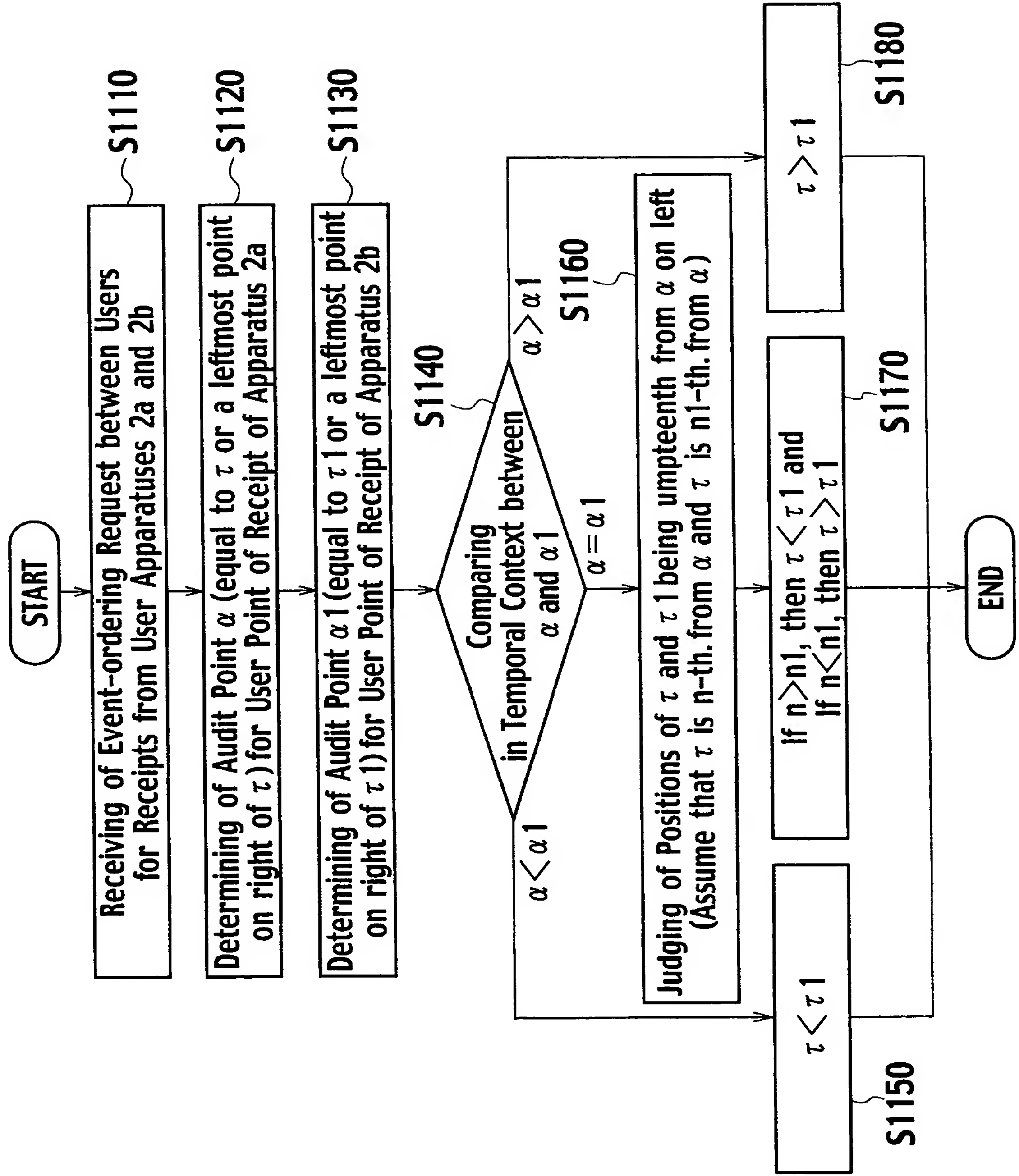
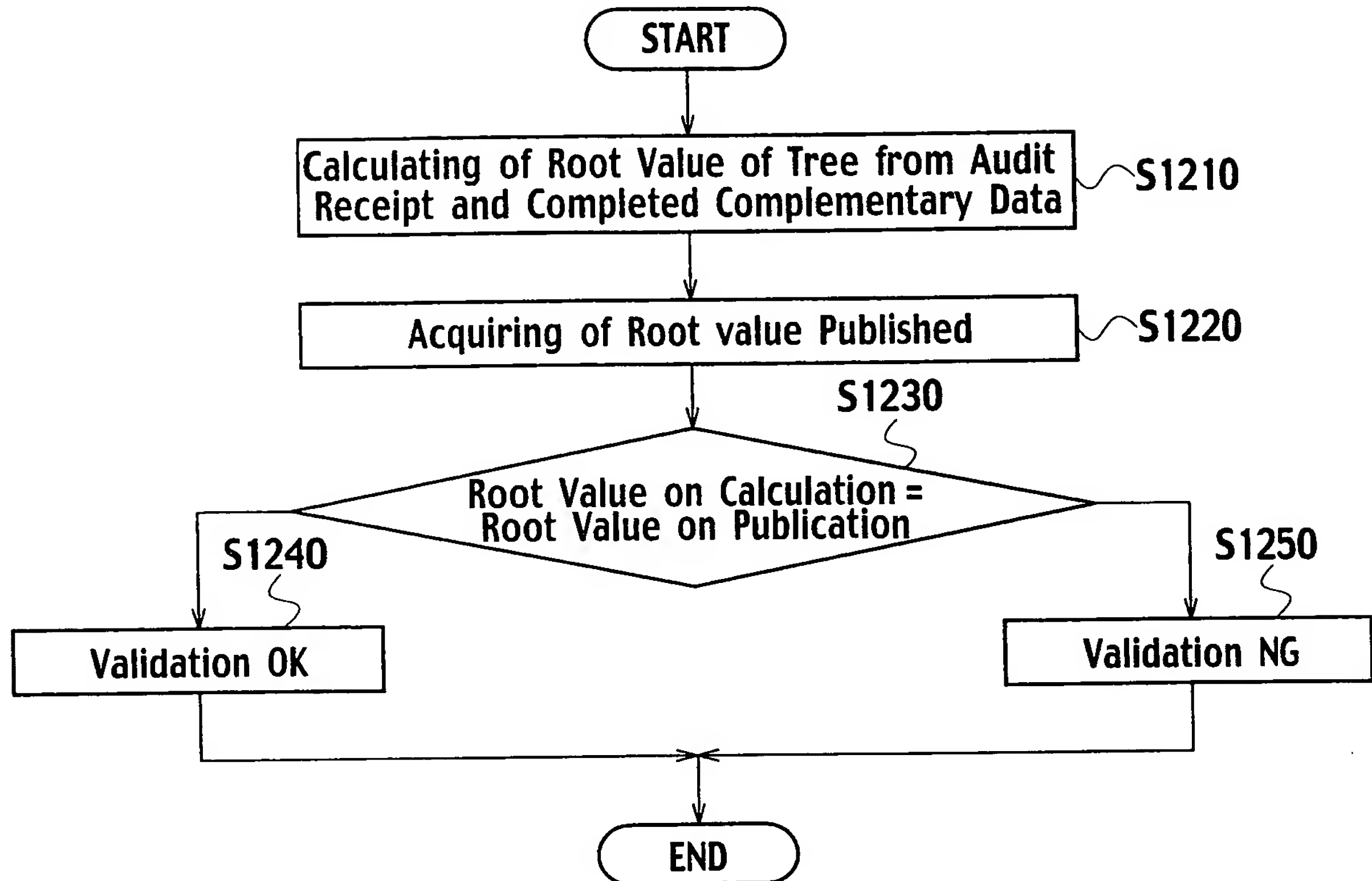


FIG. 17



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FIG. 18



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FIG. 19

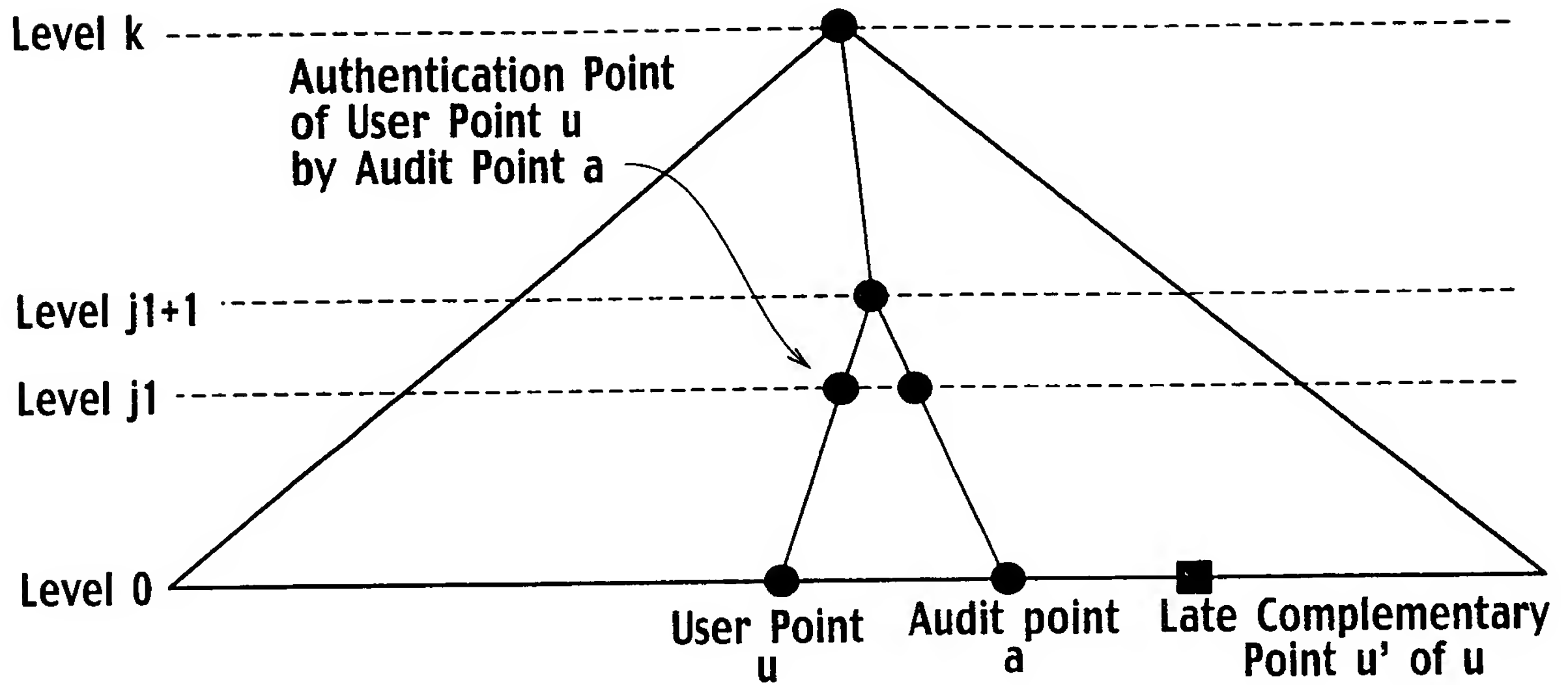


FIG. 20

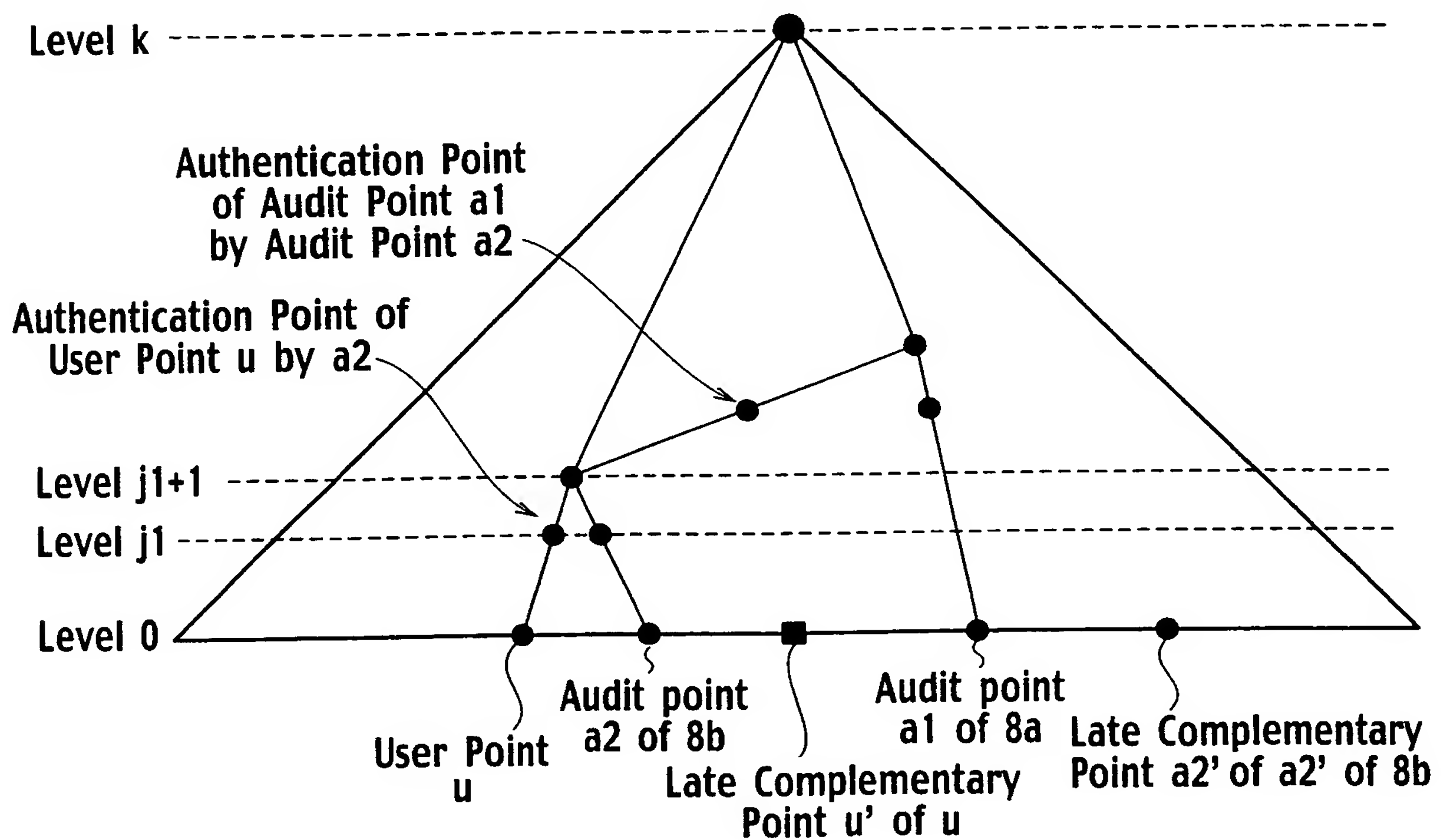


FIG. 21

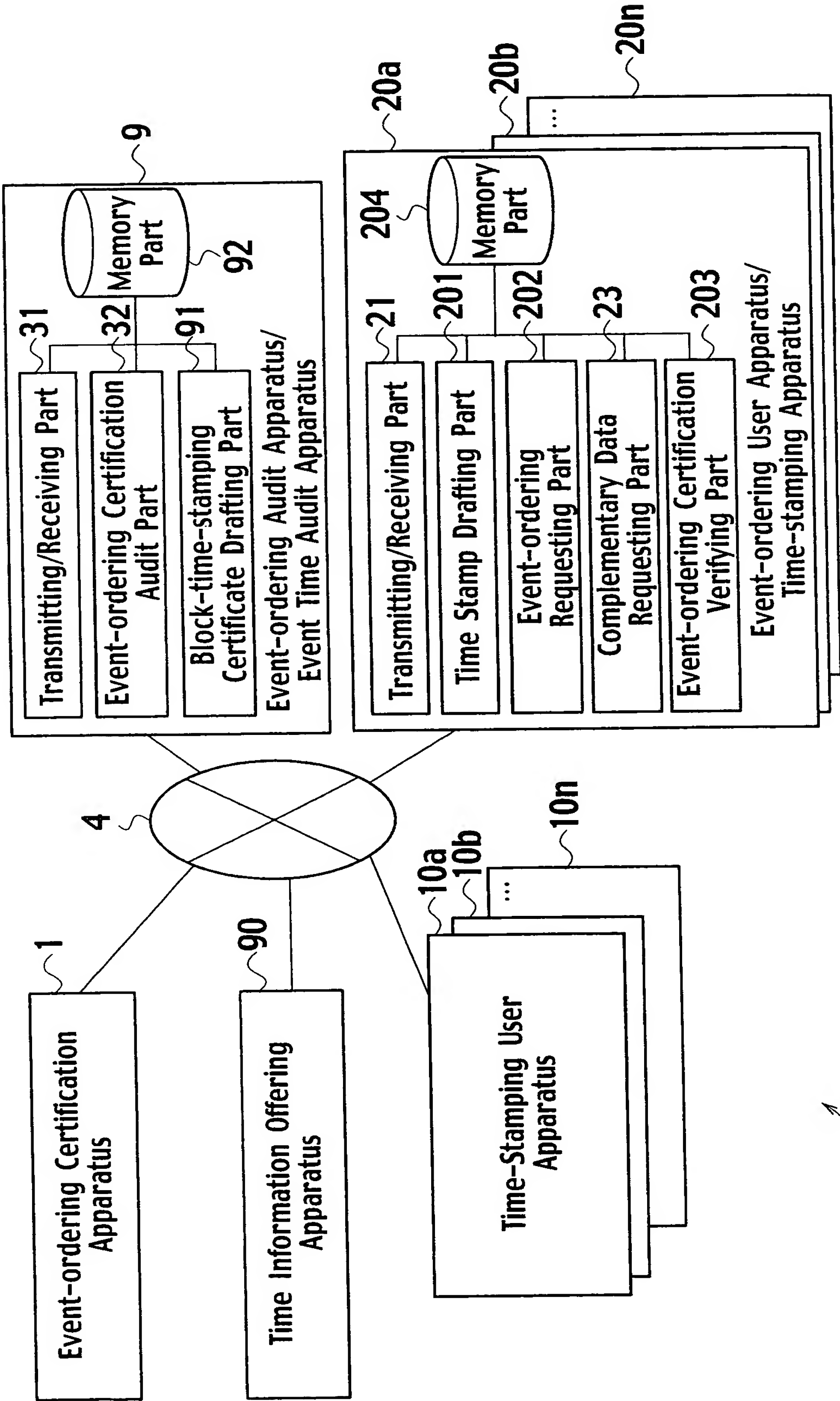


FIG. 22

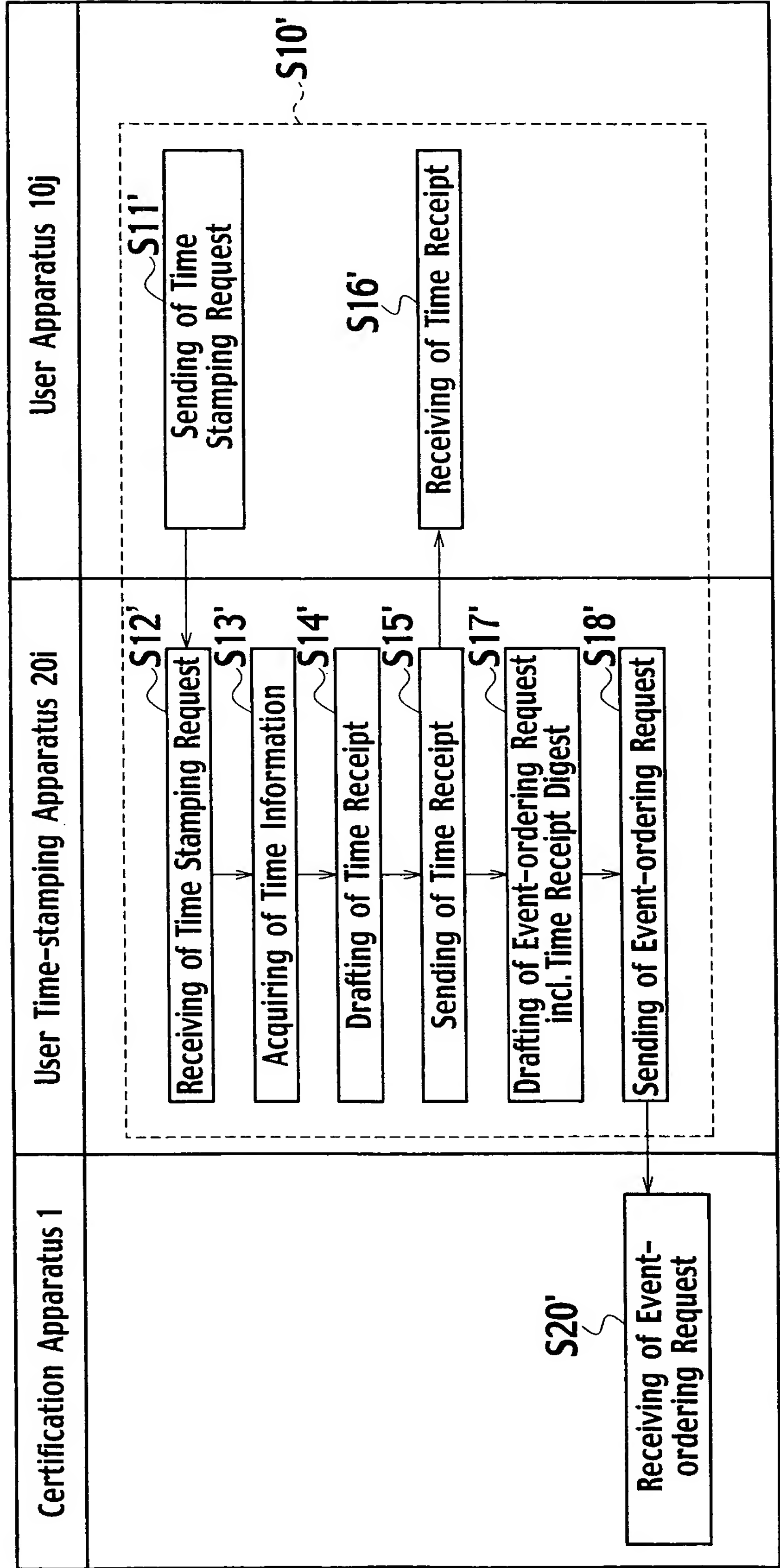


FIG. 23

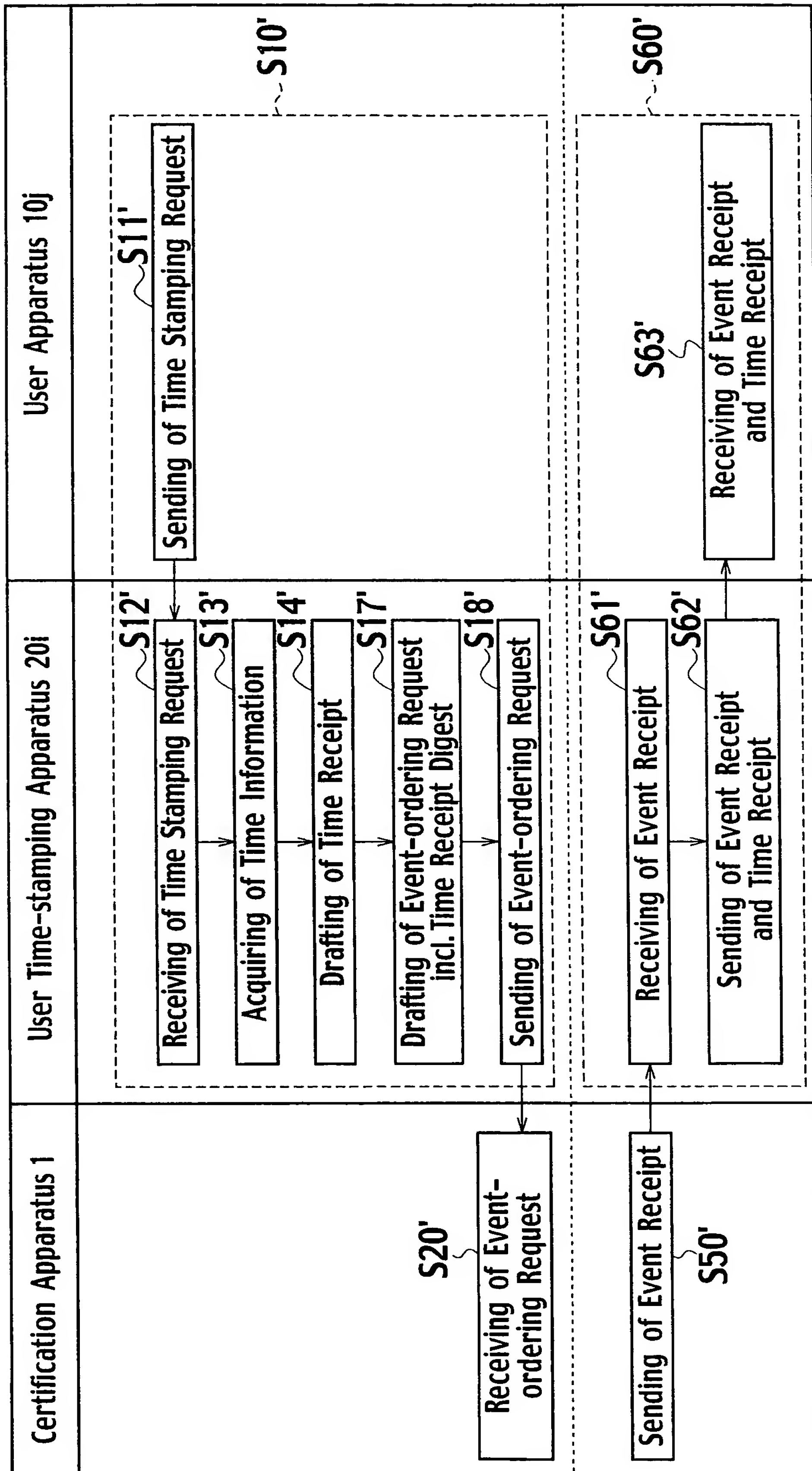


FIG. 24

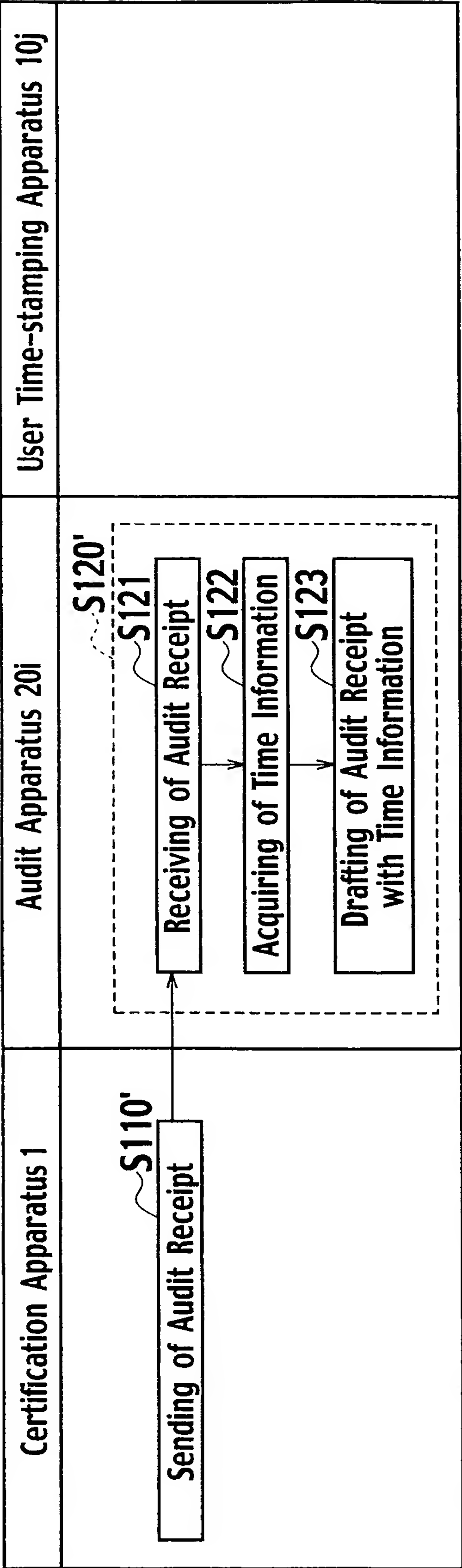


FIG. 25

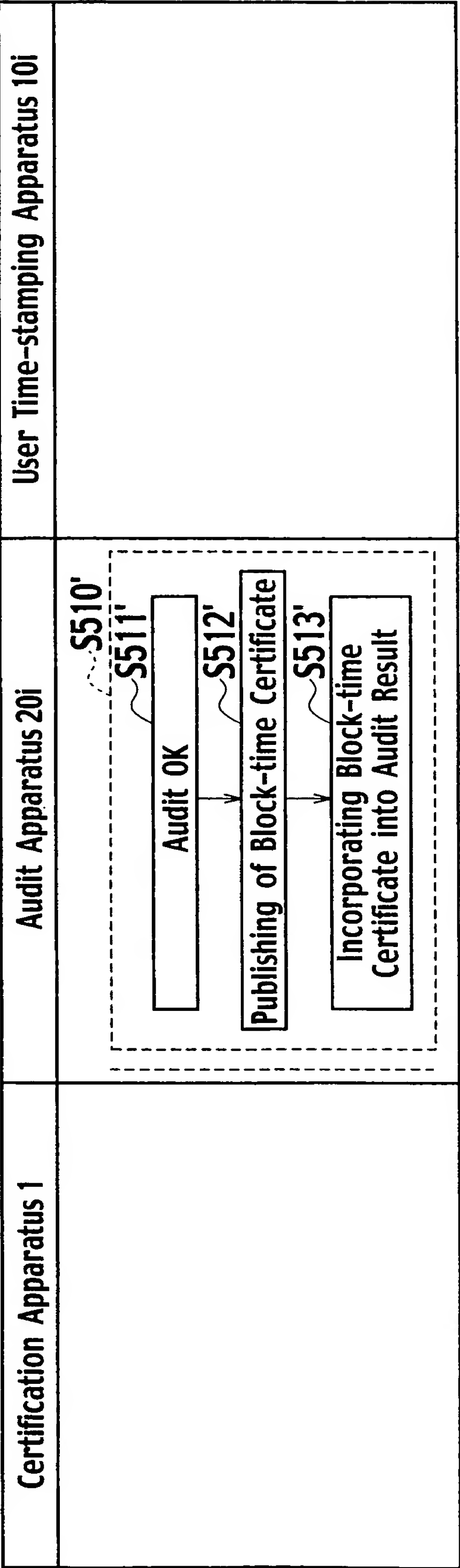


FIG. 26

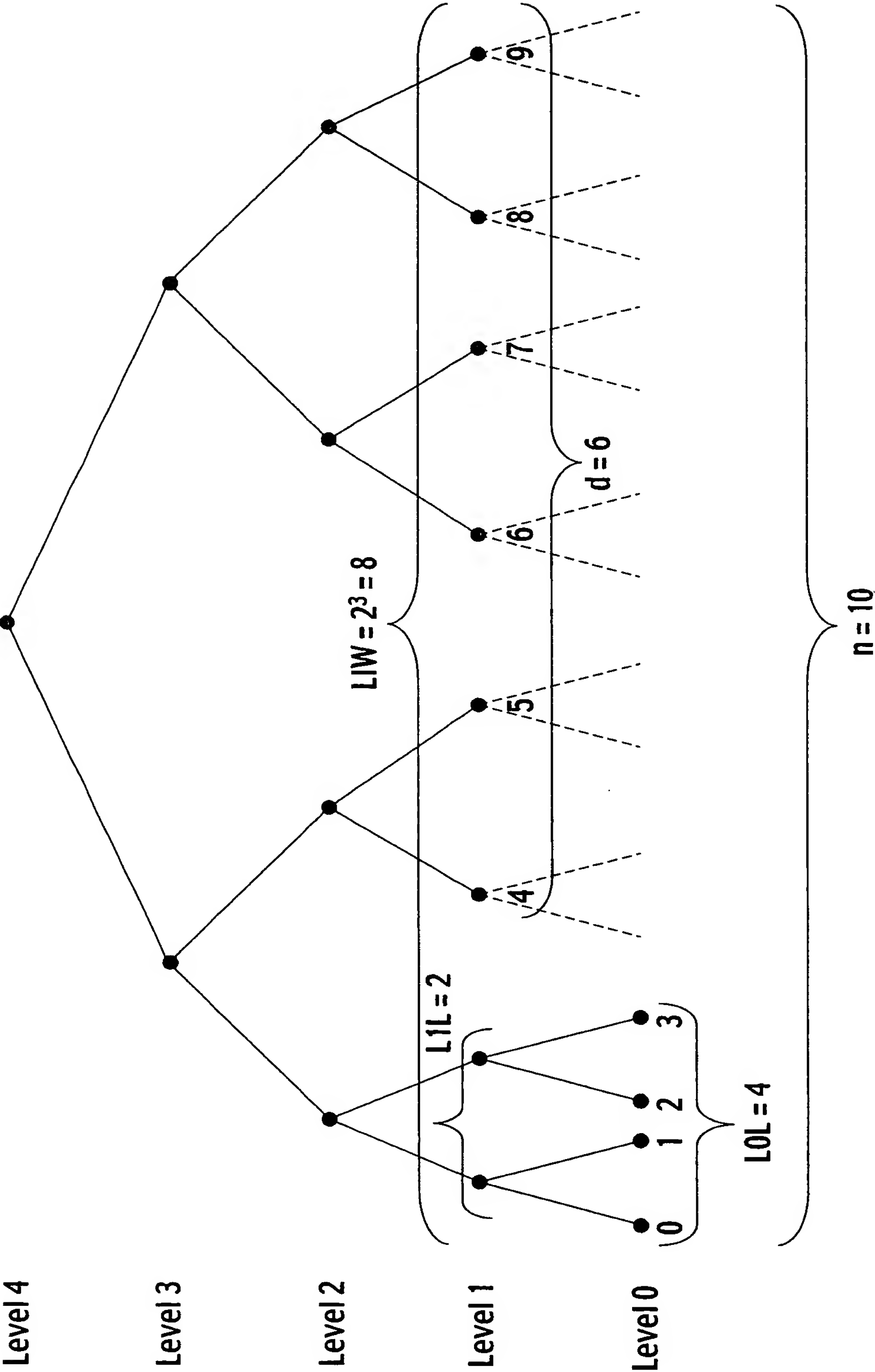


FIG. 27

(1) Loop 1: In a constructive method No. 3, the following processes are repeated until a regular time interval is completed.

- (1.1) Setting a request on acceptance to x
- (1.2) Increasing n by increment of 1
- (1.3) Loop 2: Performing of the follow processes for j=0, ..., k
 - (1.3.1) $i \rightarrow ij$
 - (1.3.2) When $j = 0$, set $A[j] := x$.
(Set x to node(j, ij).)
 - (1.3.3) When $j > 0$, perform as follows.
 - Set $x0 := A_{j-1}[\text{index}(\text{leftChild}(j, i))]$
(Set x0 to an assigned value for left-child of node(j, i).)
 - Set $x1 := A_{j-1}[\text{index}(\text{rightChild}(j, i))]$
(Set x1 to an assigned value for right-child of node(j, i).)
 - Calculate $x2 := h(x0 \parallel x1)$
 - Set $A[j] := x2$
(Assign x2 to node(j, i).)
 - (1.3.4) Increasing ij by increment of 1
 - (1.3.5) Withdraw from loop 2 if i is an even number.

Completion of loop 2
Completion of loop 1

Processing Procedure 1

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FIG. 28

- (2) Performing of the following processes after withdrawing from loop 1 on reaching finish time.
- (2.1) Set $k := \text{ceiling}(\log_2(n))$.
 - (2.2) Calculate $\text{rtPath}(k, 0, n-1)$ and Set $((0, r(0), \dots, k, r(k)))$ to the calculation result.
 - (2.3) Loop 3: Performing of the follow processes for $j=0, \dots, k$
 - (2.3.1) $i \rightarrow i_j$
 - (2.3.2) Case of $j = 0$:
 - (2.3.2.1) When i is an odd number:
 - Produce a dummy $r := R(0, i)$
 - Set $A_j[i] := r$
(Assign r to $\text{node}(0, i)$.)
 - Set $b_j := \text{true}$.
 - Increase i_j by increment of 1.
 - (2.3.2.2) Case of $0 < j \leq k$:
 - (2.3.2.1) When $i = r(j)$:
 - (when $\text{node}(j, i)$ is on $\text{rtPath}(k, 0, n-1)$):
 - (2.3.2.1.1) $x_0 := A_{j-1}[\text{index}(\text{leftChild}(j, i))]$
(Set x_0 to an assigned value for left-child of $\text{node}(j, i)$.)
 - (2.3.2.1.2) $x_1 := A_{j-1}[\text{index}(\text{rightChild}(j, i))]$
(Set x_1 to an assigned value for right-child of $\text{node}(j, i)$.)
 - (2.3.2.1.3) Calculate $x_2 := h(x_0 \parallel x_1)$
 - (2.3.2.1.4) Set $A_j[i] := x_2$
(Assign x_2 to $\text{node}(j, i)$.)
 - (2.3.2.1.5) When i is an even number and $j < k$:
 - Increase i by increment of 1.
 - Calculate $r := R(j, i)$ and Set $A_j[i] := r$
(Assign r to $\text{node}(j, i)$.)
 - Set $b_j := \text{true}$.
 - Set $i_j := i + 1$
 - (2.3.2.2) When $i = r(j) + 1$, an odd number and $j < k$:
 - Calculate $r := R(j, i)$ and Set $A_j[i] := r$
(Assign r to $\text{node}(j, i)$.)
 - Set $b_j := \text{true}$.
 - Increase i_j by increment of 1.
- Completion of loop 3

Processing Procedure 2

FIG. 29

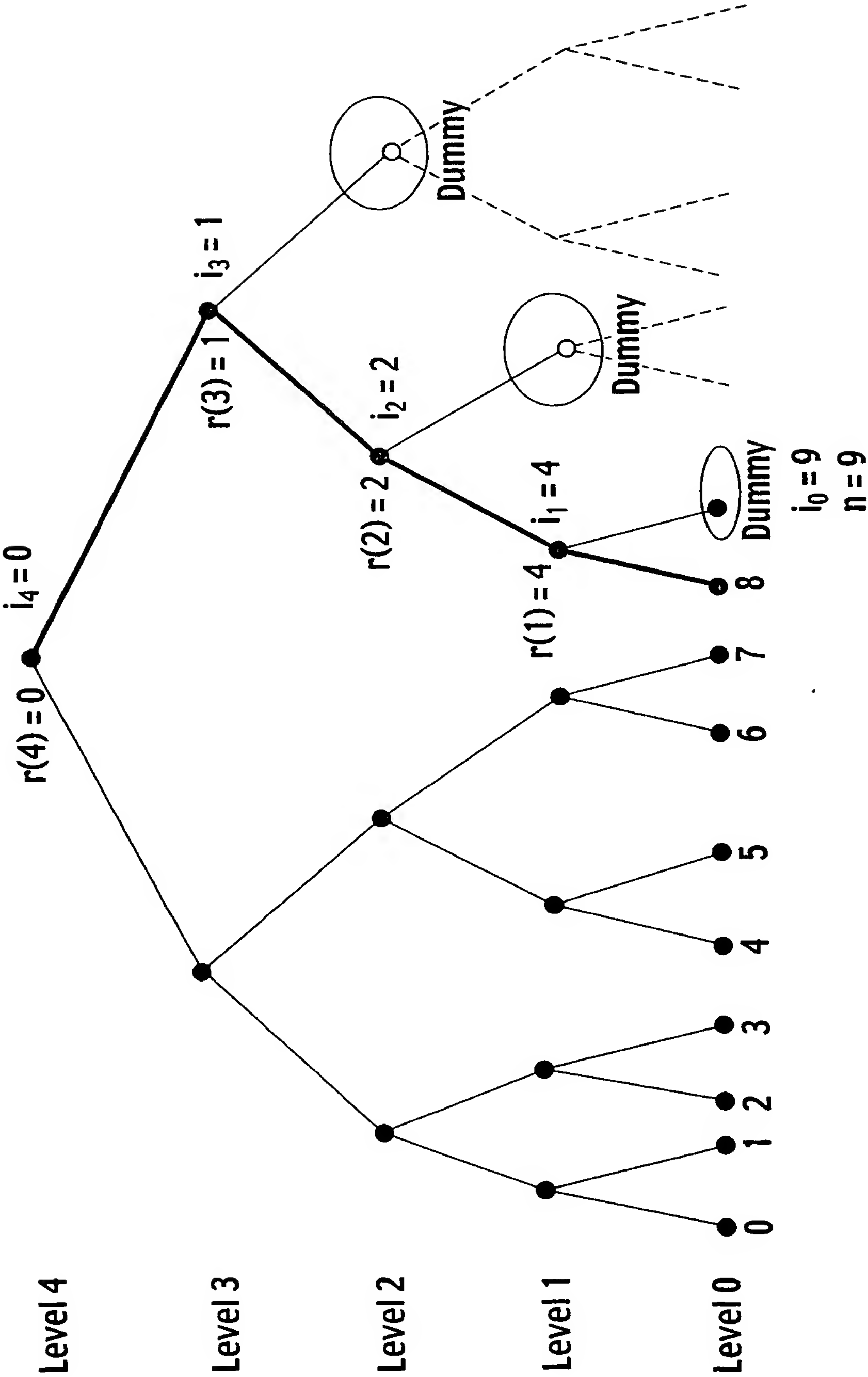
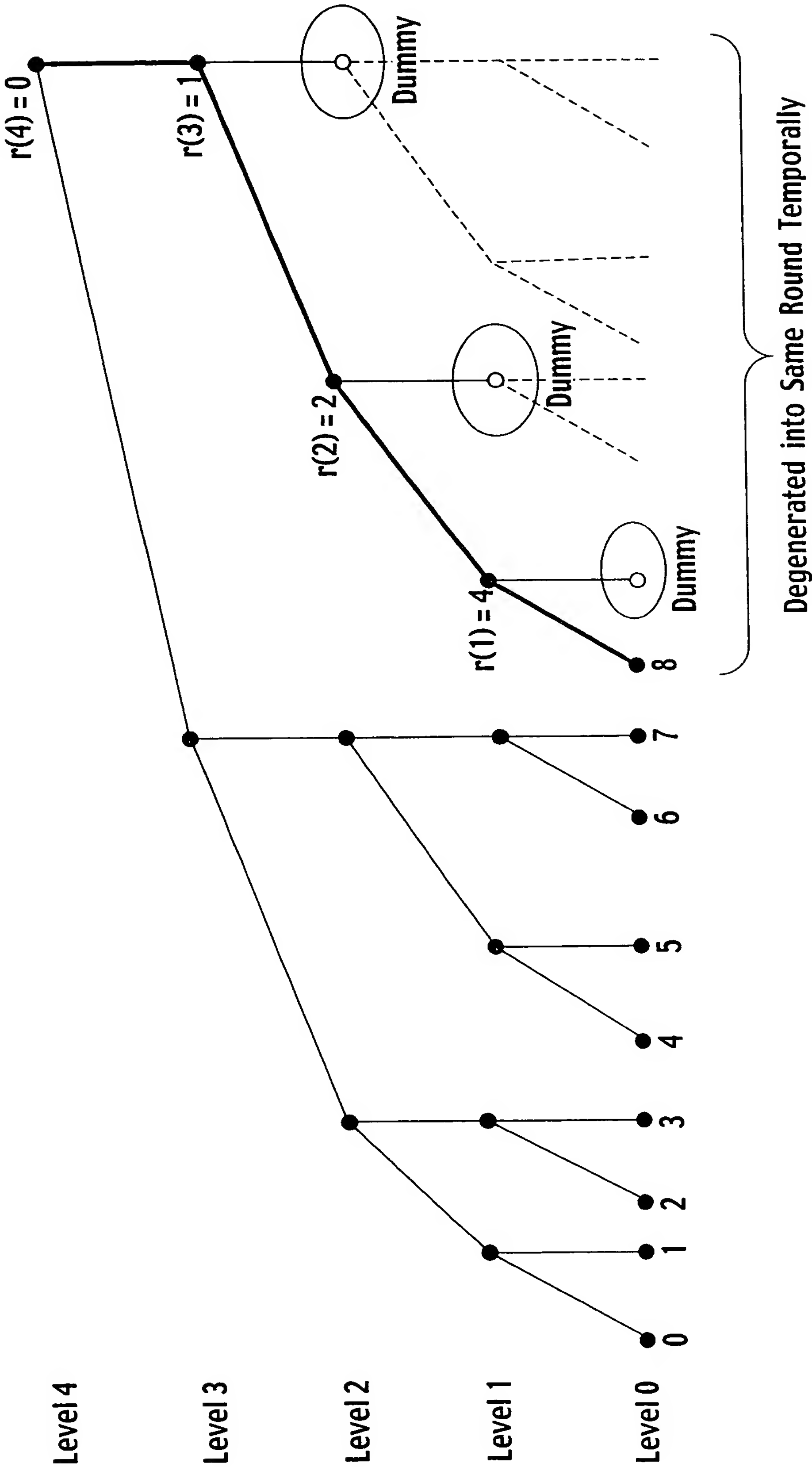


FIG. 30



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FIG. 31

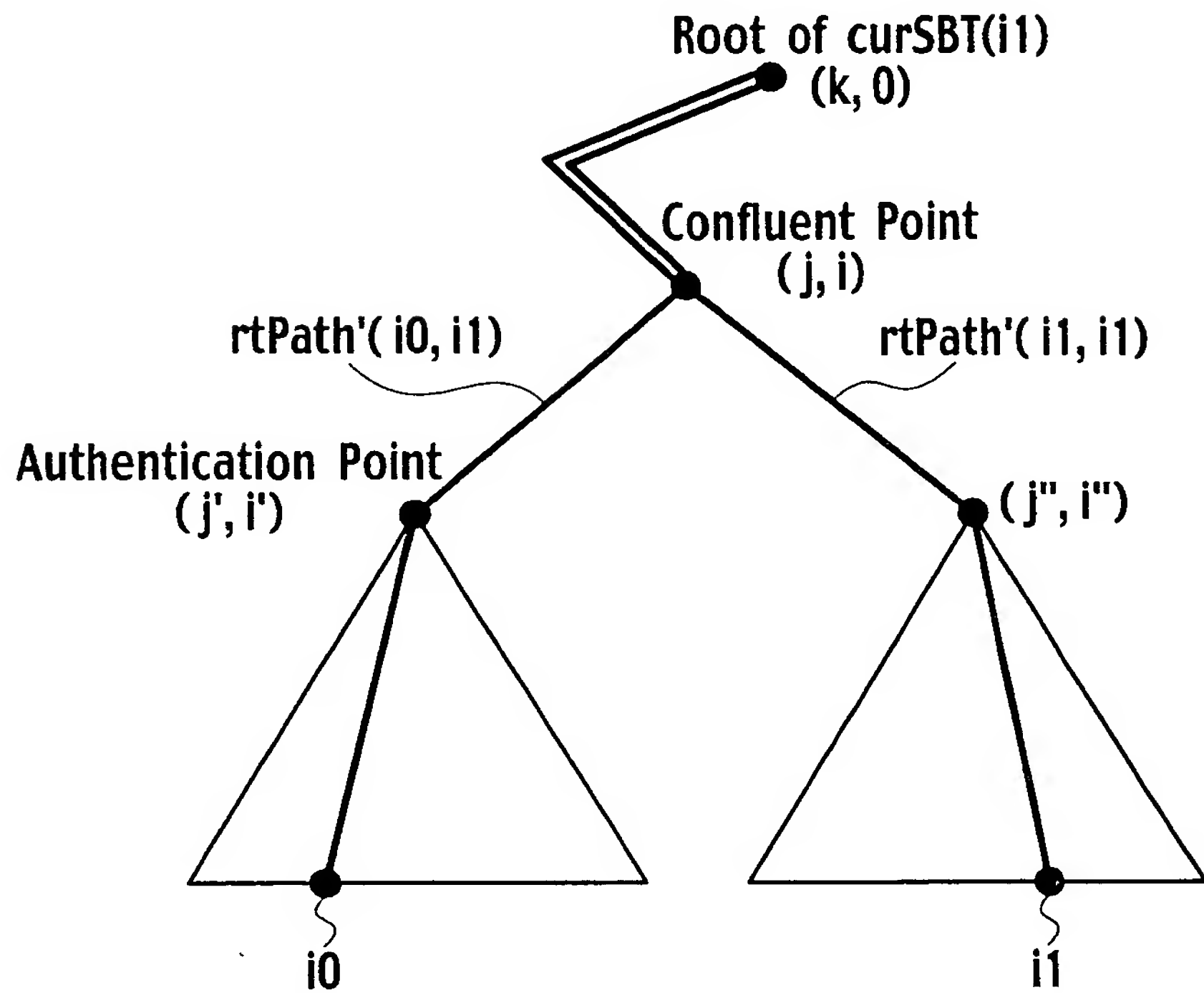
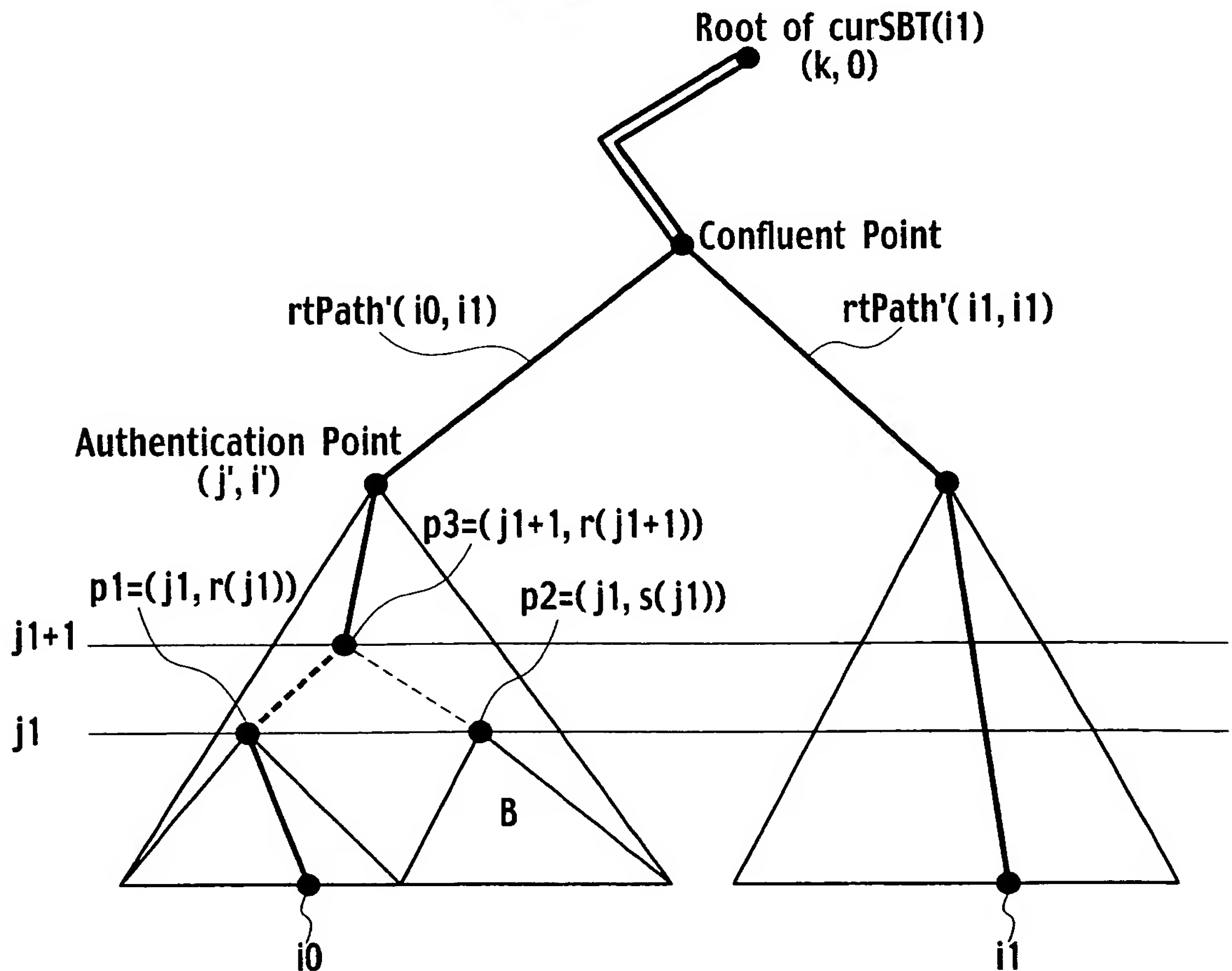


FIG. 32



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FIG. 33

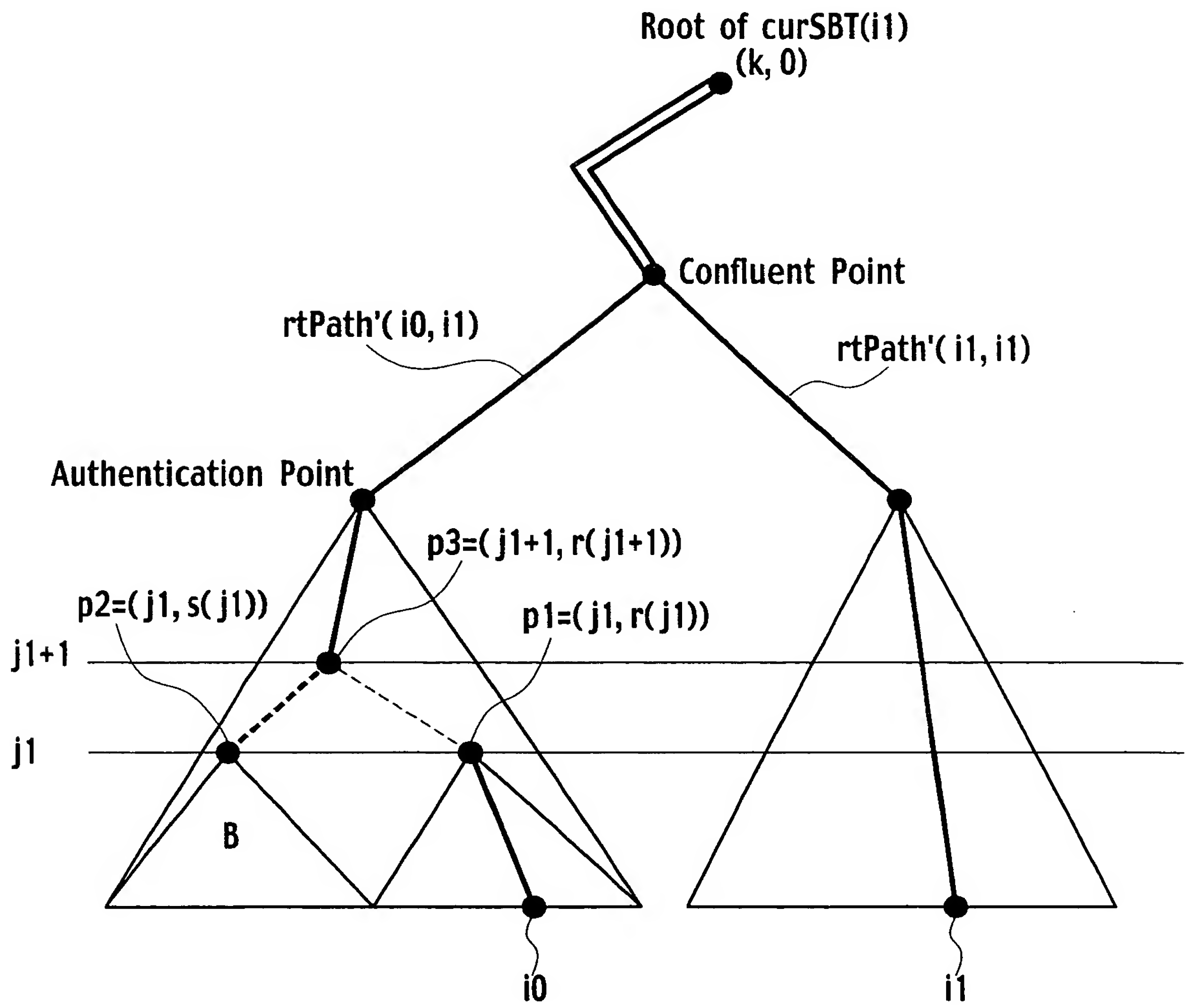
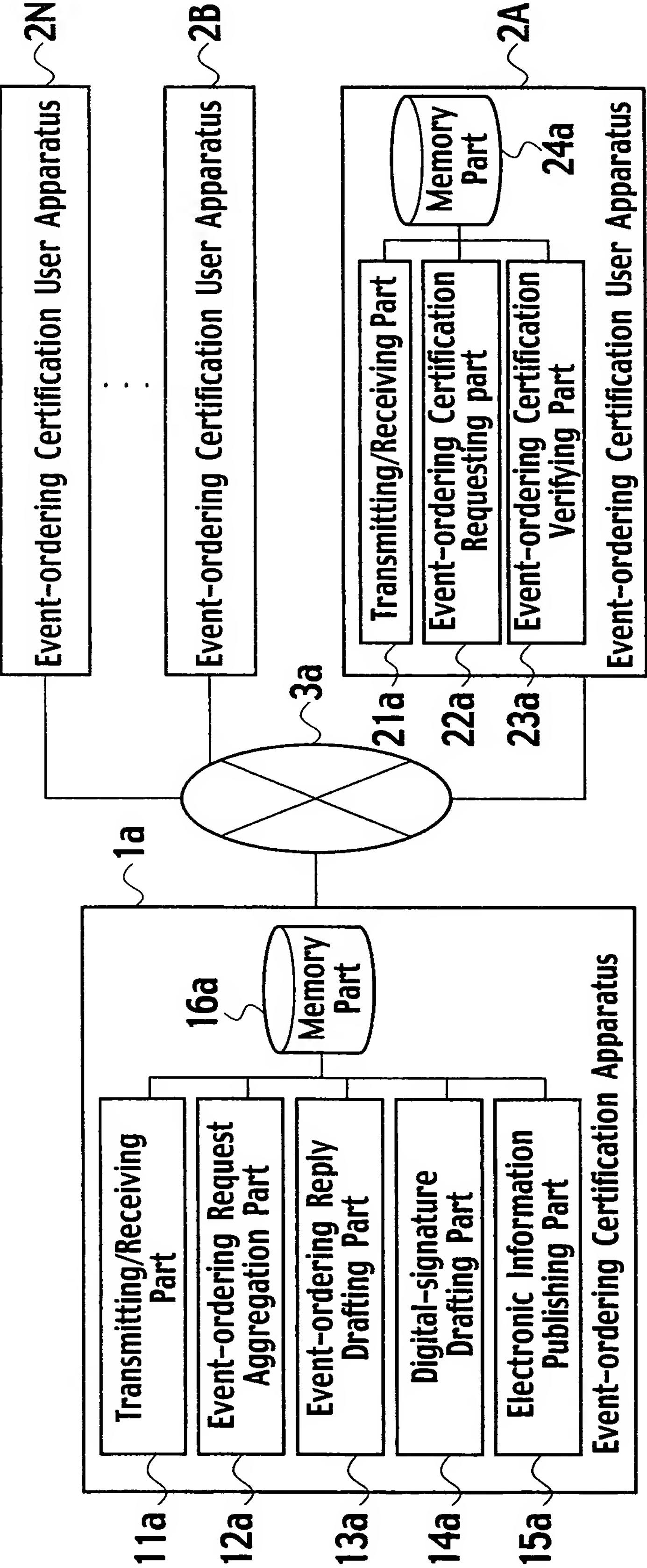


FIG. 34



100a

FIG. 35

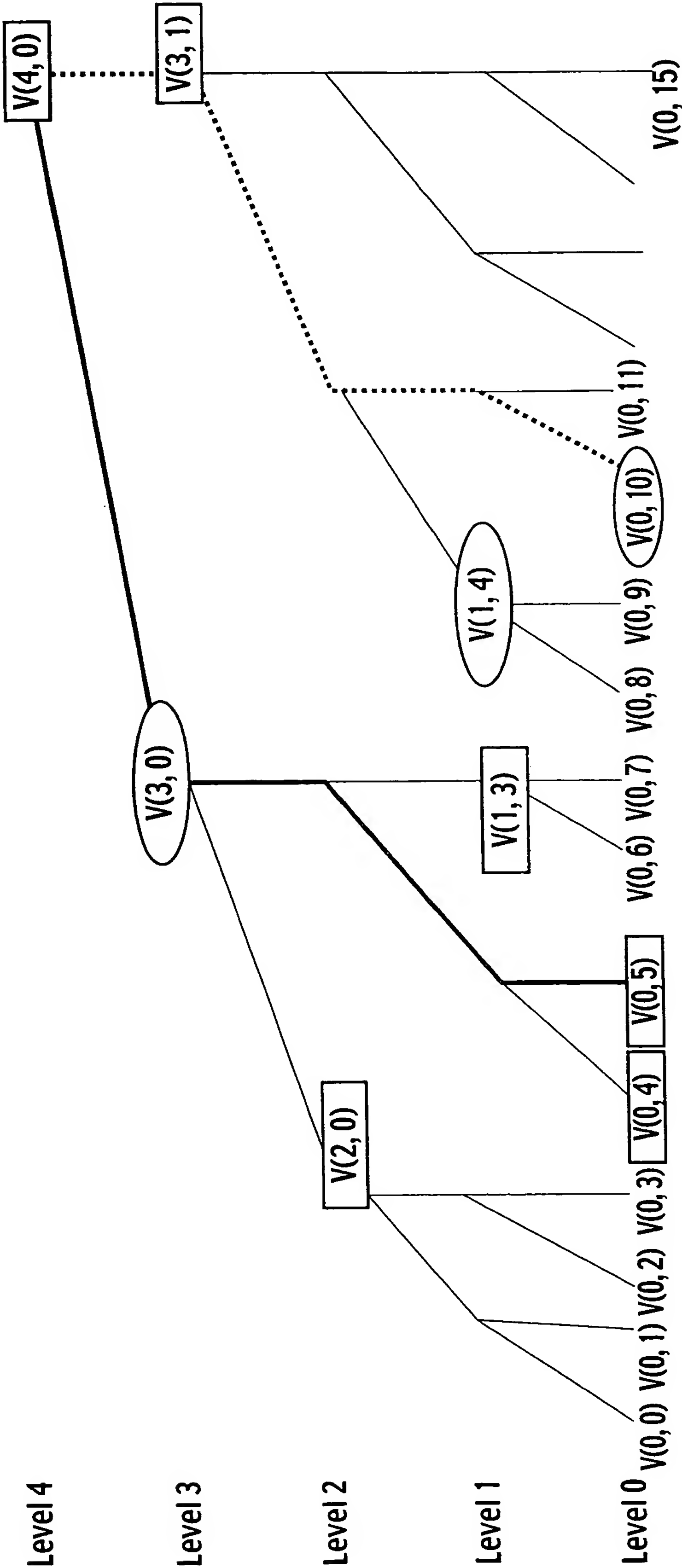
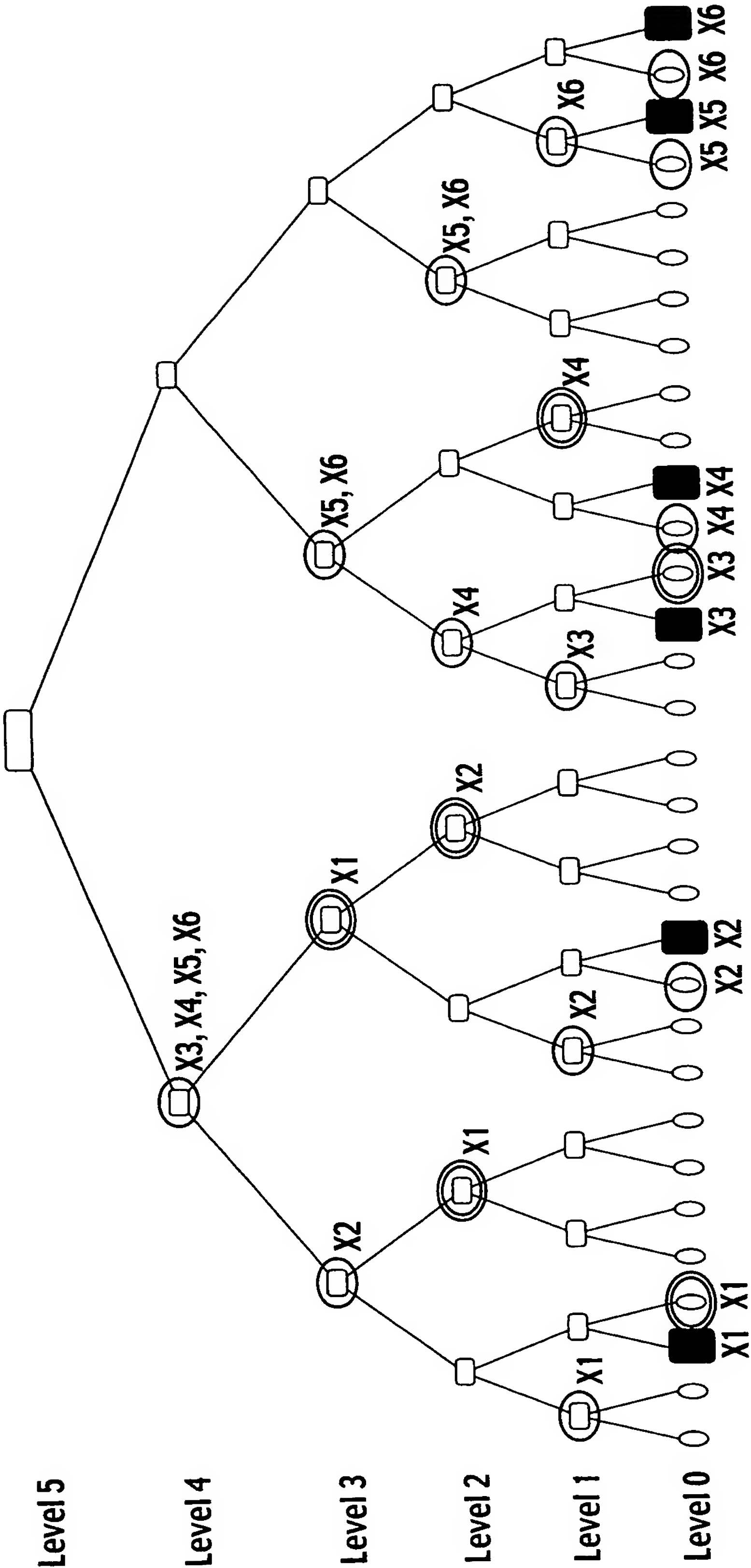


FIG. 36

ITEM	SIGN	REQUIRED
Original Data	Y	<input type="radio"/>
Sequentially Assigned Data-item	Z	<input type="radio"/>
Sequential Aggregation Tree No.	n	<input type="radio"/>
Sequential Aggregation Tree Leaf No.	i	<input type="radio"/>
Immediate Complementary Data of Registration Point (Positional Information Assigned Value)	SK	<input type="radio"/>
Late Complementary Data of Each Past Registration Point (Positional Information Assigned Value)	TK	<input type="radio"/>

Event-ordering Receipt
EOC(y)

FIG. 37






-  X : Requested Registration Point X (X=X1, X2, X3, X4, X5, X6)
-  X : Requested Registration Point X: Immediate Complementary Data
-  X : Requested Registration Point X: Late Complementary Data

FIG. 38

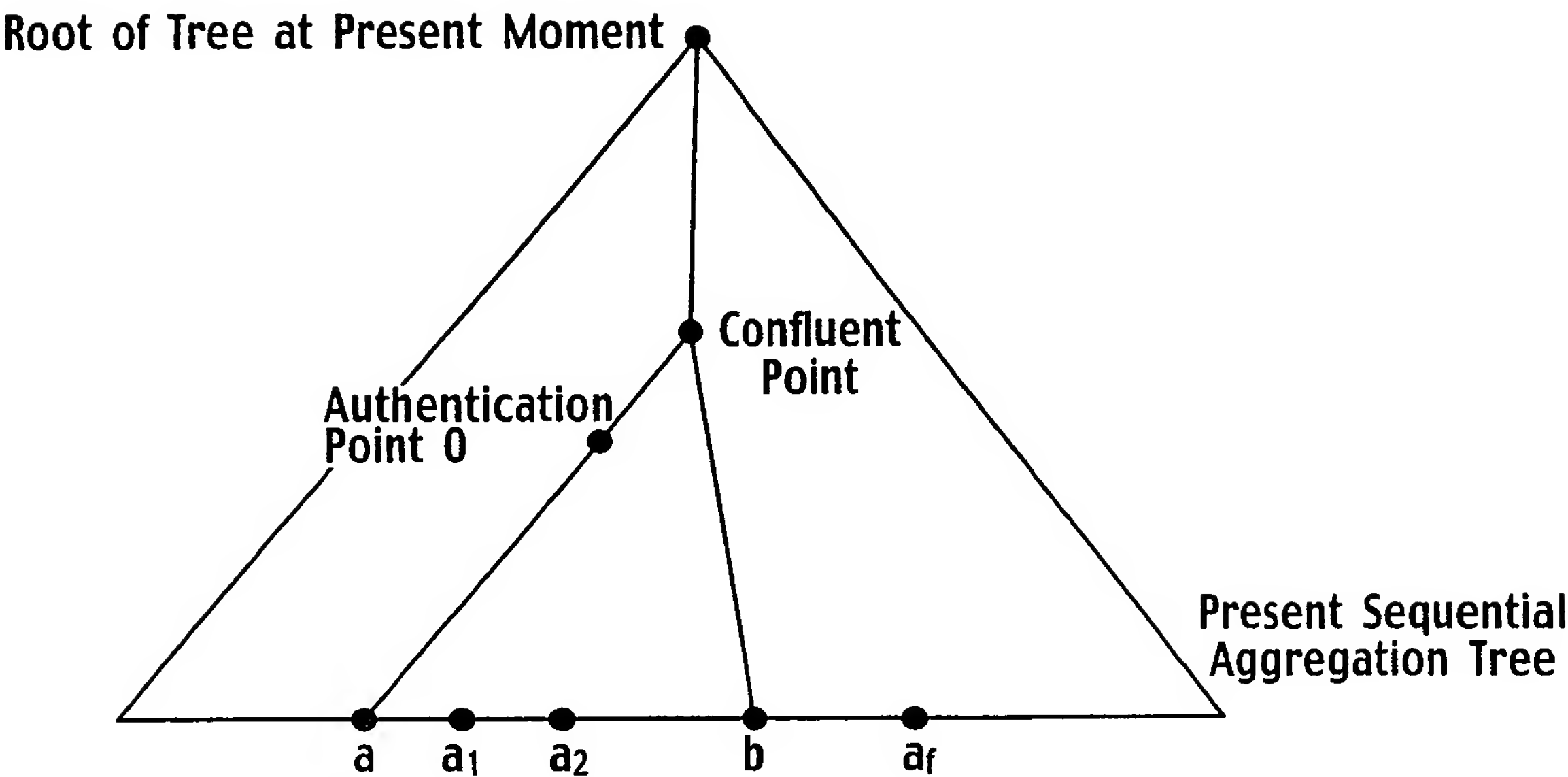


FIG. 39

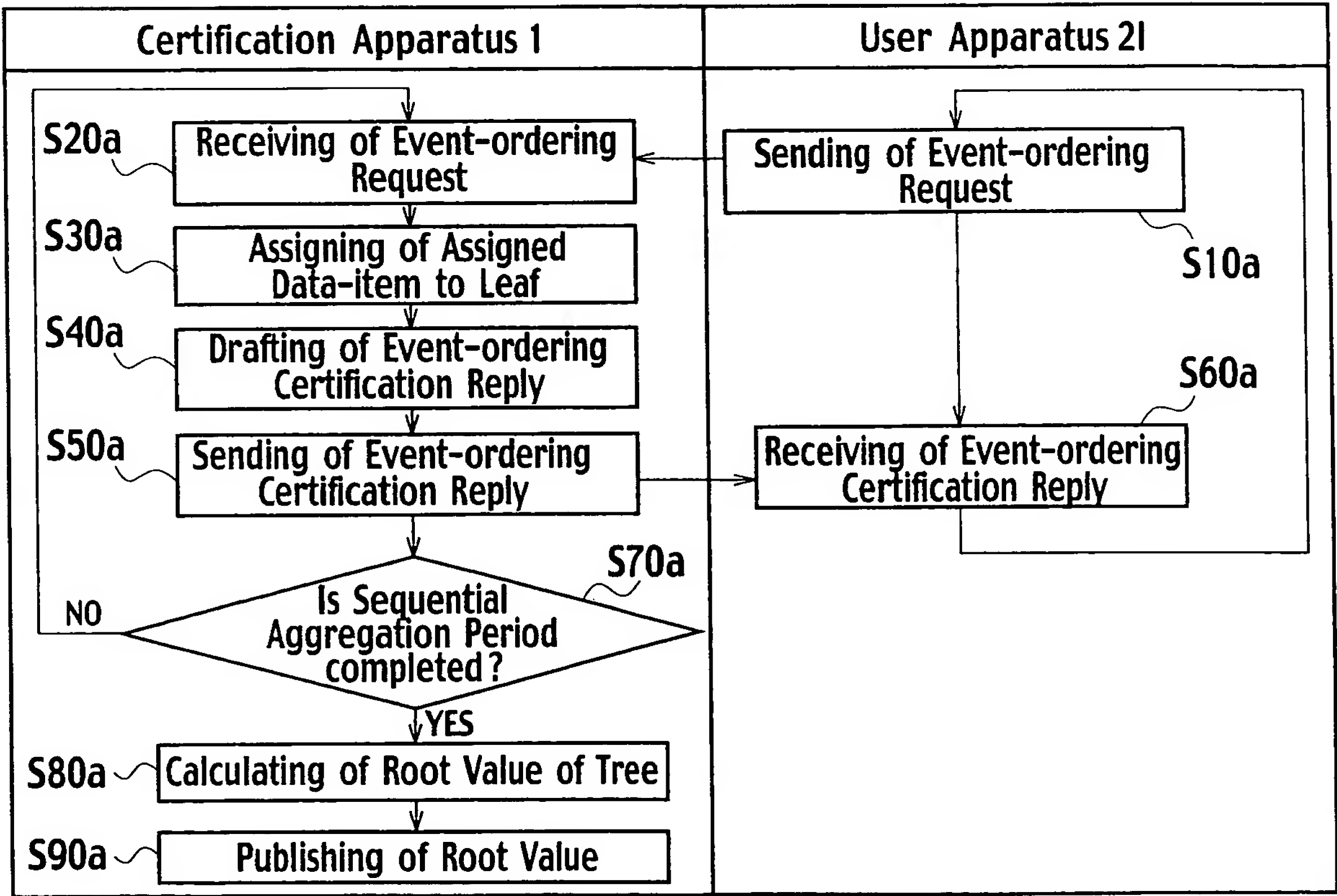


FIG. 40

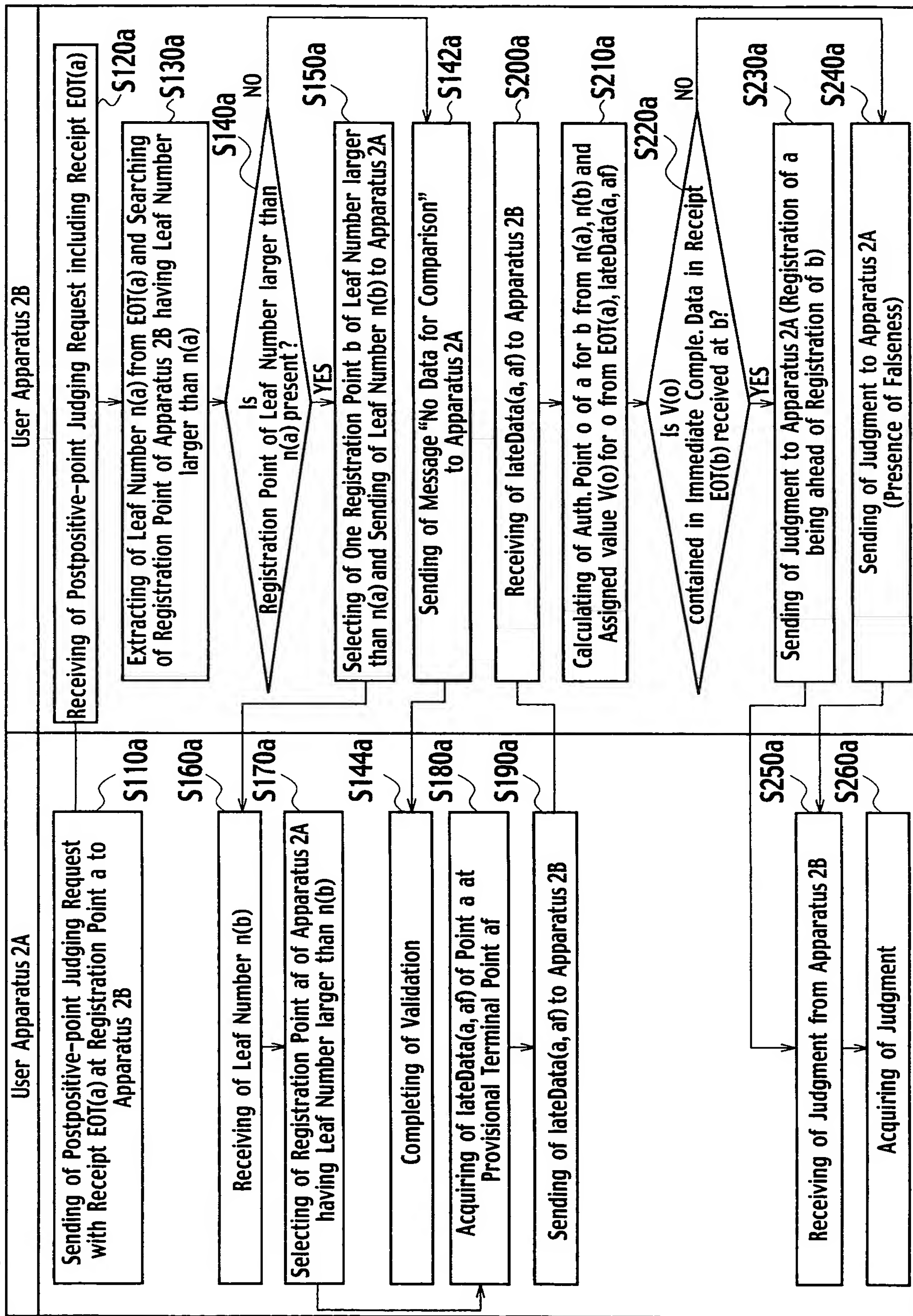


FIG. 41

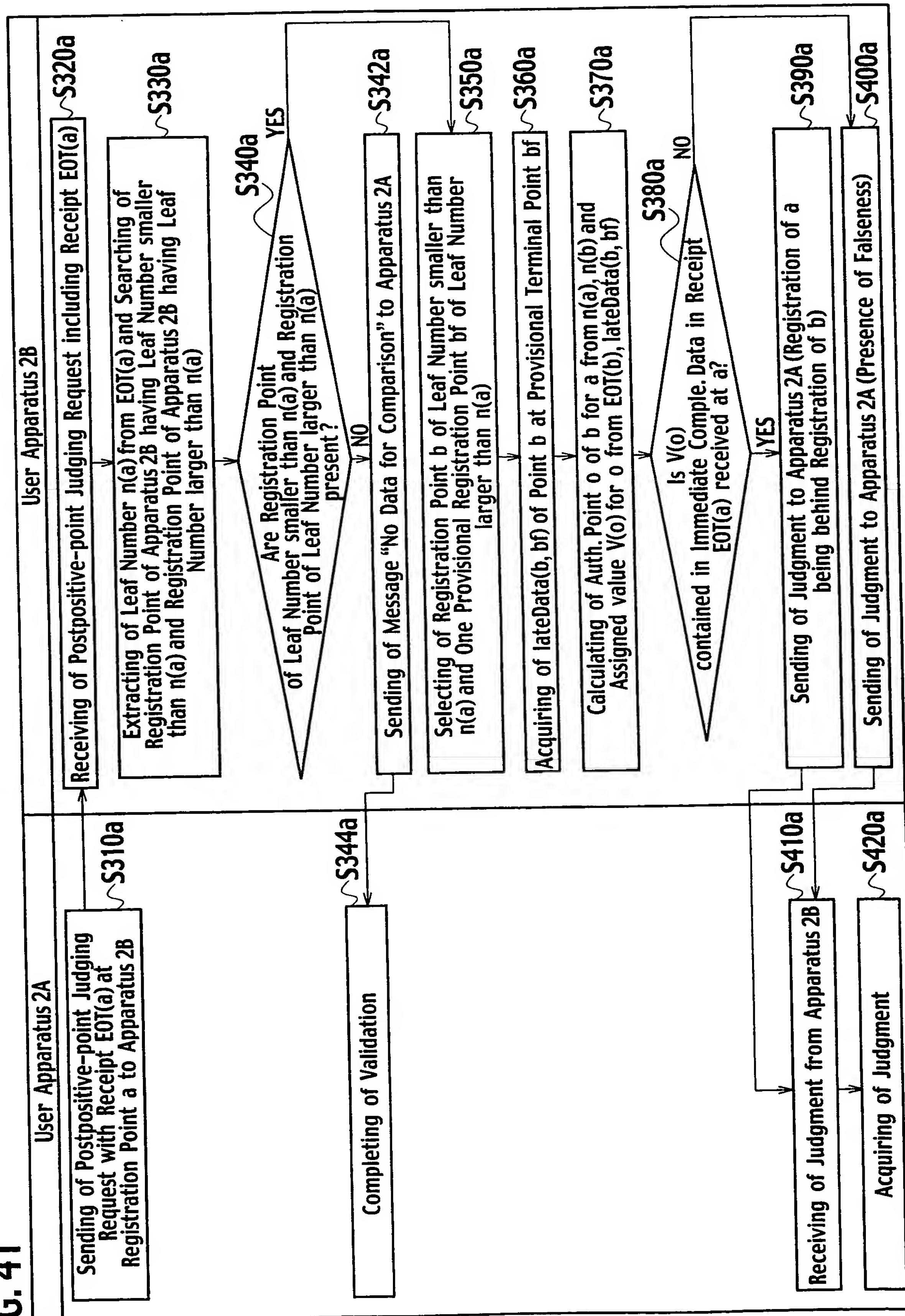
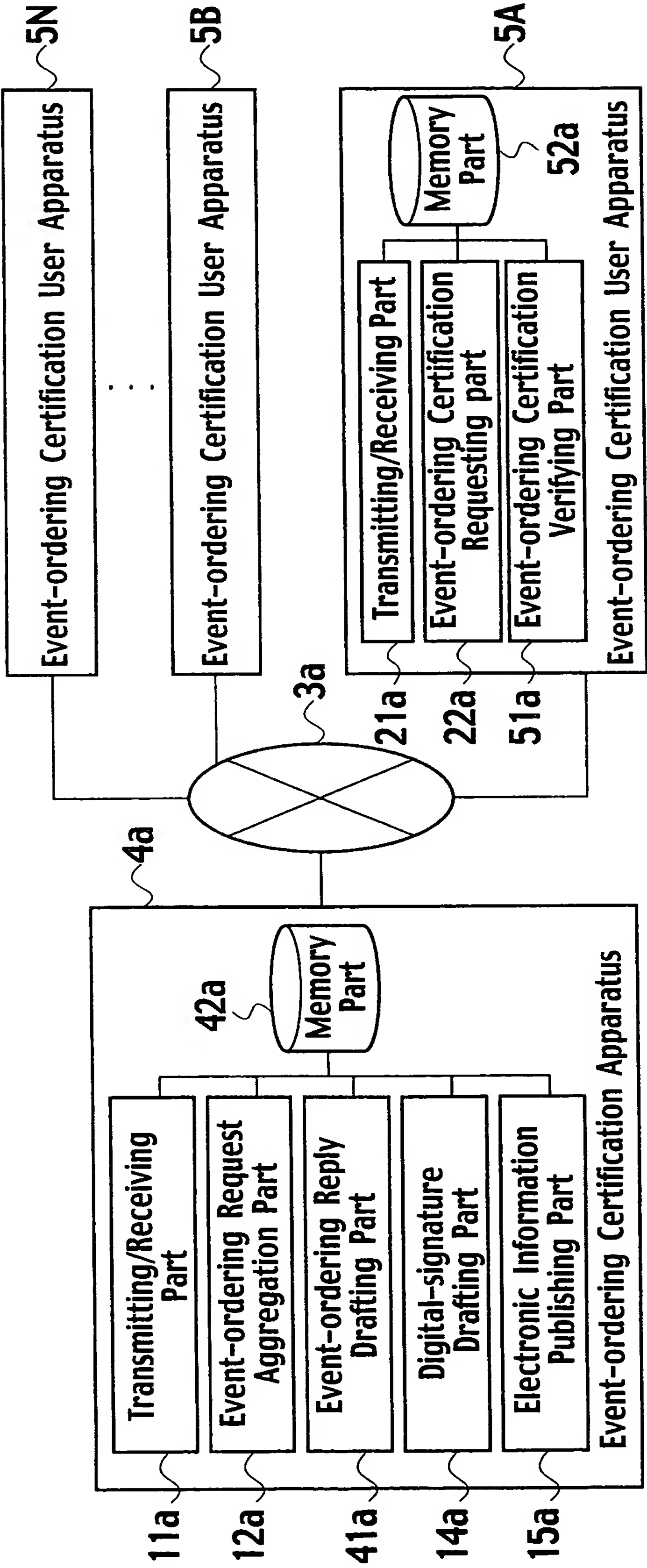


FIG. 42



200a

FIG. 43

ITEM	SIGN	REQUIRED
Original Data	y	<input type="radio"/>
Sequentially Assigned Data-item	z	<input type="radio"/>
Sequential Aggregation Tree No.	n	<input type="radio"/>
Sequential Aggregation Tree Leaf No.	i	<input type="radio"/>
Immediate Complementary Data of Registration Point (Positional Information Assigned Value)	SK	<input type="radio"/>
Late Complementary Data of Immediately-preceding Registration Point (Positional Information Assigned Value)	TK2	<input type="radio"/>

Event-ordering Receipt EOC(y)

FIG. 44

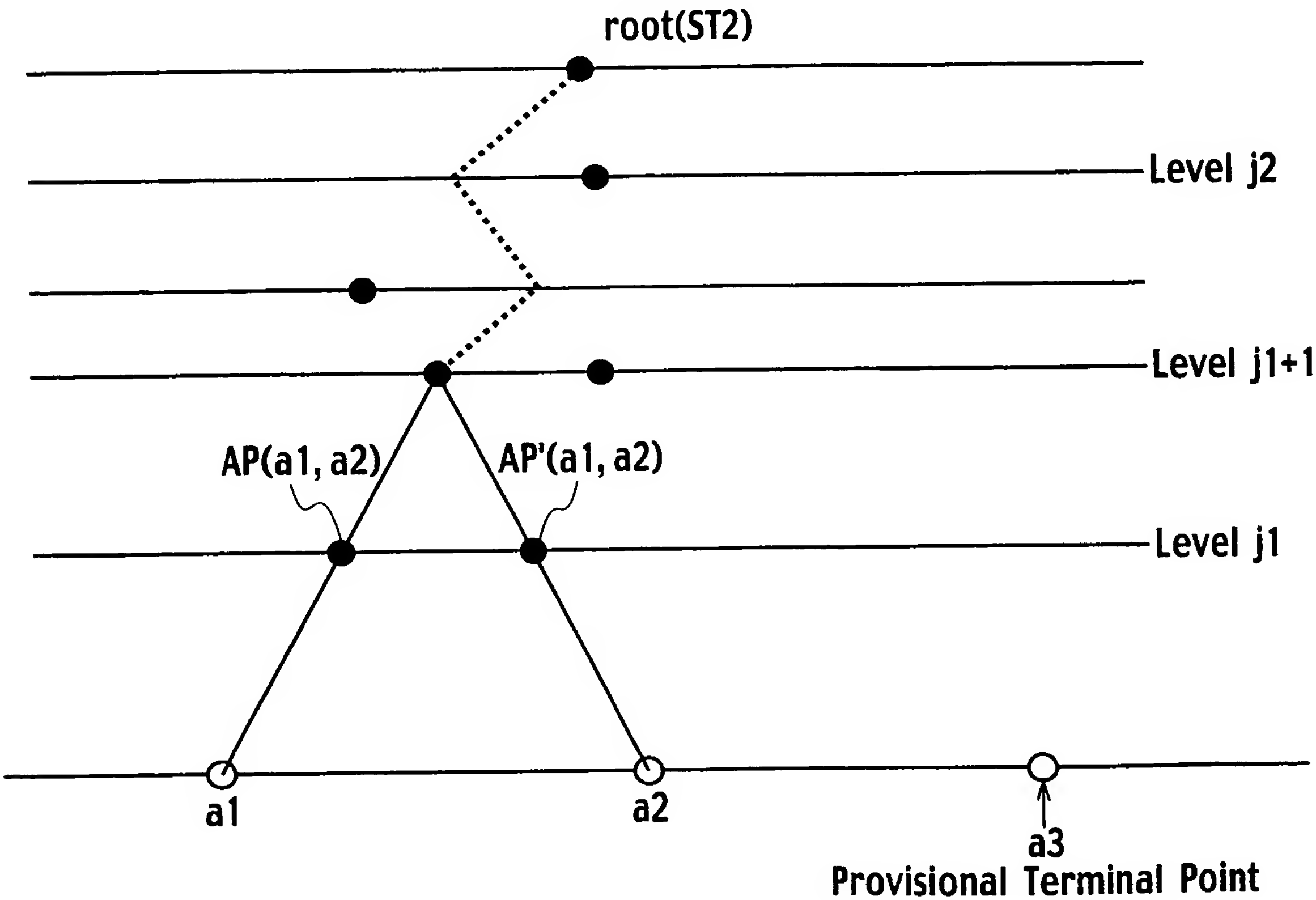


FIG. 45

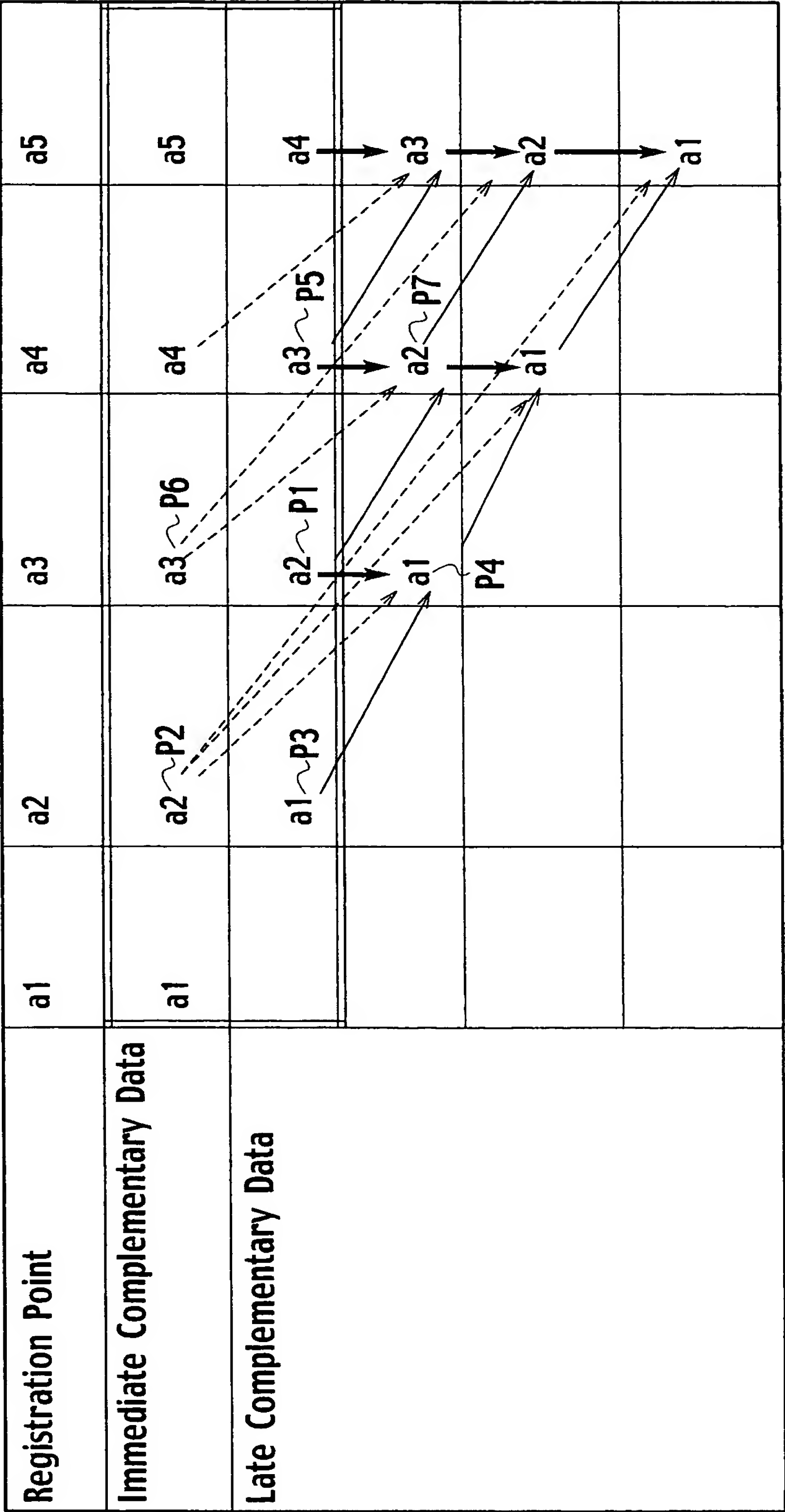
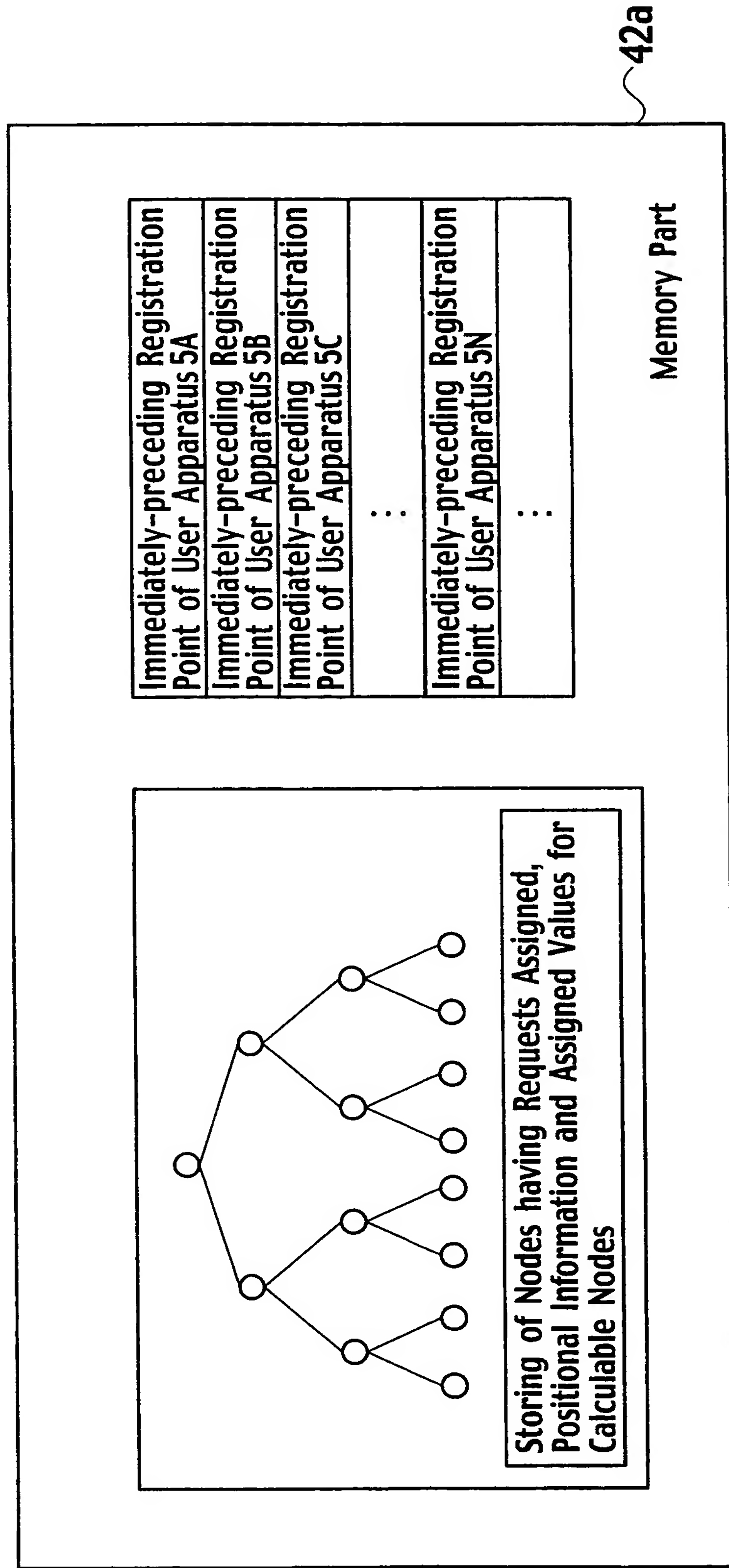


FIG. 46



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FIG. 47

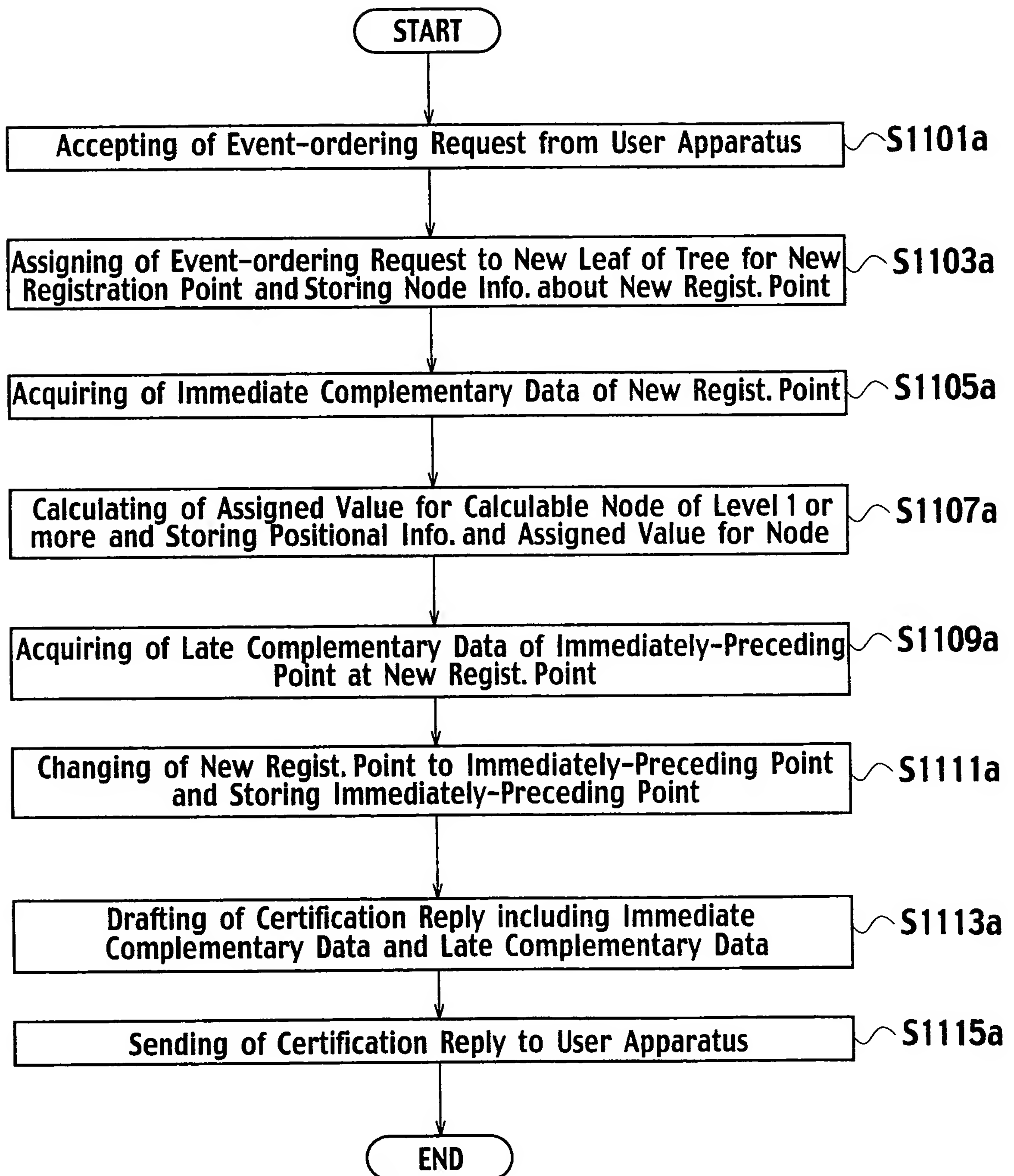
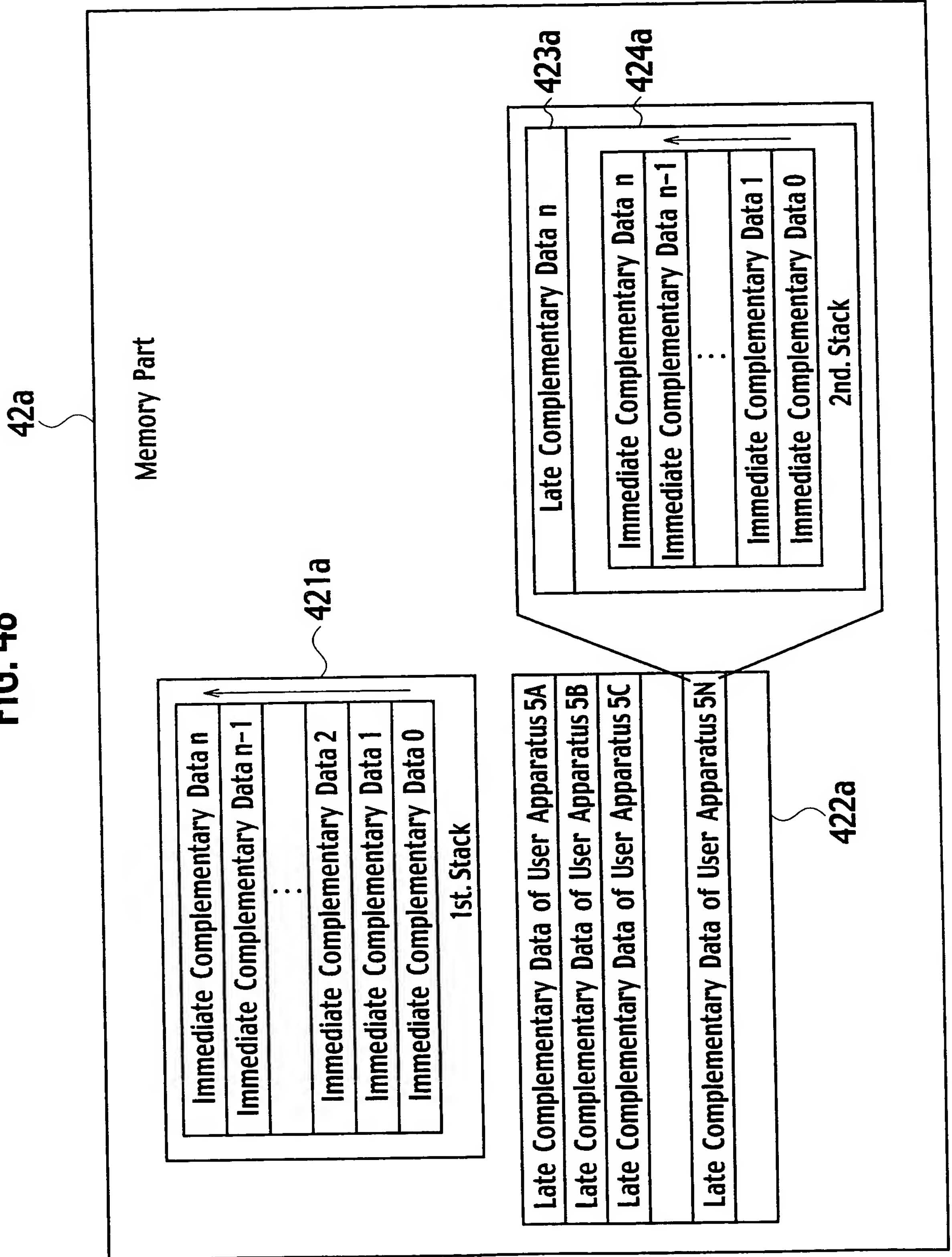


FIG. 48



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FIG. 49

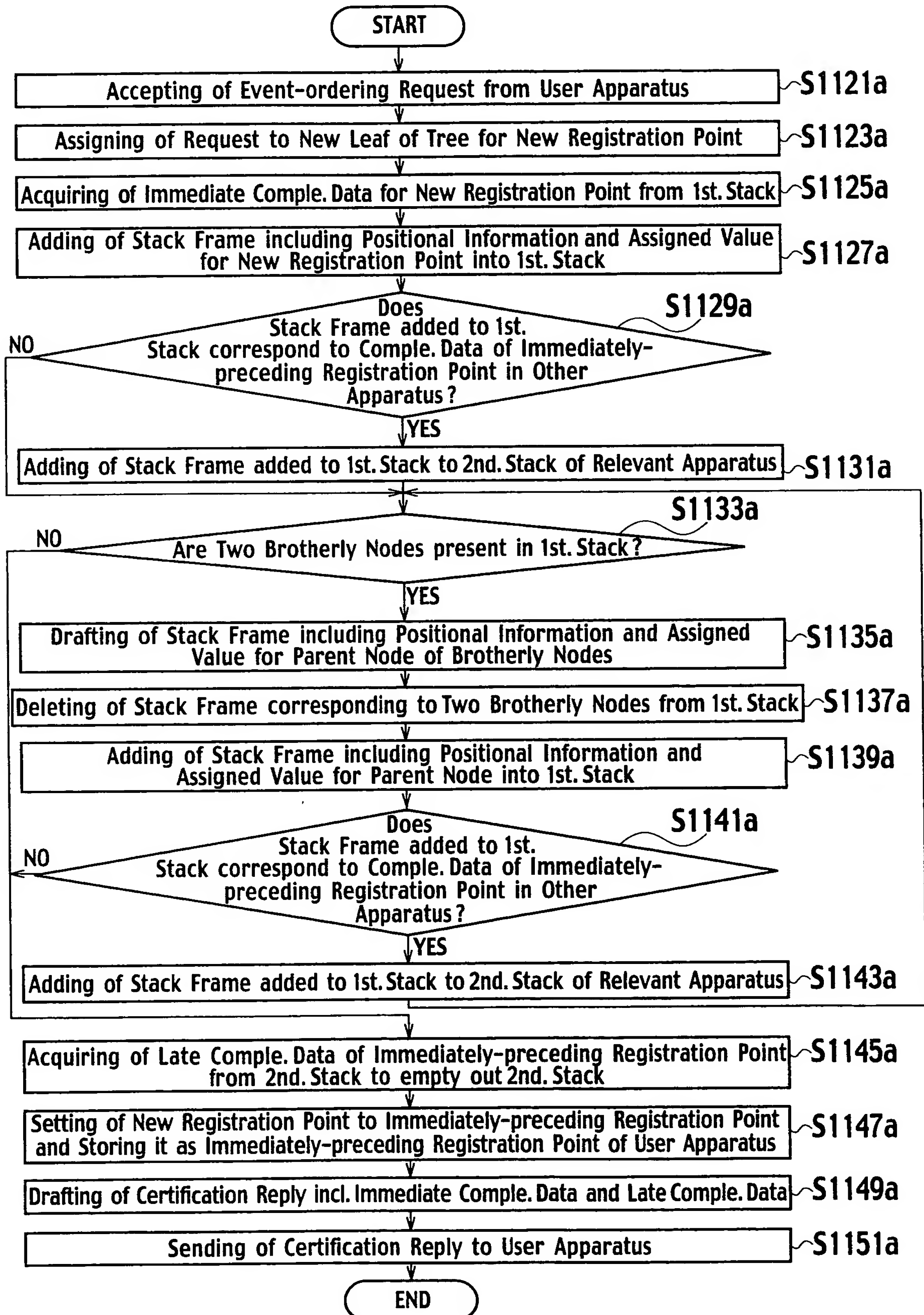
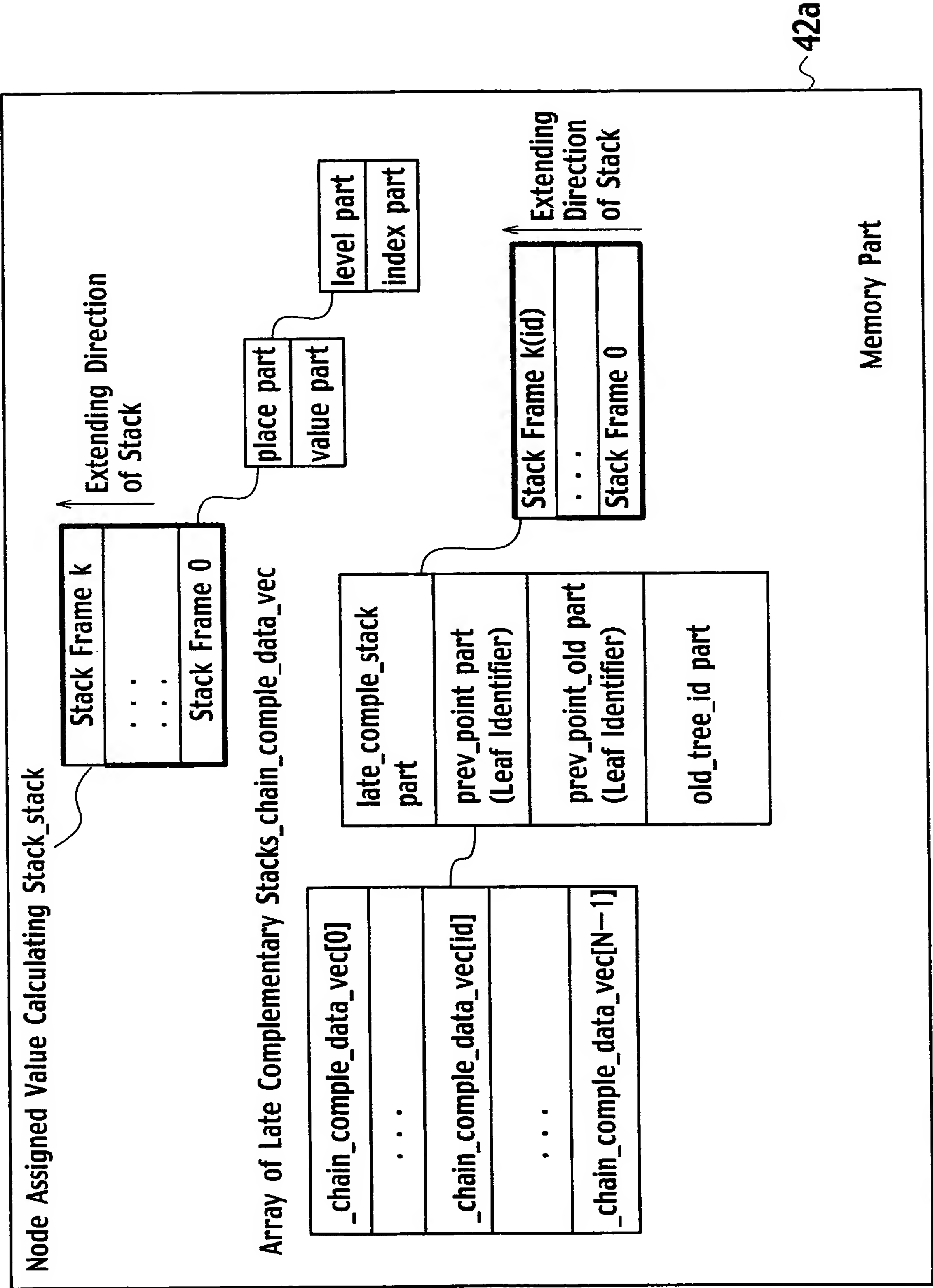
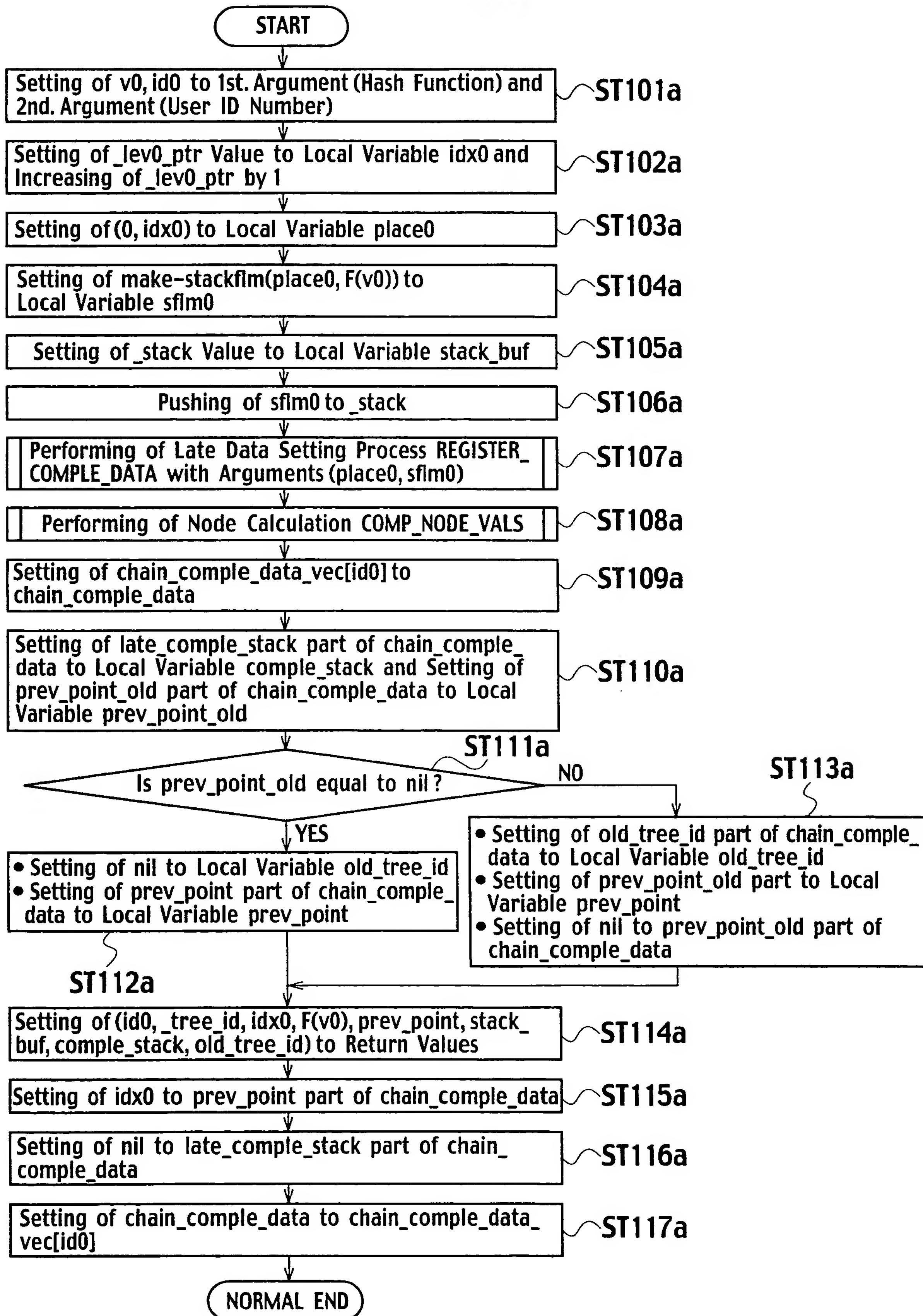


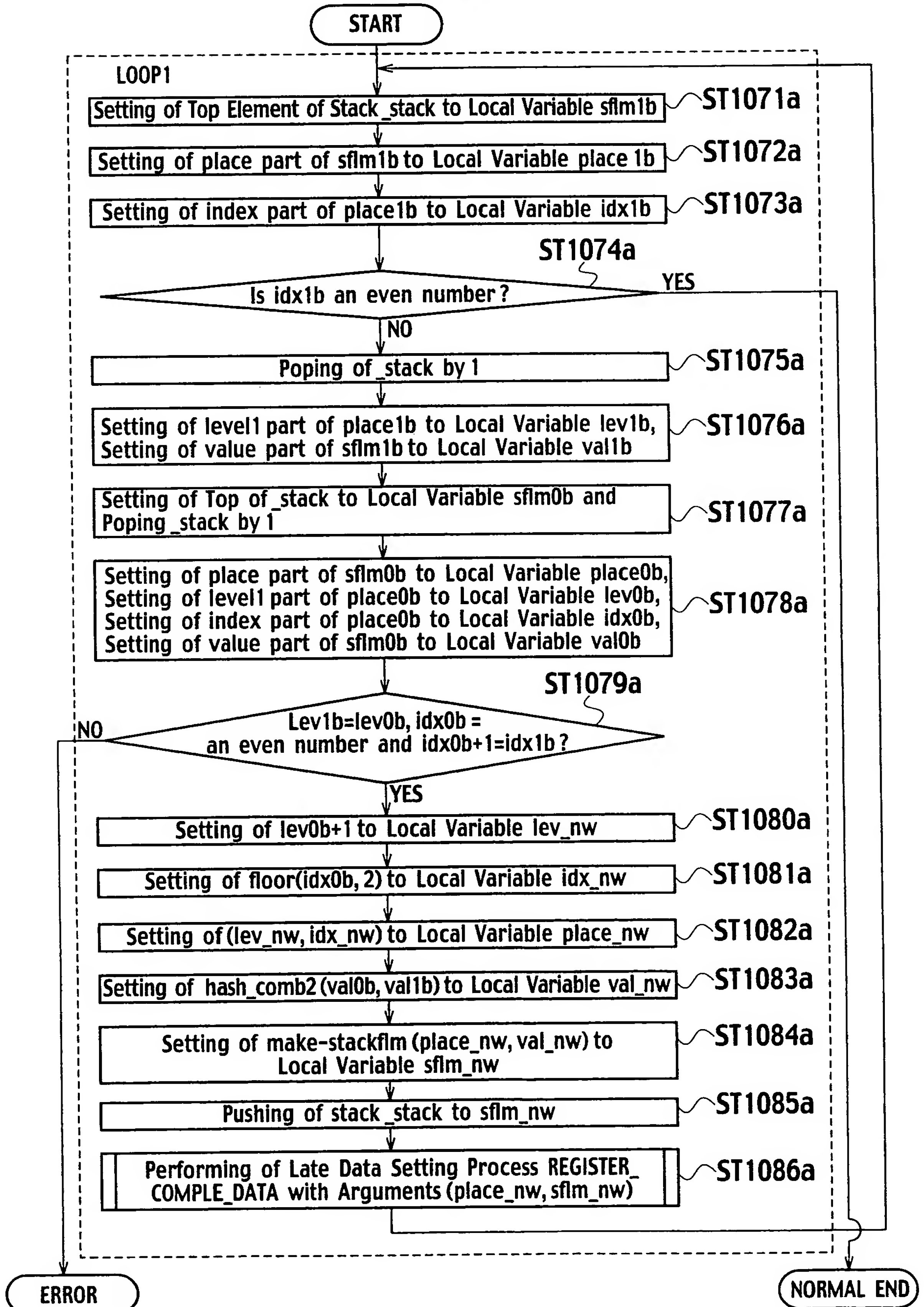
FIG. 50



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FIG. 51



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FIG. 52



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FIG. 53

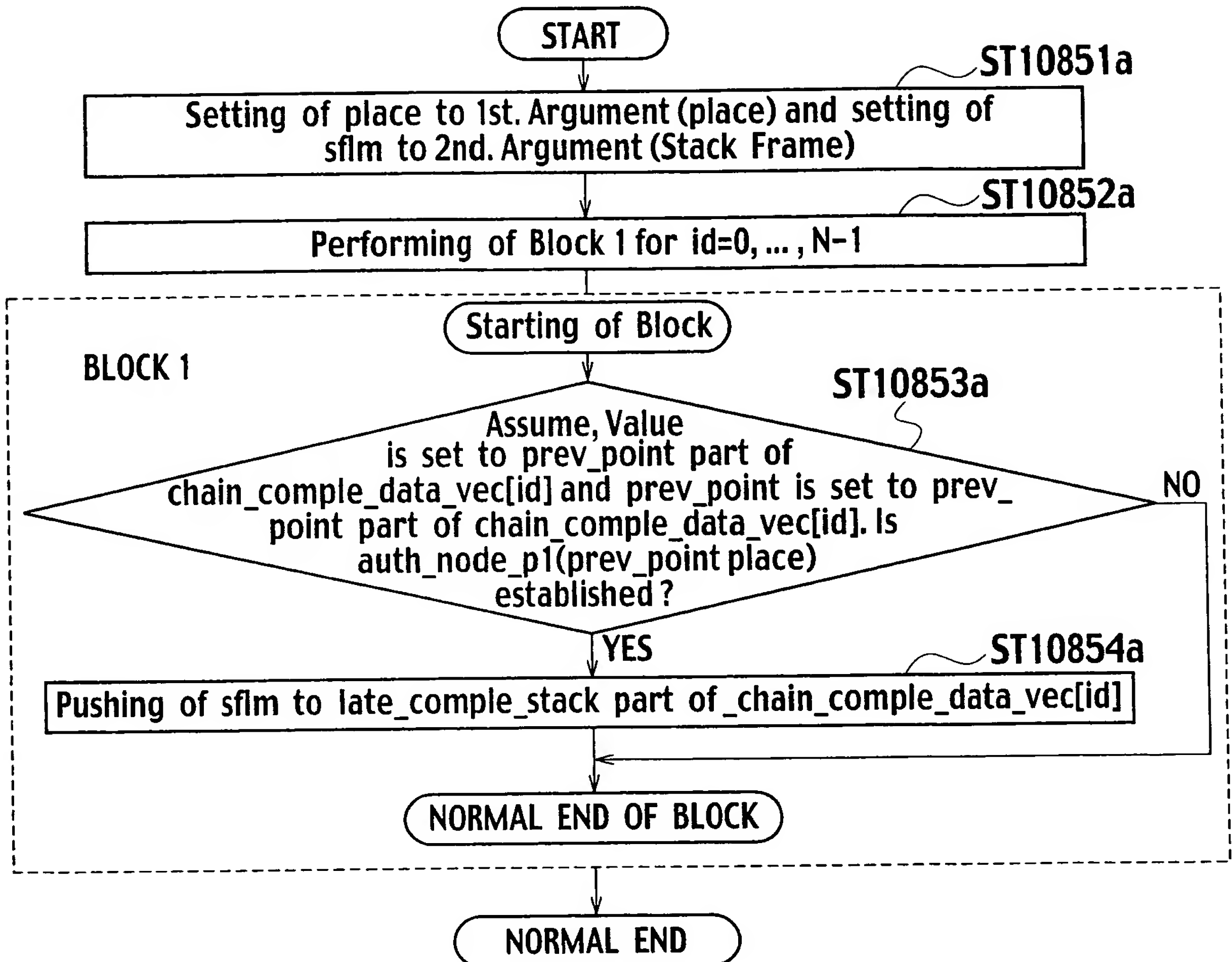
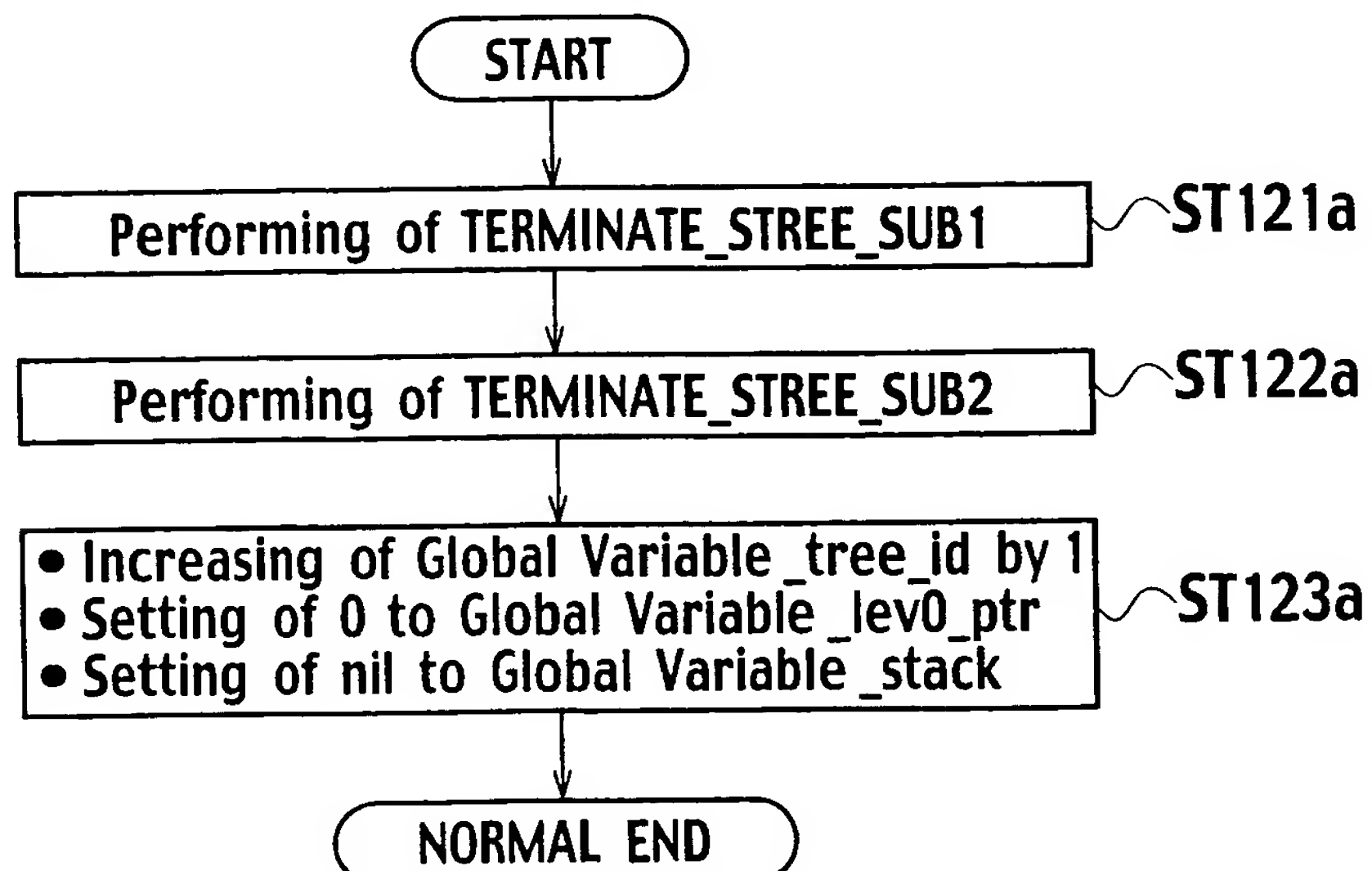
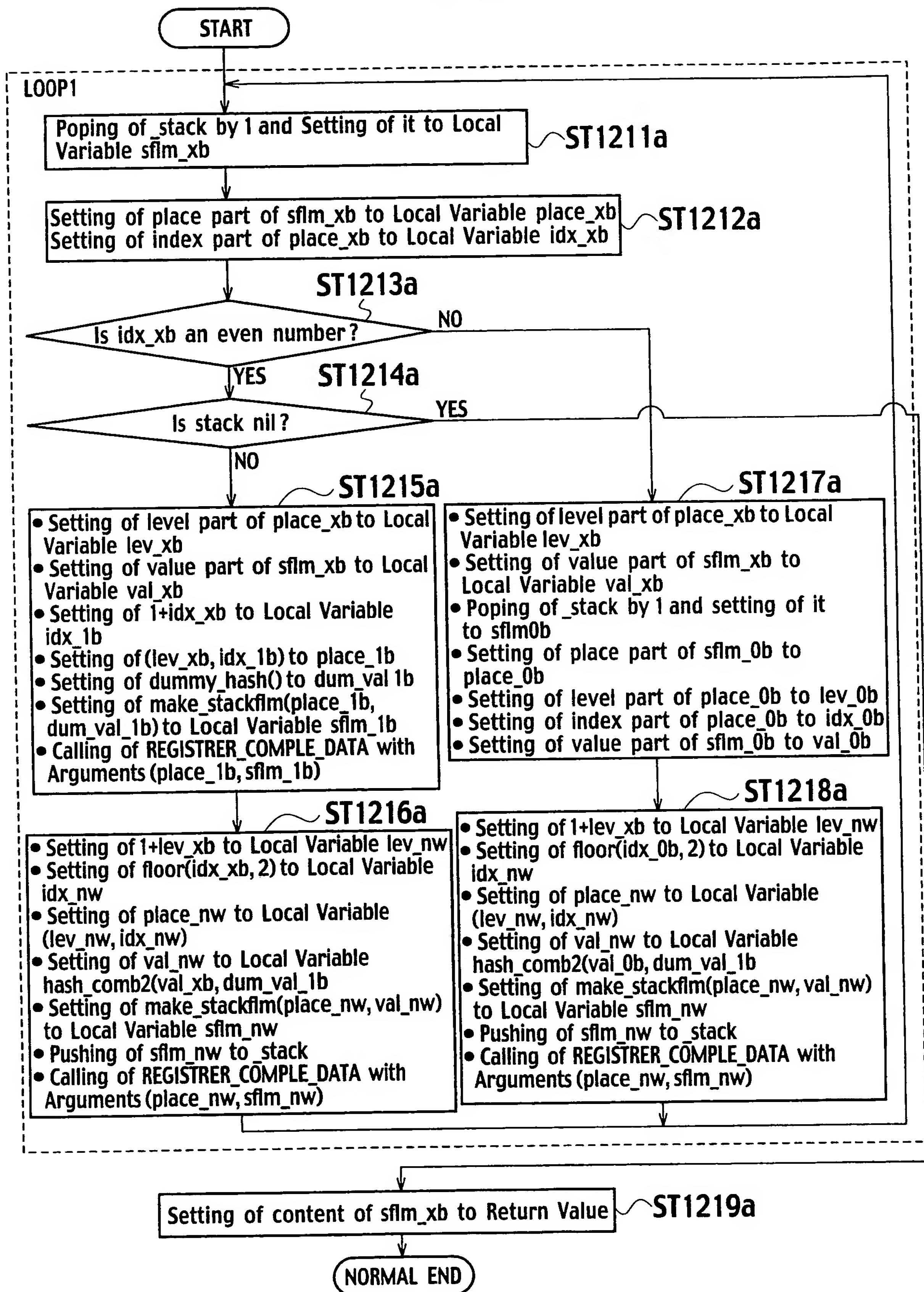


FIG. 54



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FIG. 55



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FIG. 56

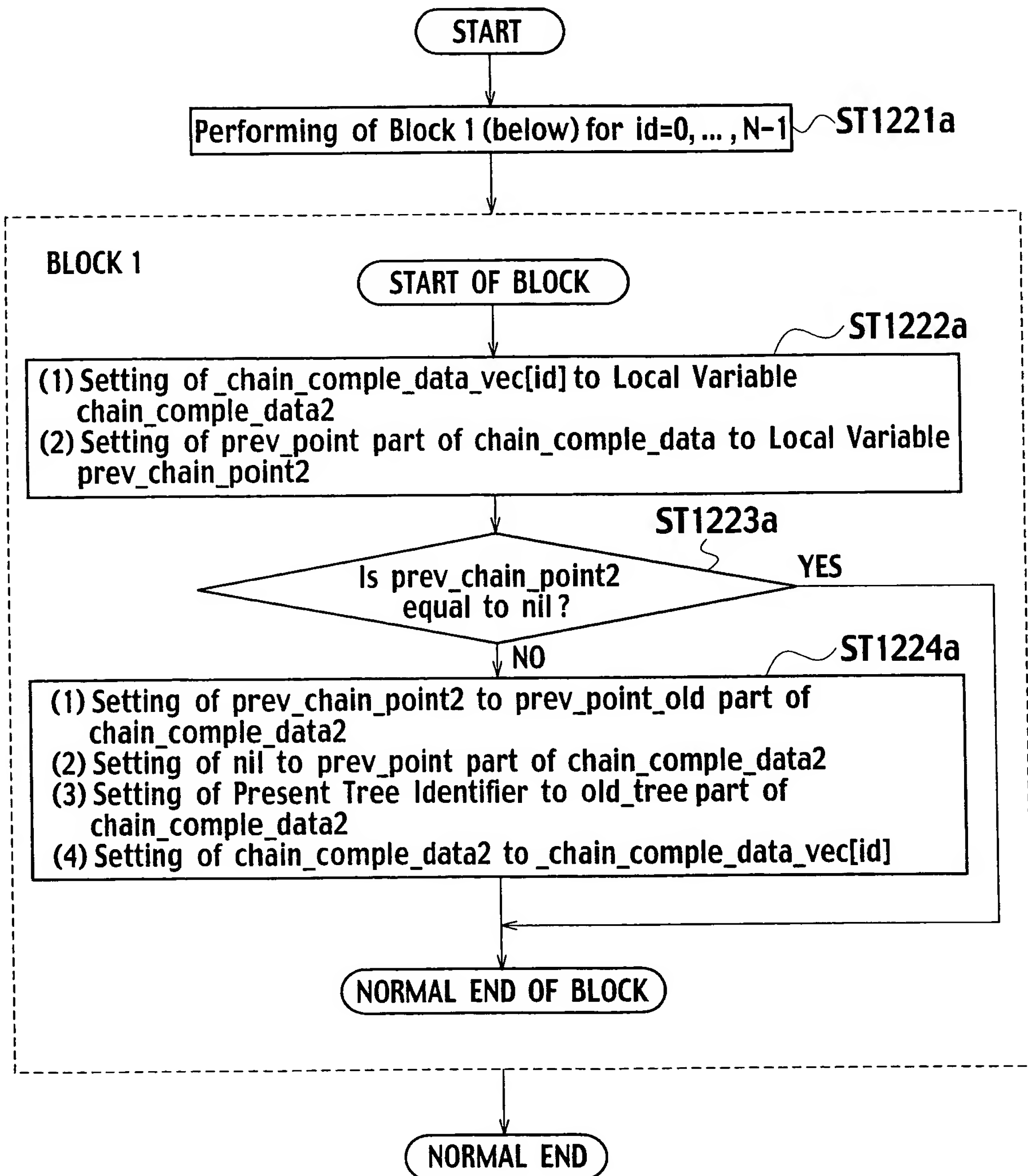
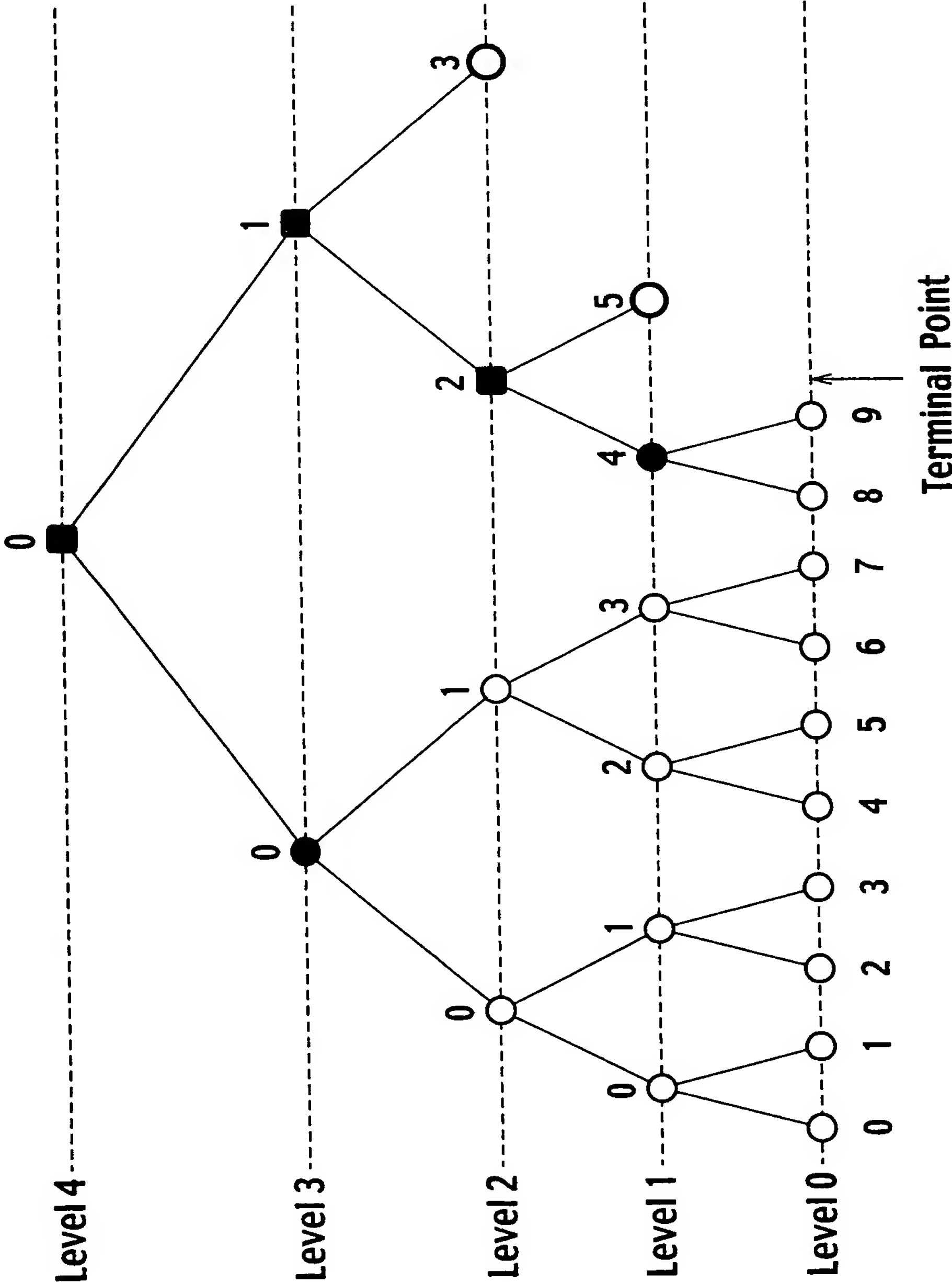
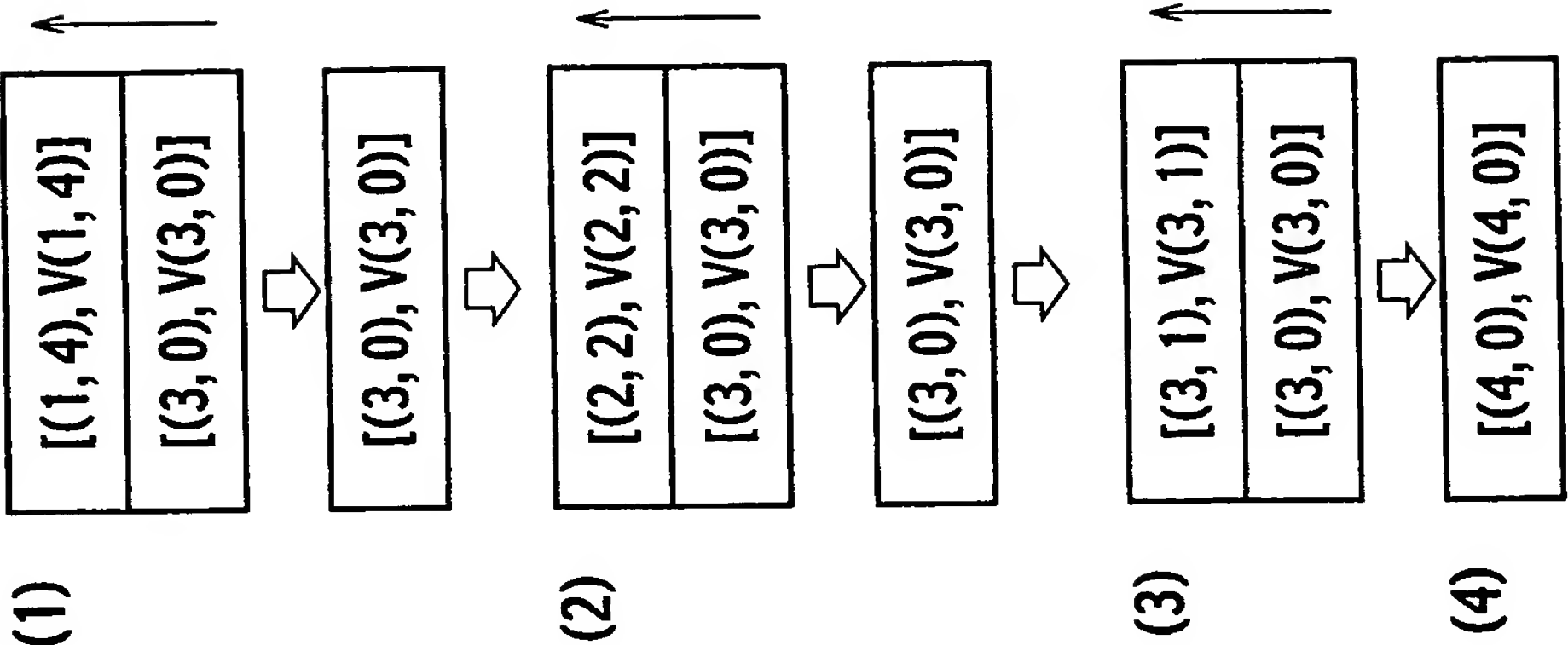


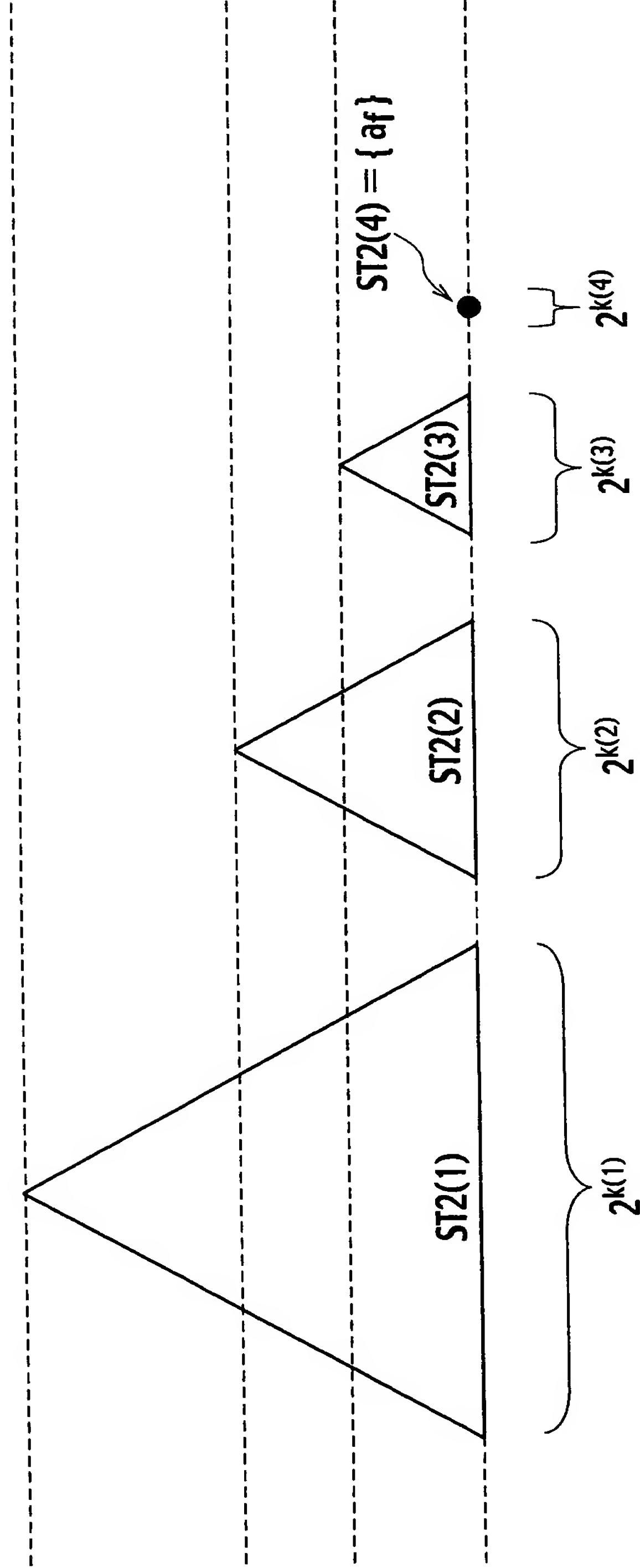
FIG. 57

Change in Content of _stack



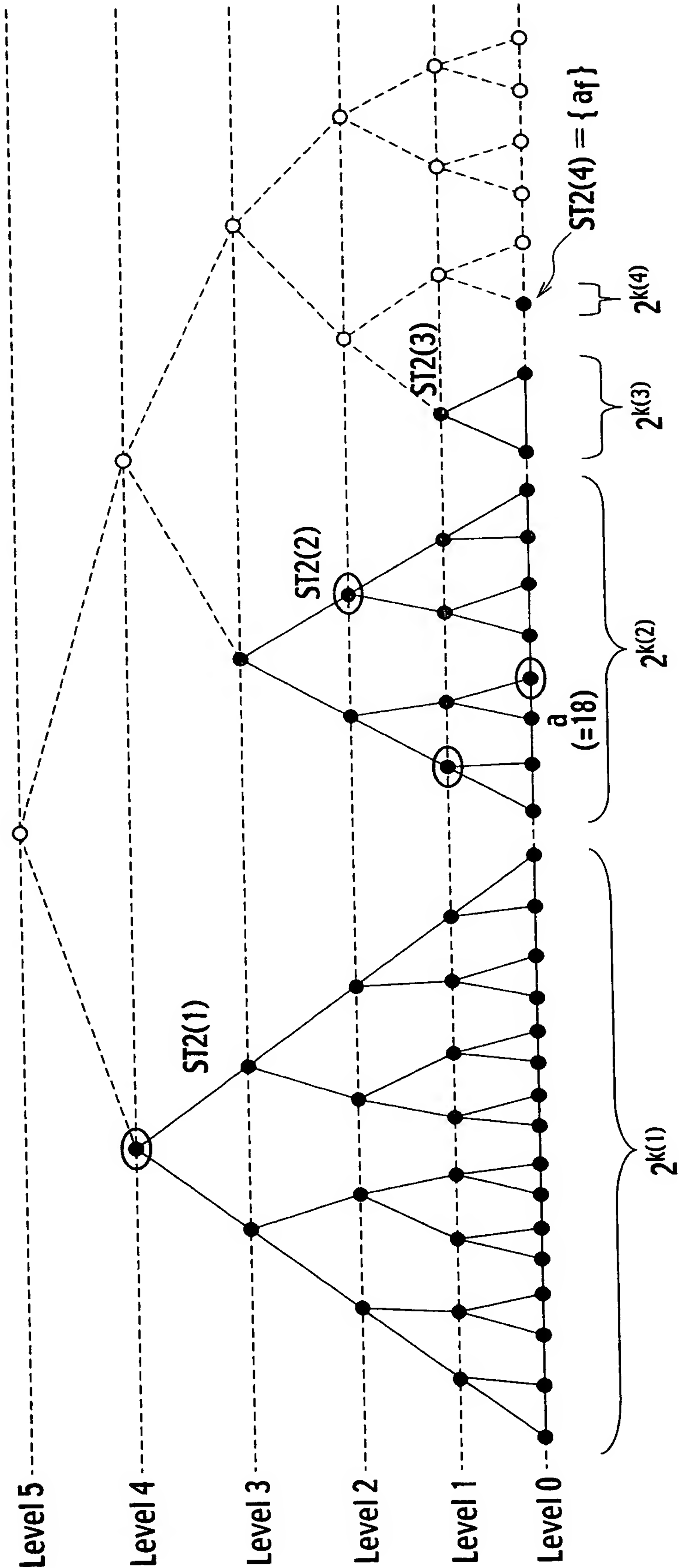
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FIG. 58



$$k(1) > k(2) > k(3) > k(4) = 0$$

FIG. 59



$$k(1)=4 > k(2)=3 > k(3)=1 > k(4)=0$$

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FIG. 60

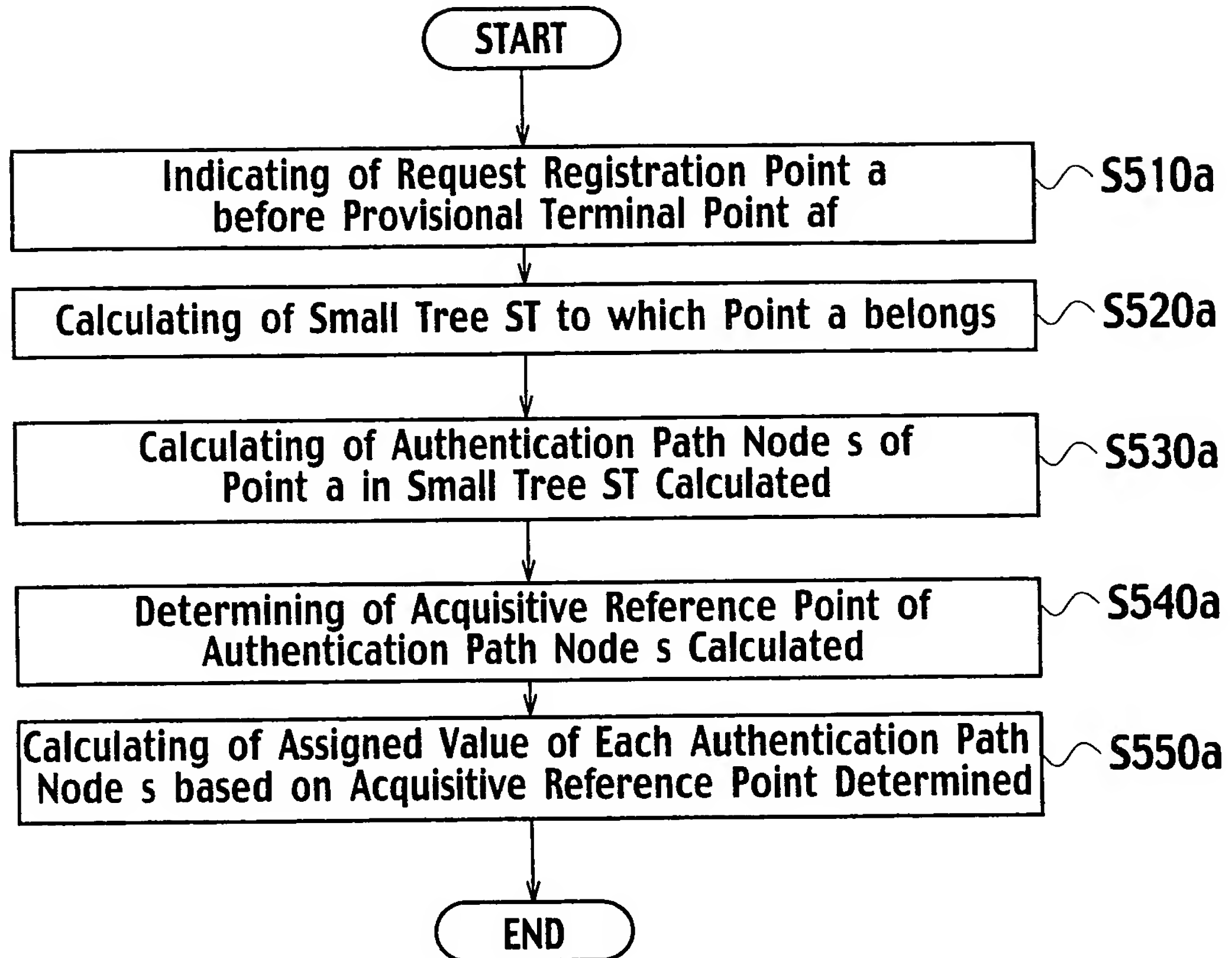
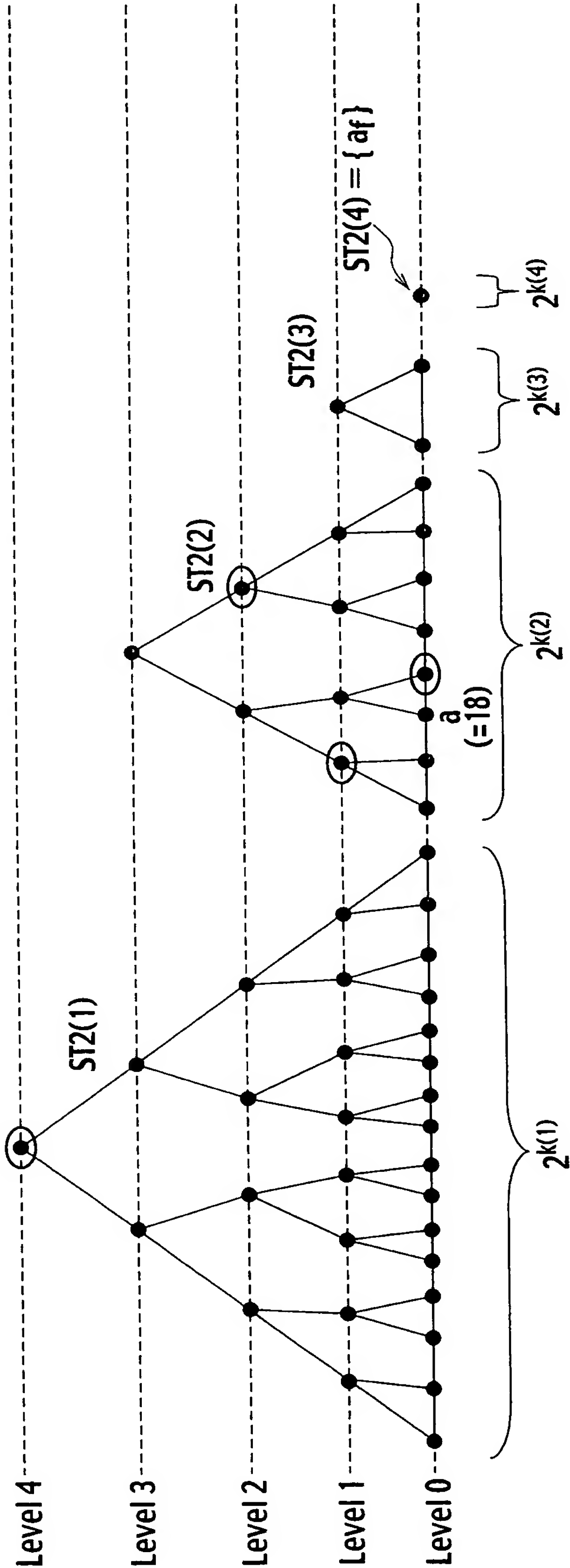


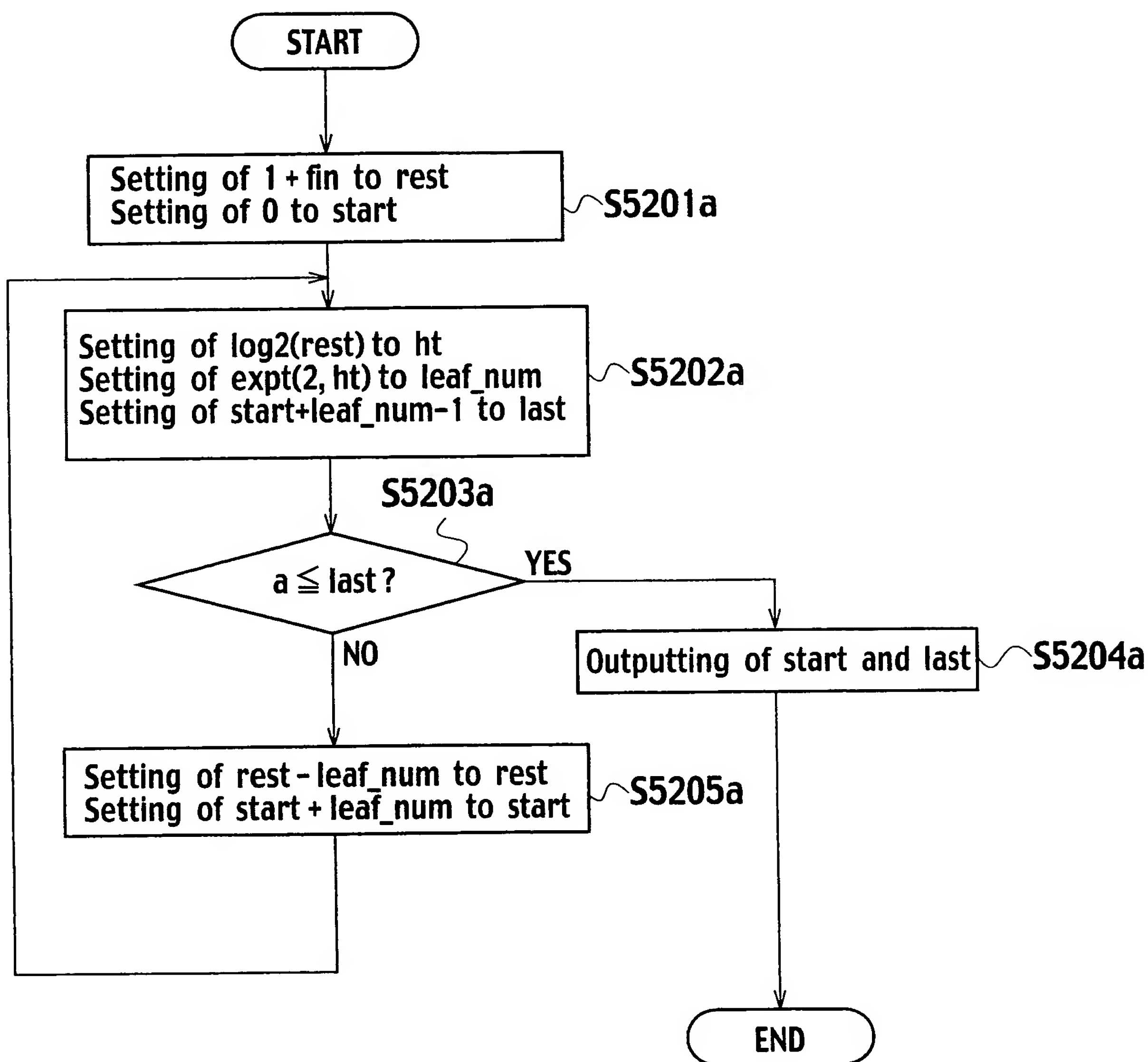
FIG. 61



$k(1)=4 > k(2)=3 > k(3)=1 > k(4)=0$

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FIG. 62



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FIG. 63

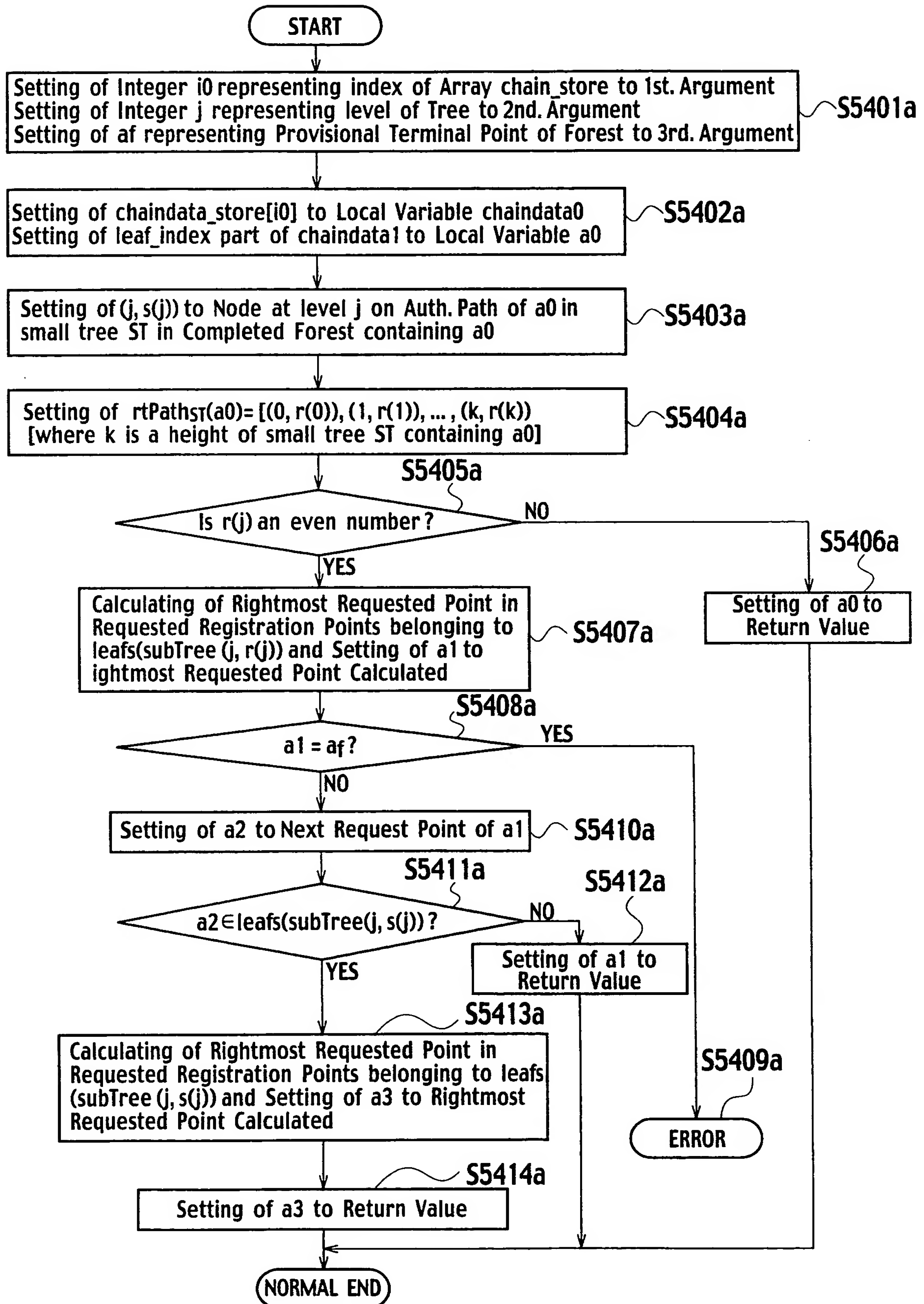
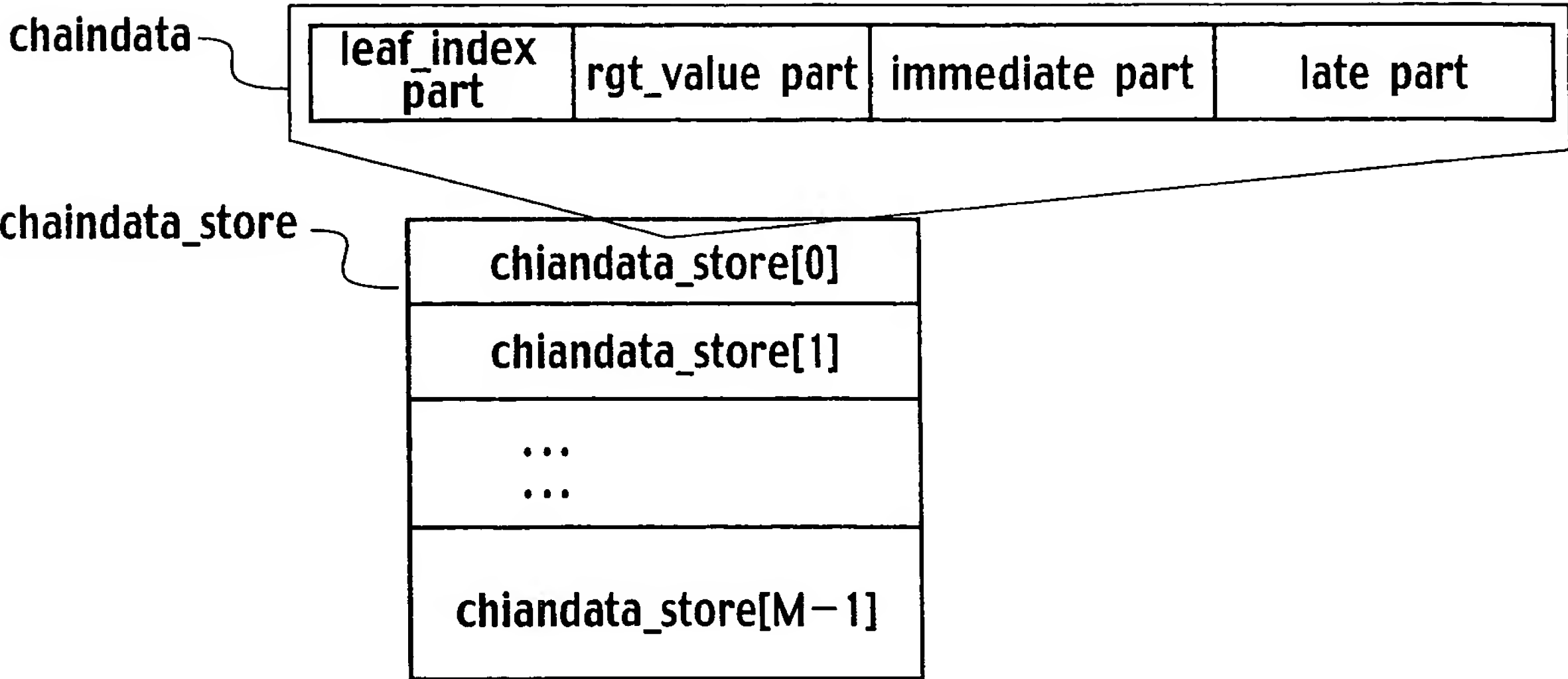
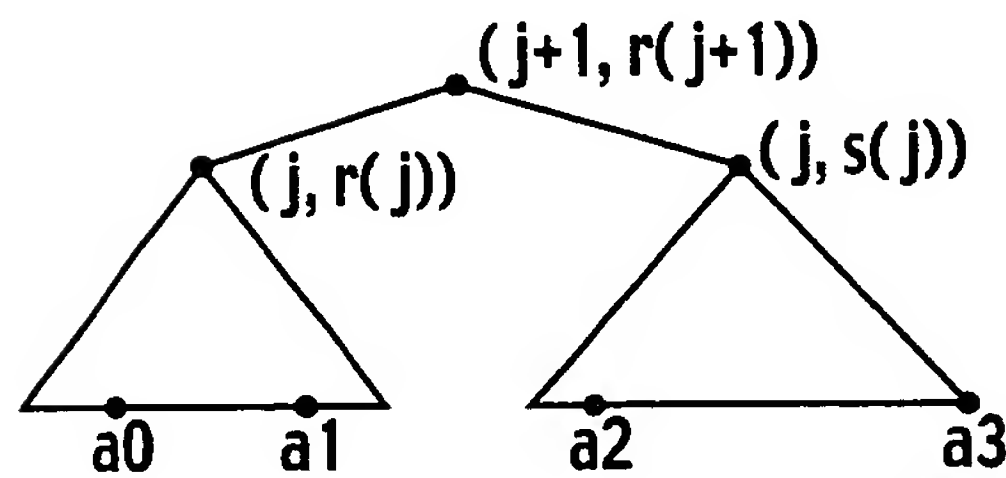


FIG. 64



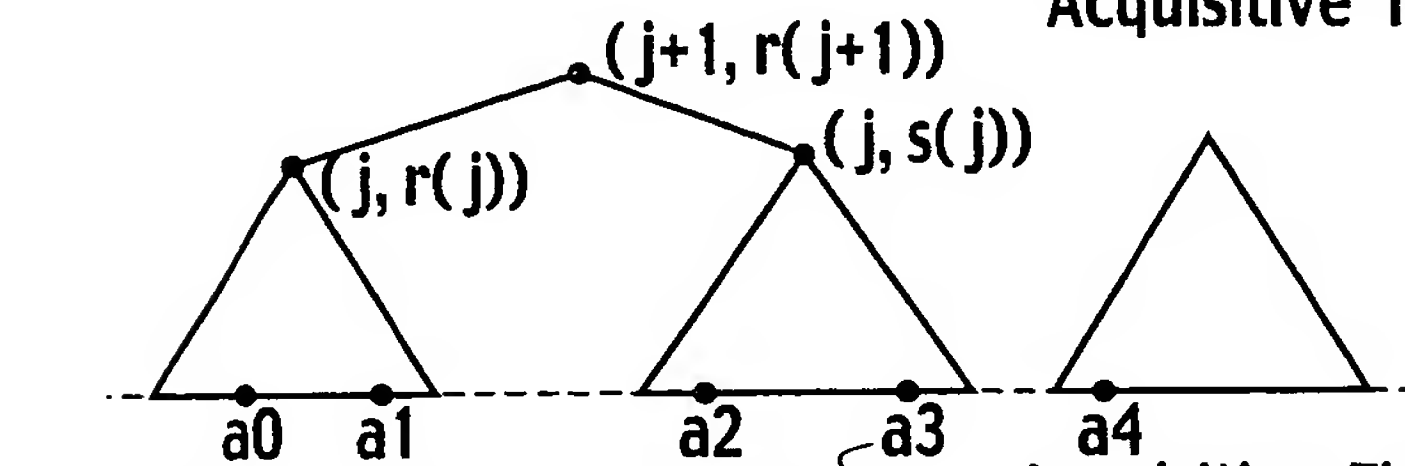
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FIG. 65A



Acquisitive Reference Point
 Acquisitive Timing Point

FIG. 65B



Acquisitive Reference Point
 Acquisitive Timing Point
 (including also Postscript Point)

FIG. 65C

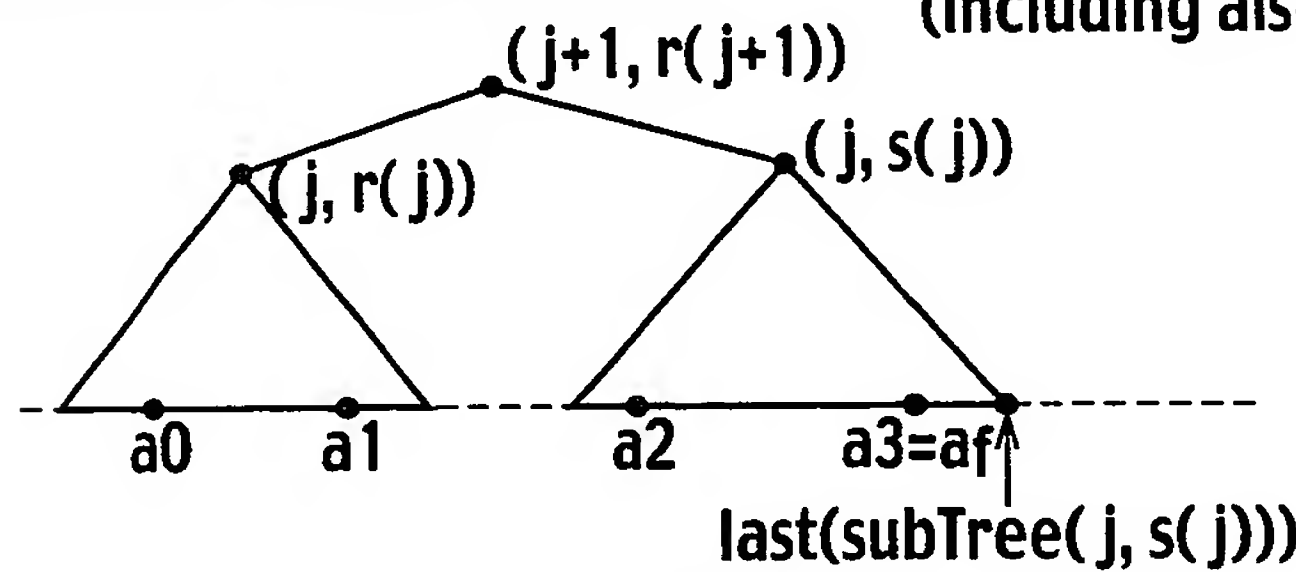
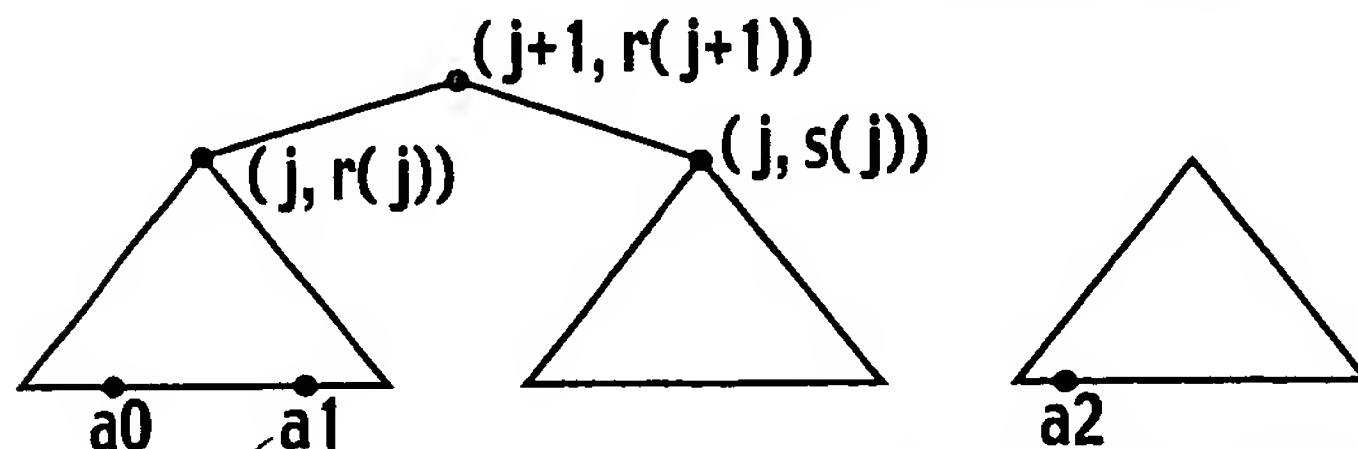
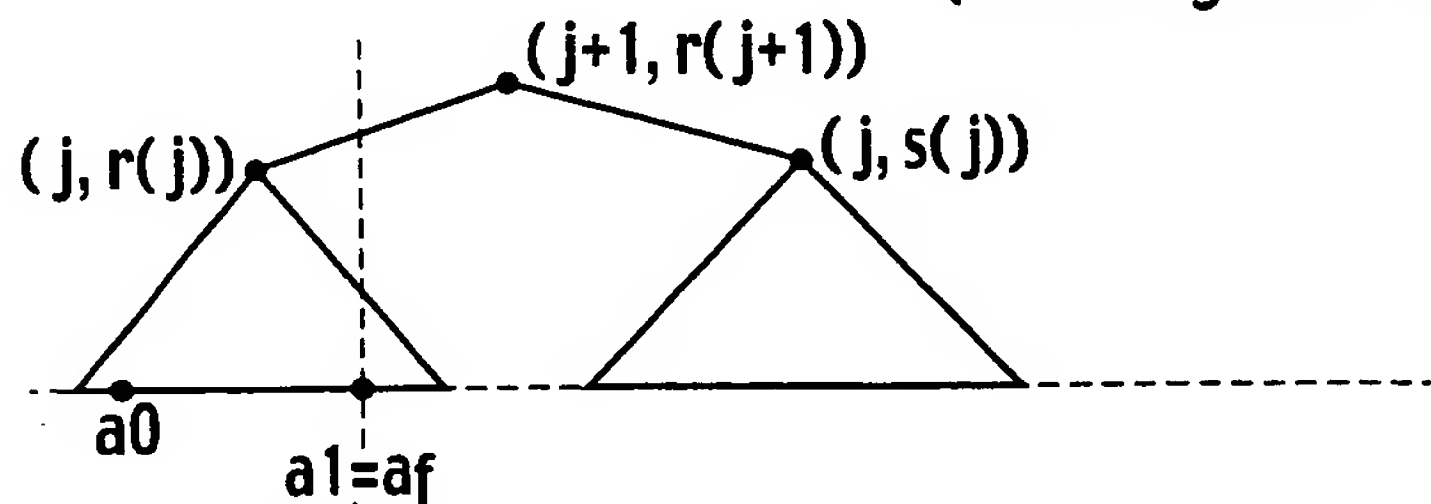


FIG. 65D



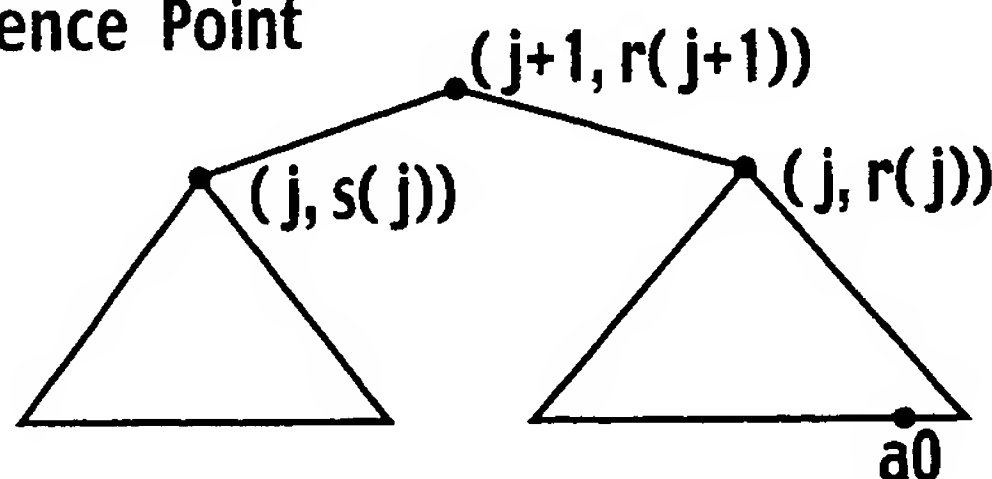
Acquisitive Reference Point
 Acquisitive Timing Point
 (including also Postscript Point)

FIG. 65E



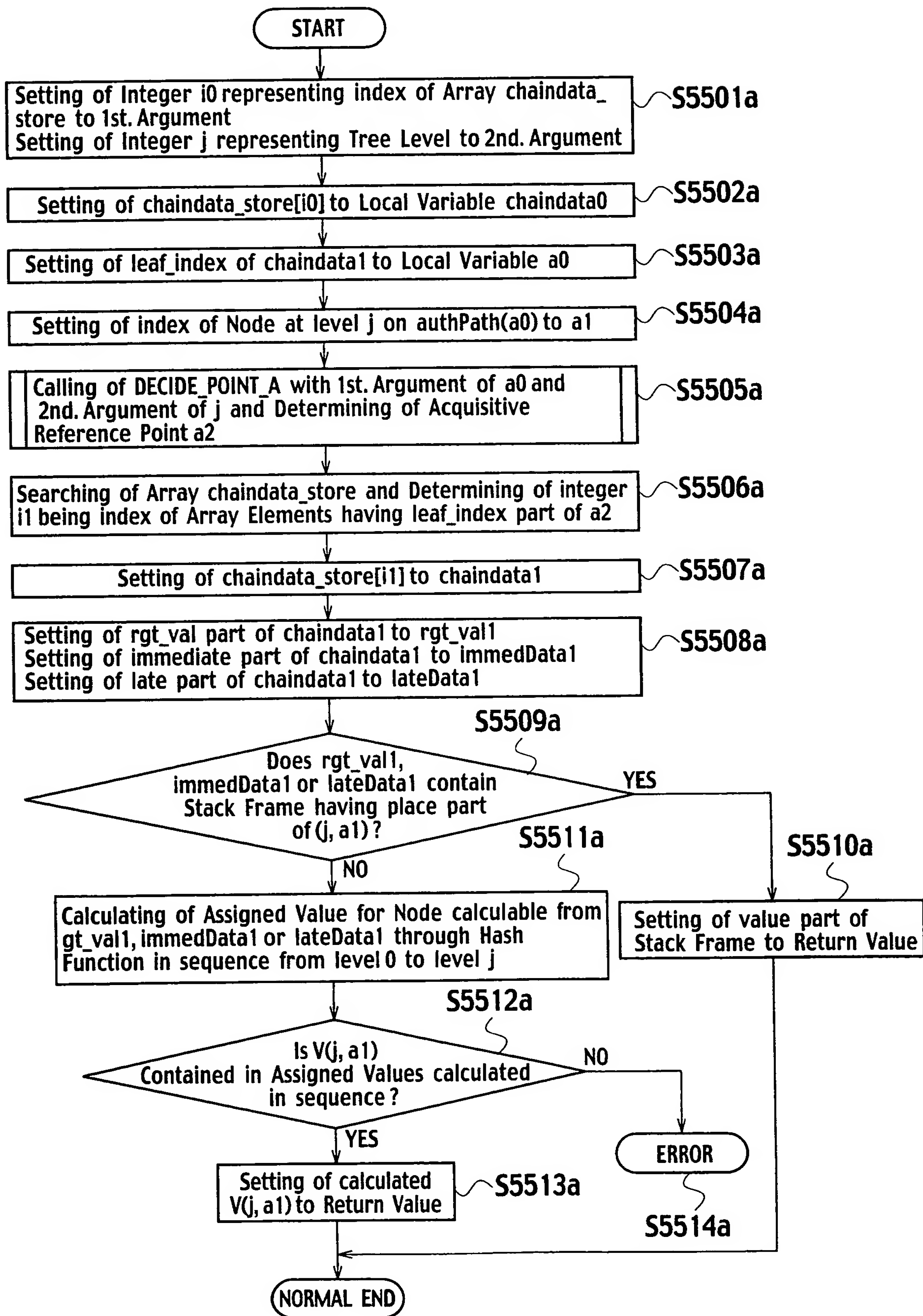
Acquisitive Reference Point

FIG. 65F



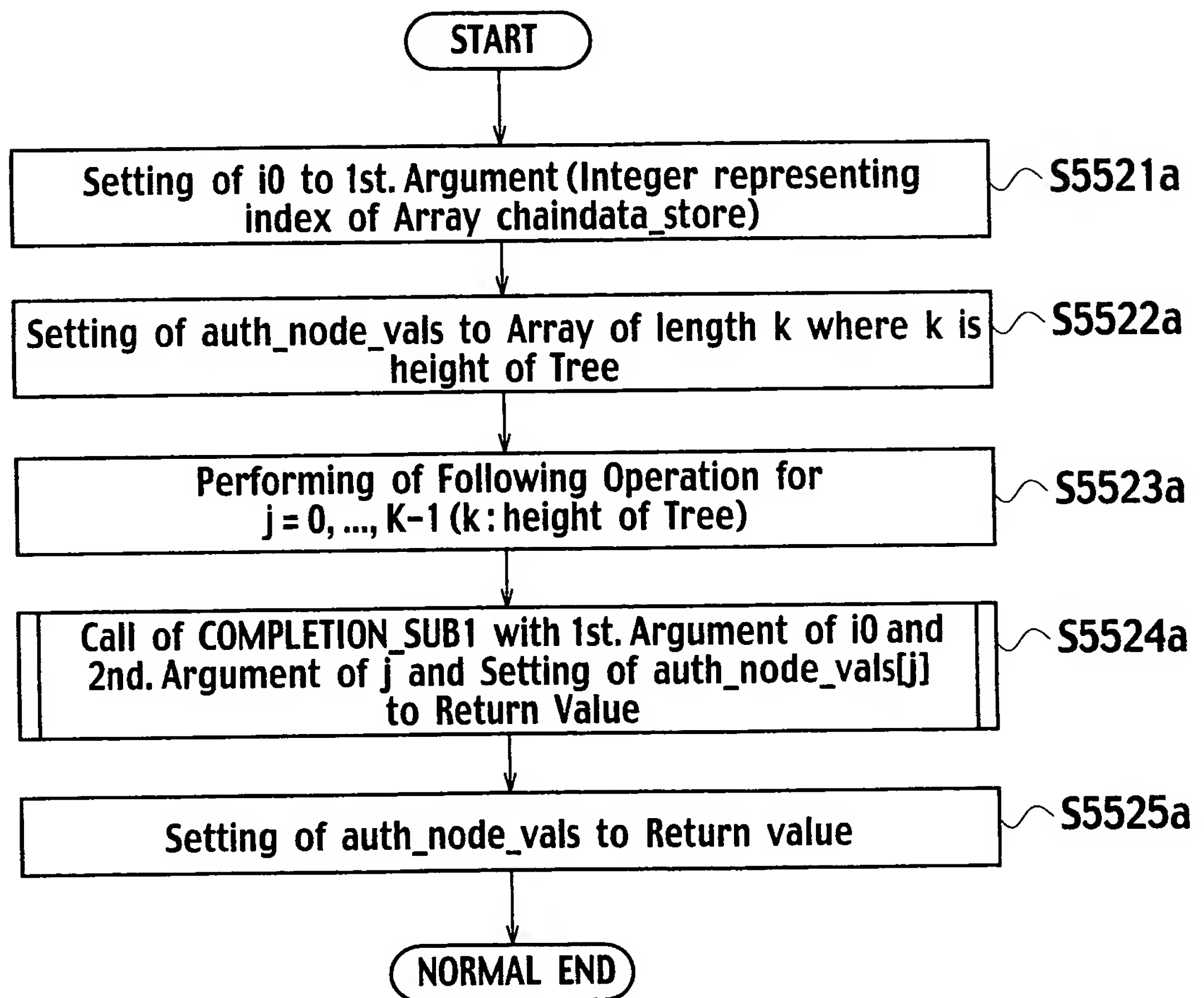
Acquisitive Reference Point
 Acquisitive Timing Point

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FIG. 66



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FIG. 67



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FIG. 68

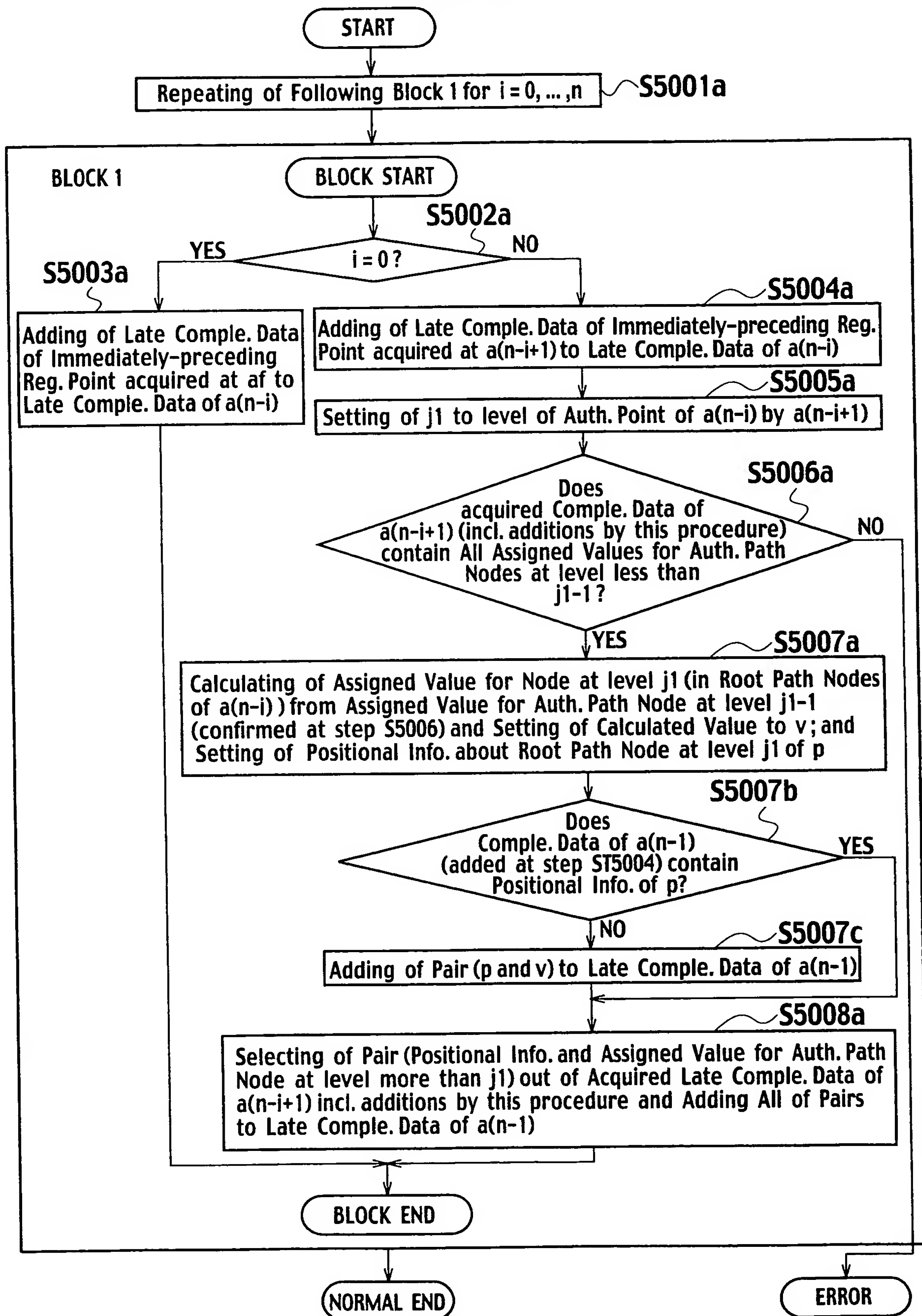


FIG. 69

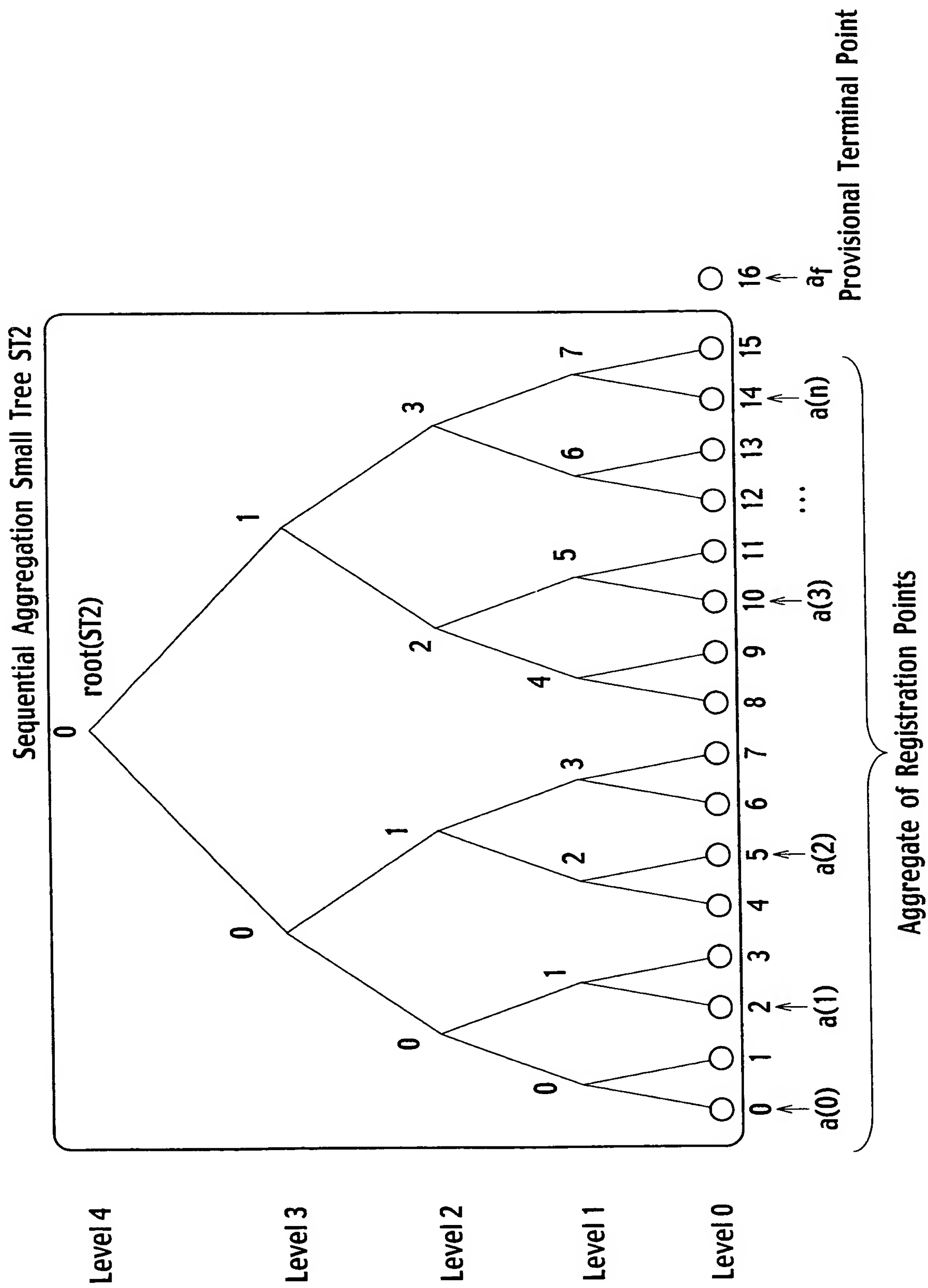


FIG. 70

Sequential Aggregation Small Tree ST2

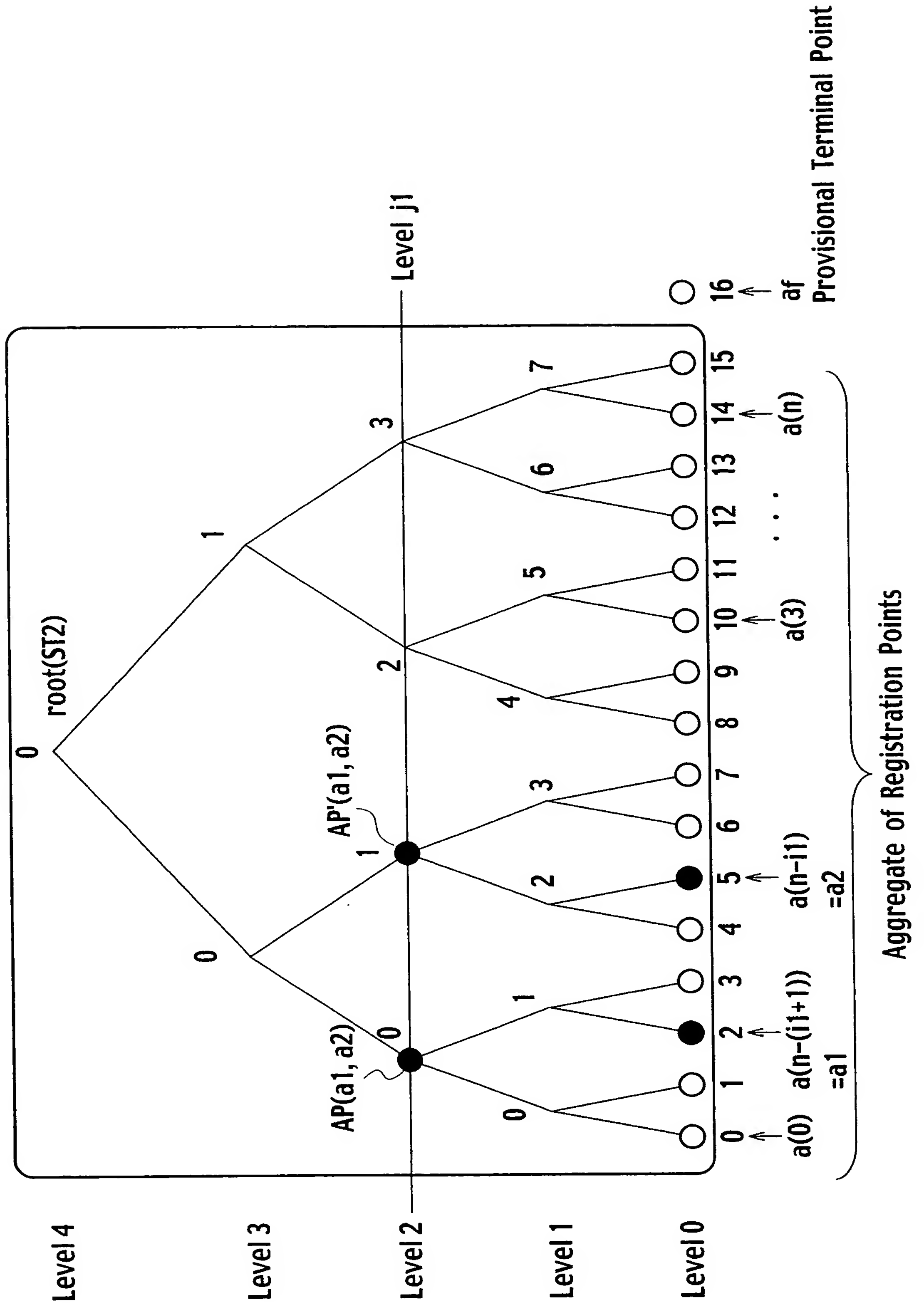


FIG. 71

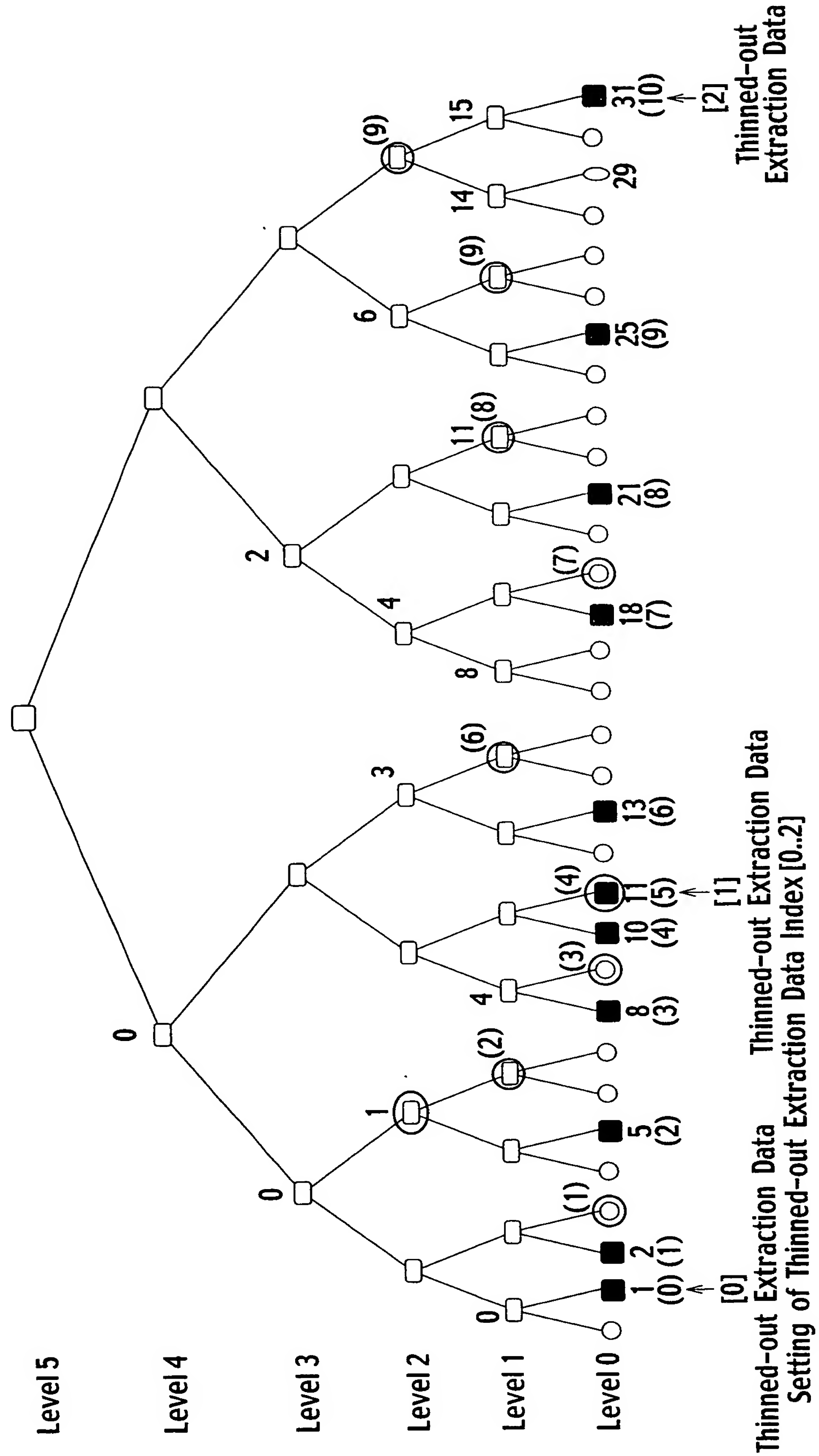


FIG. 72

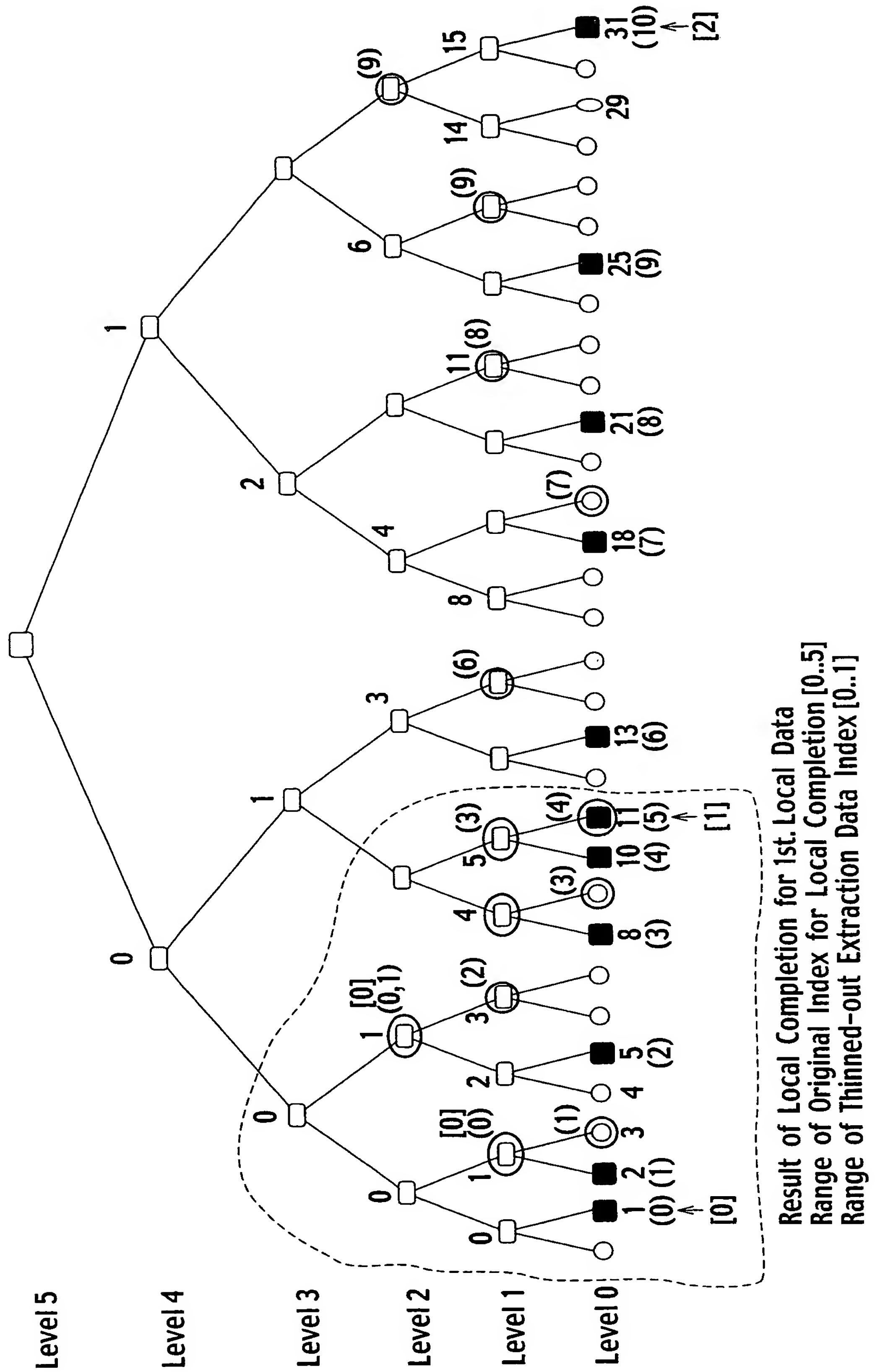


FIG. 73

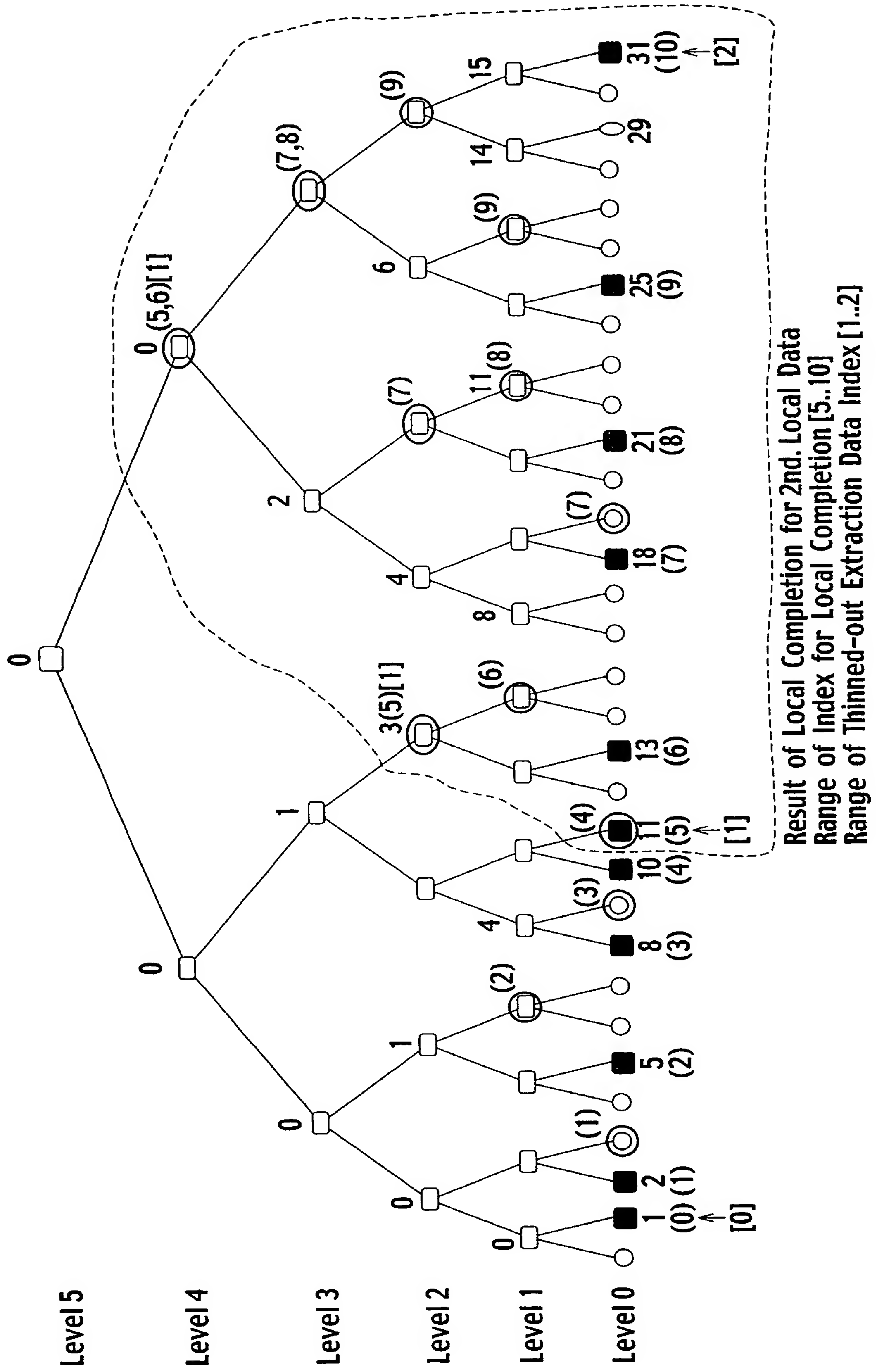
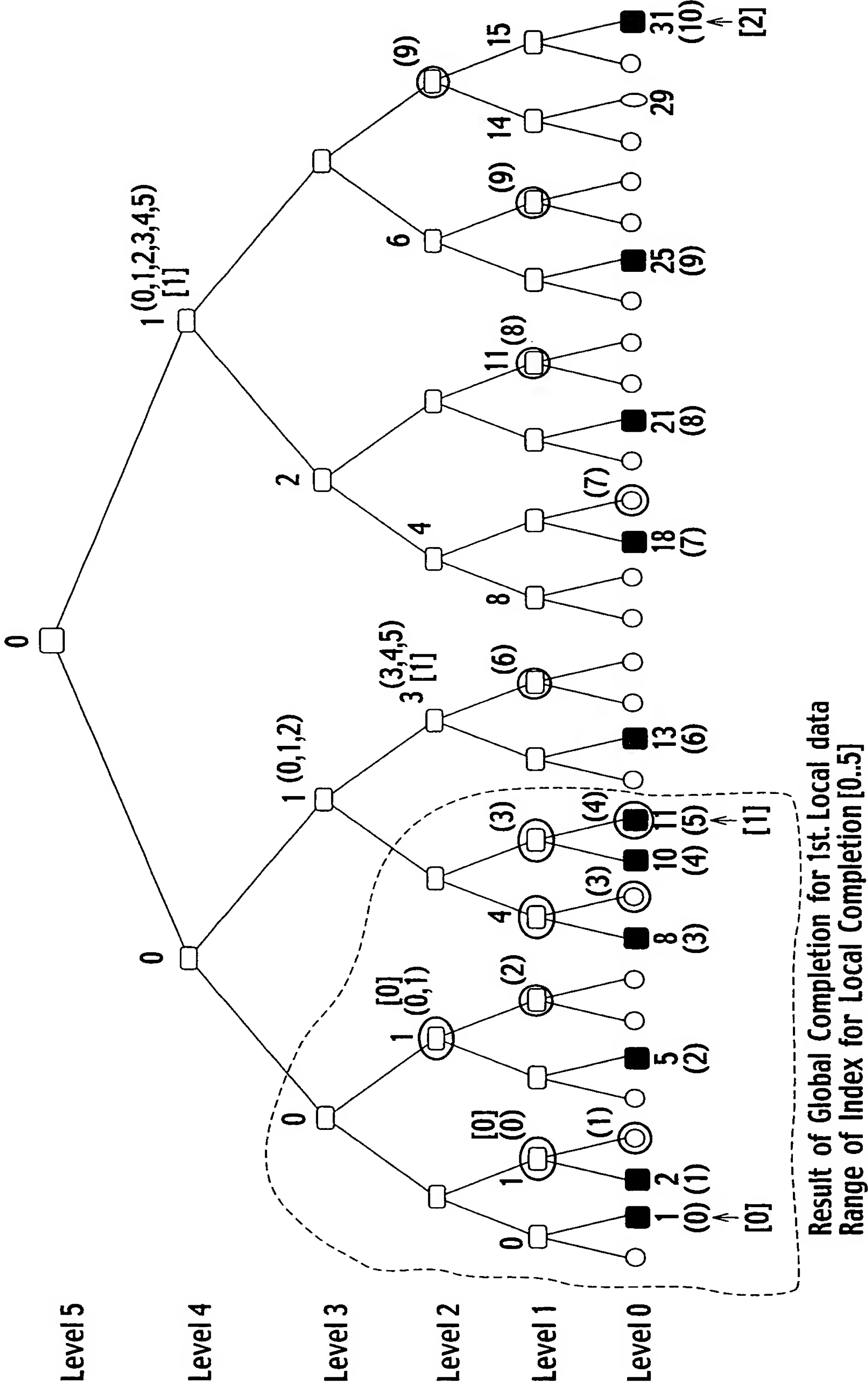


FIG. 75



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FIG. 76

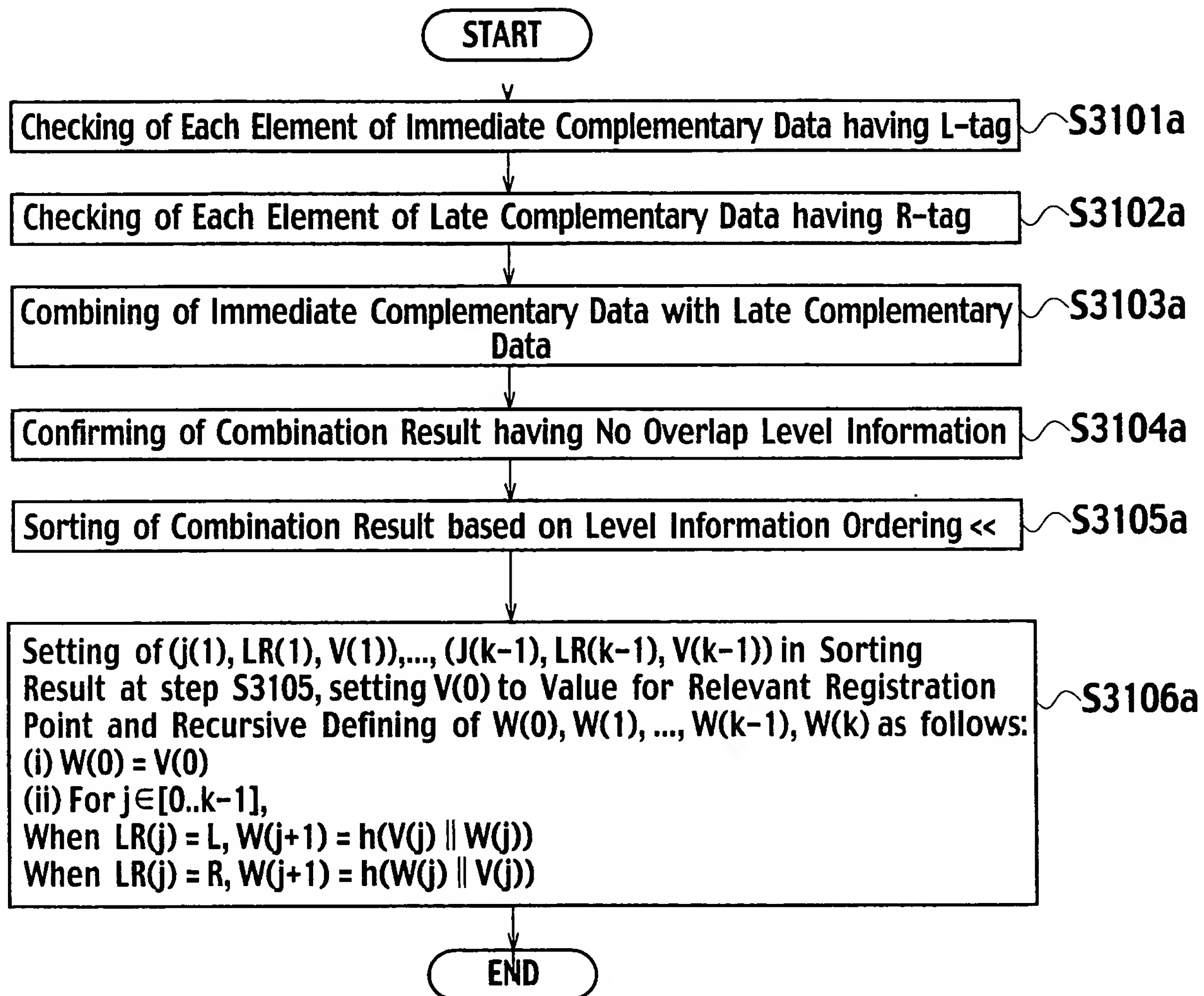


FIG. 77

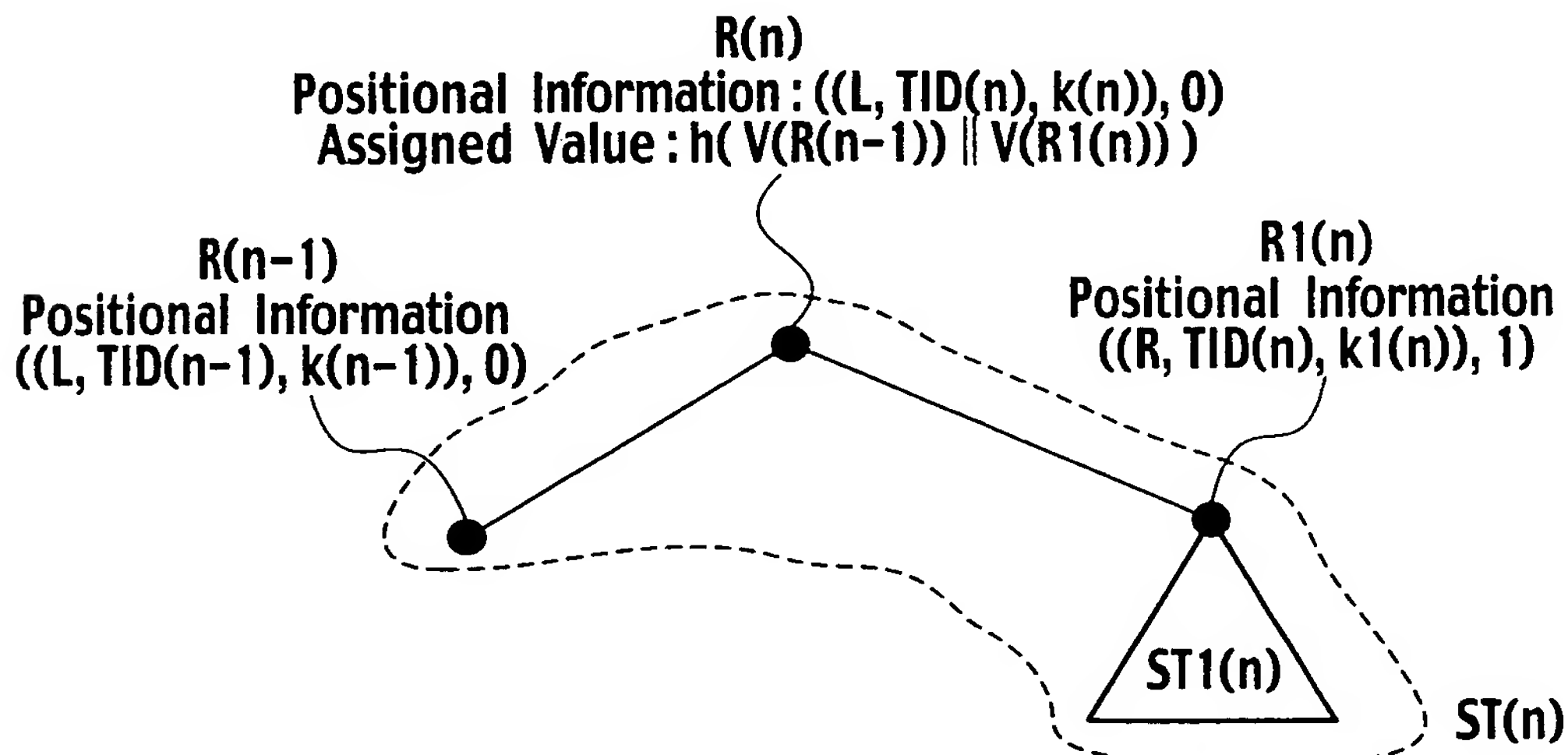
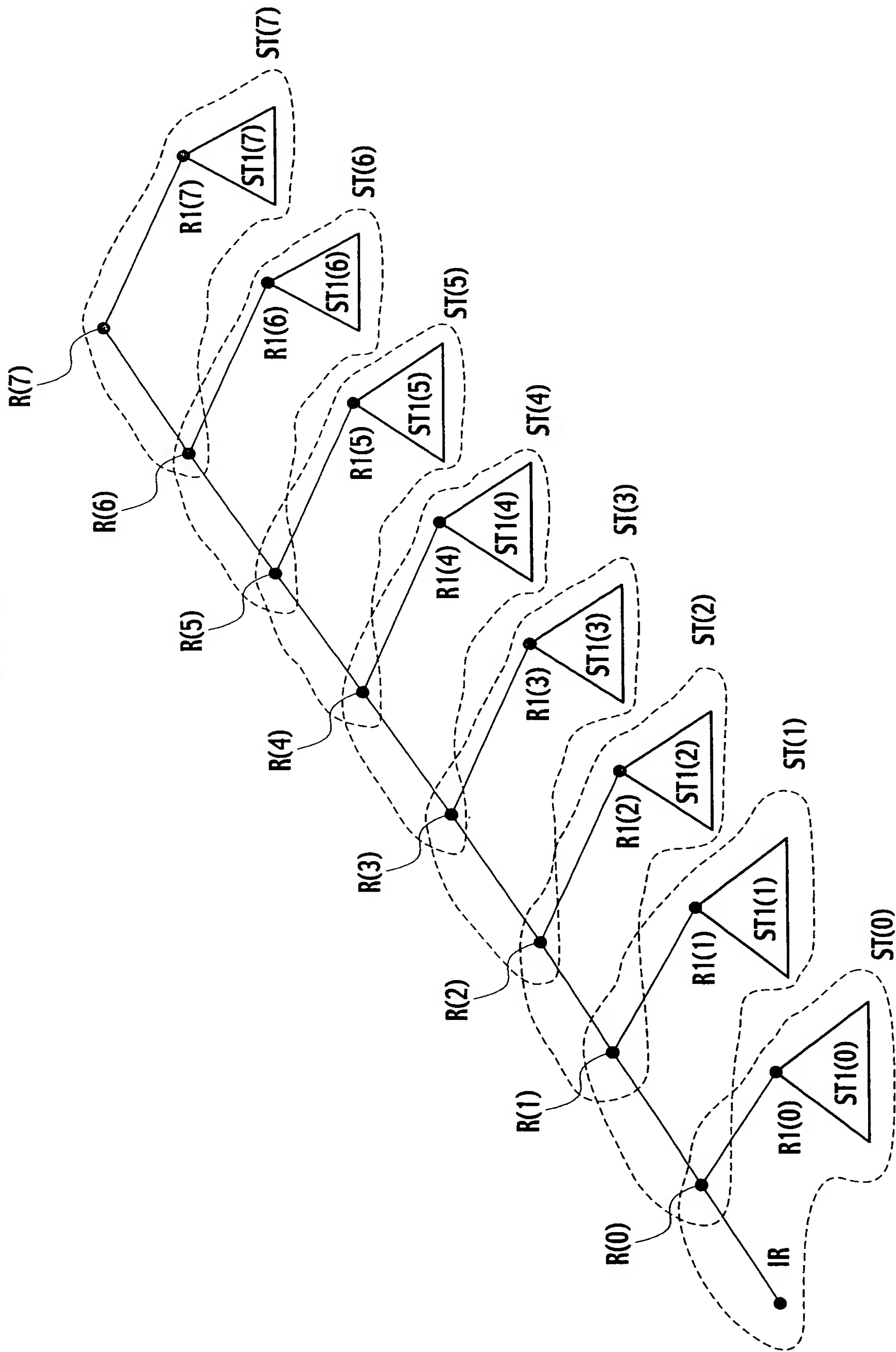


FIG. 78



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FIG. 79

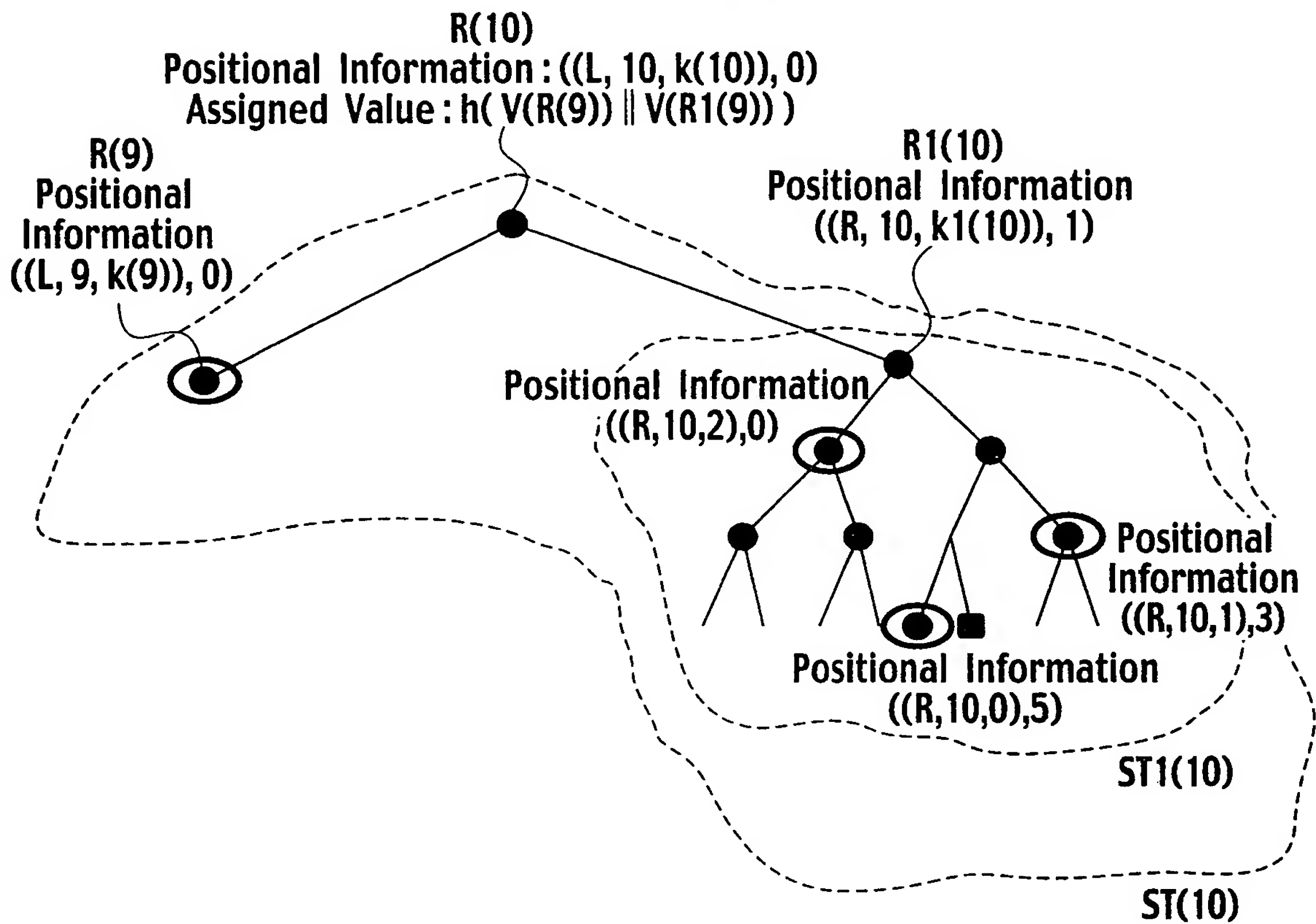
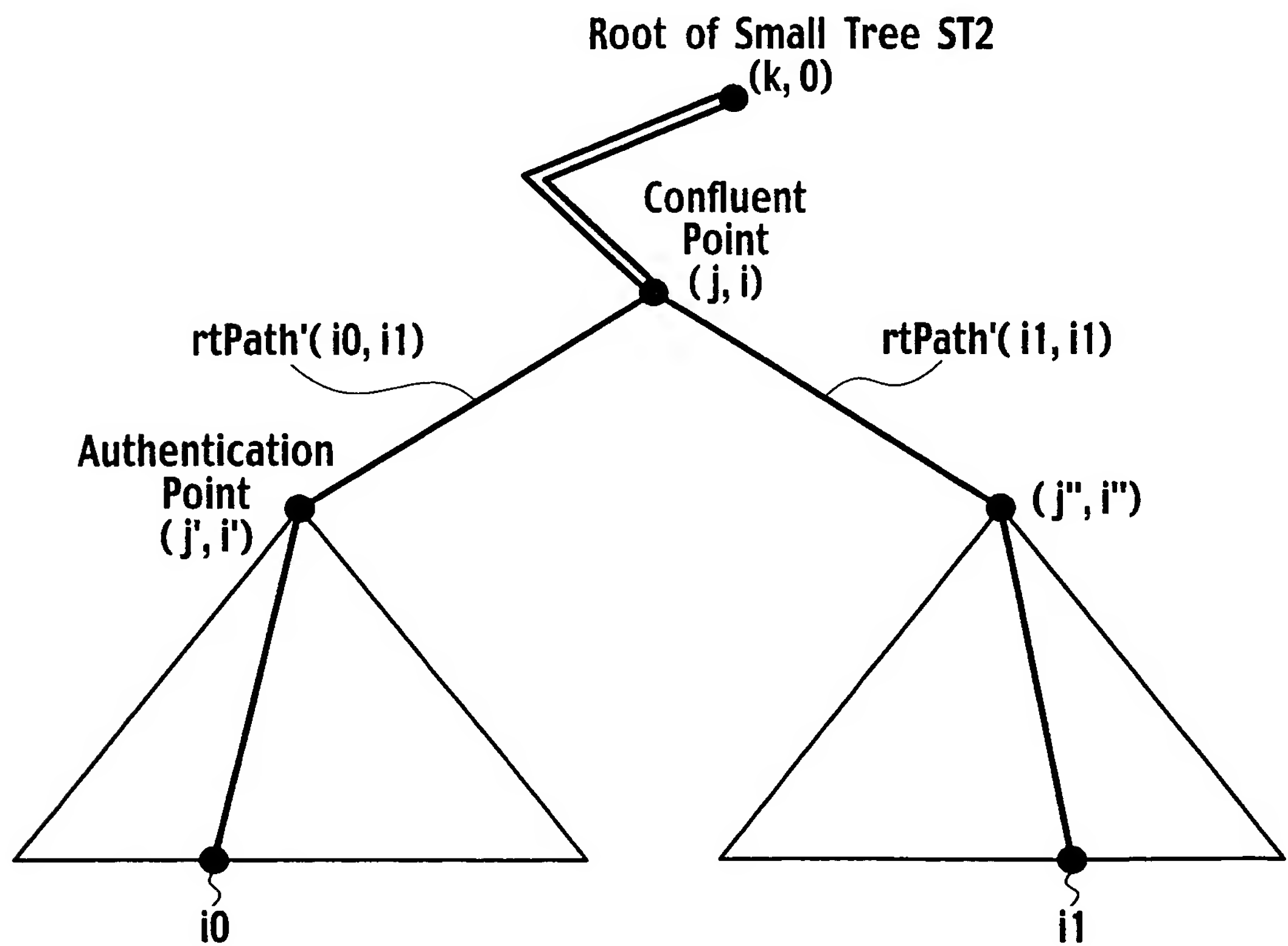
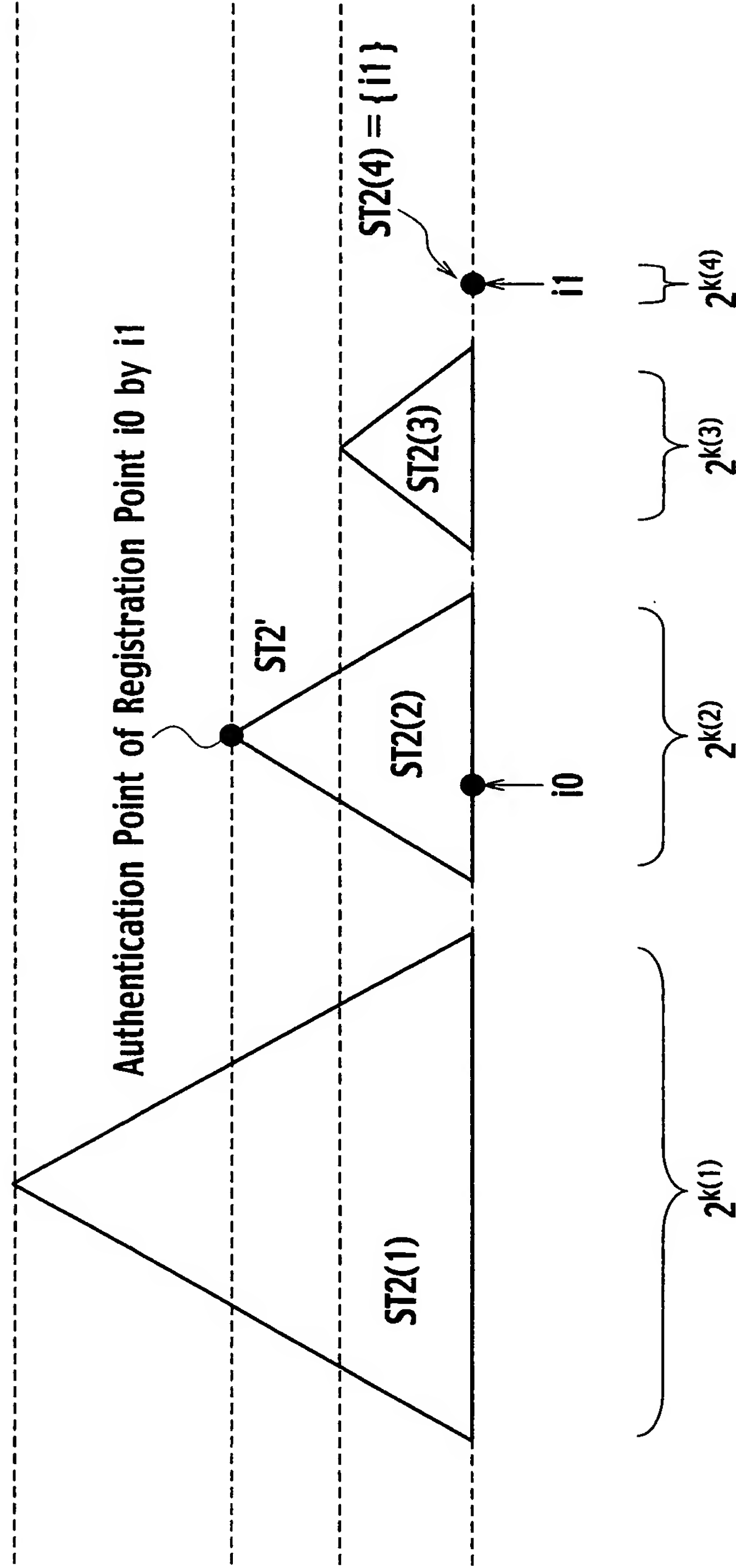


FIG. 80



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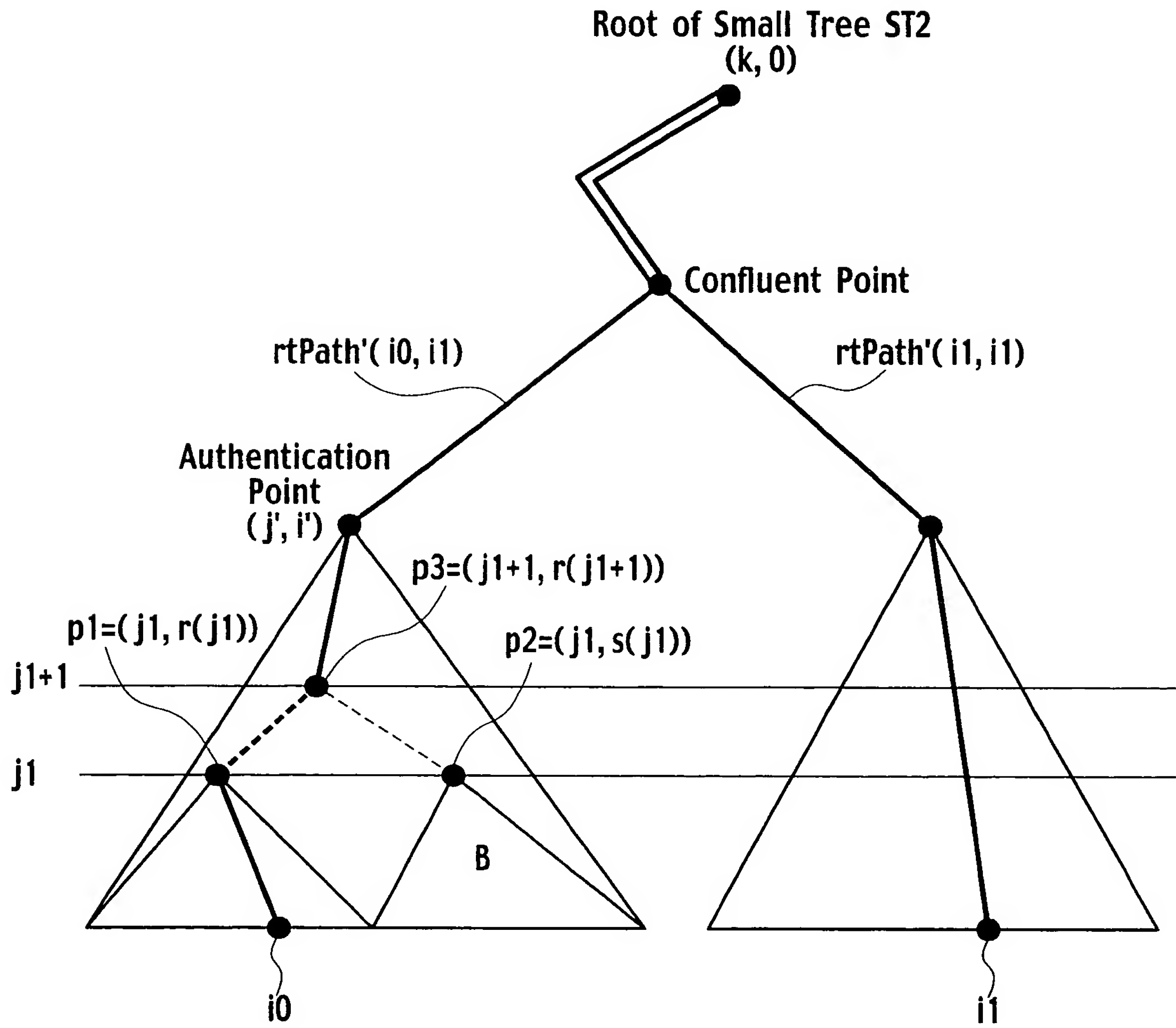
FIG. 81



$$k(1) > k(2) > k(3) > k(4) = 0$$

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FIG. 82



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FIG. 83

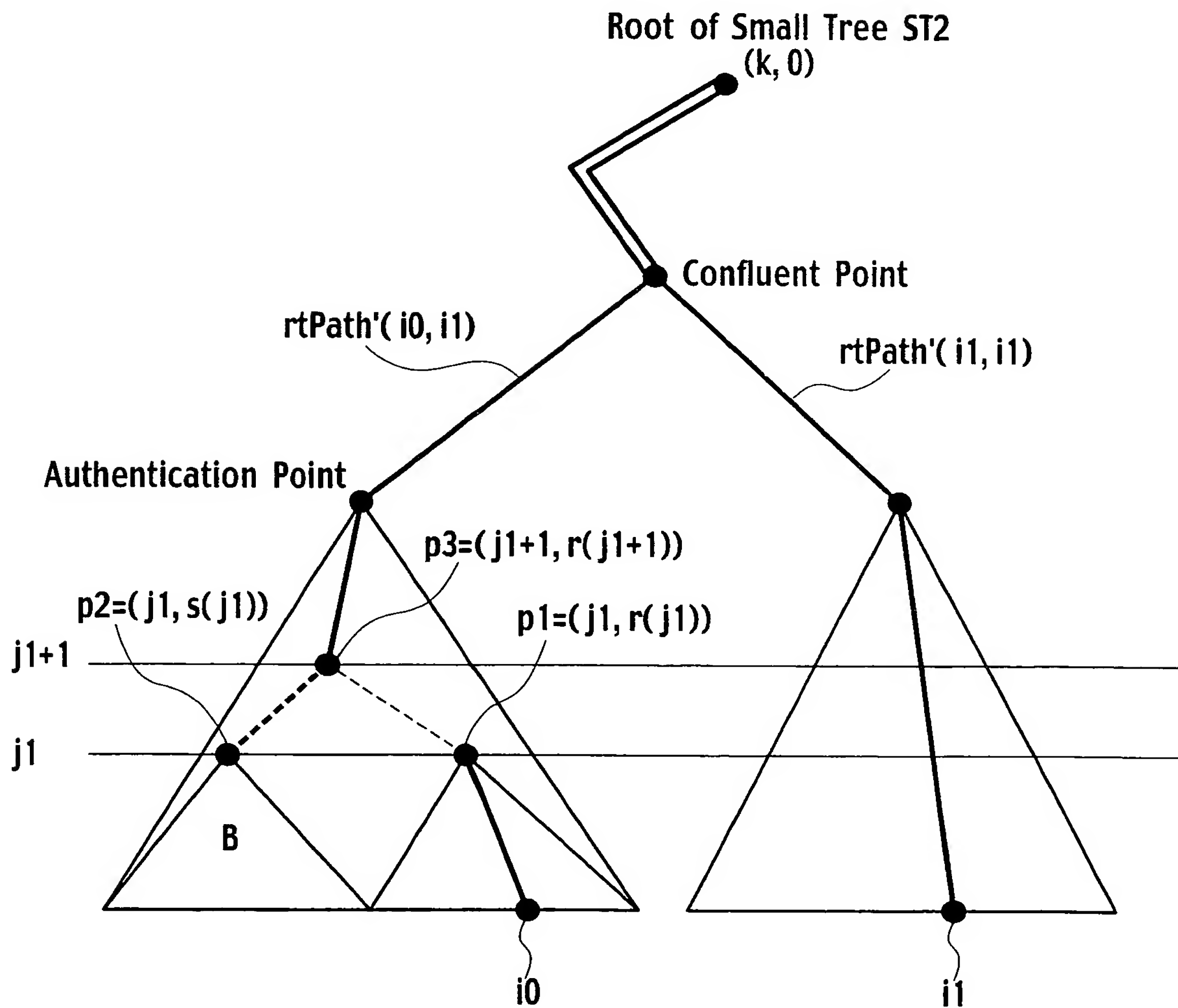
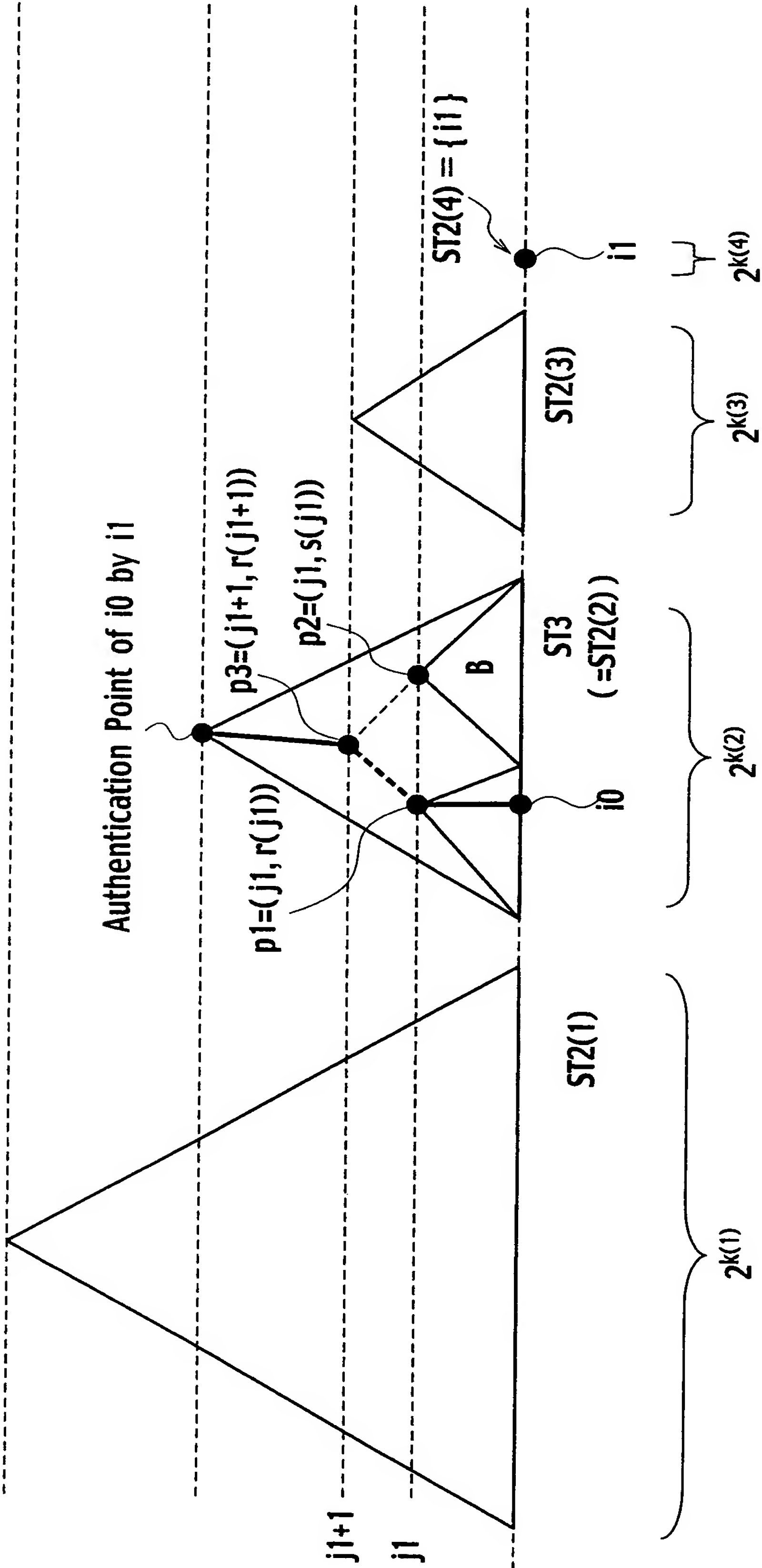
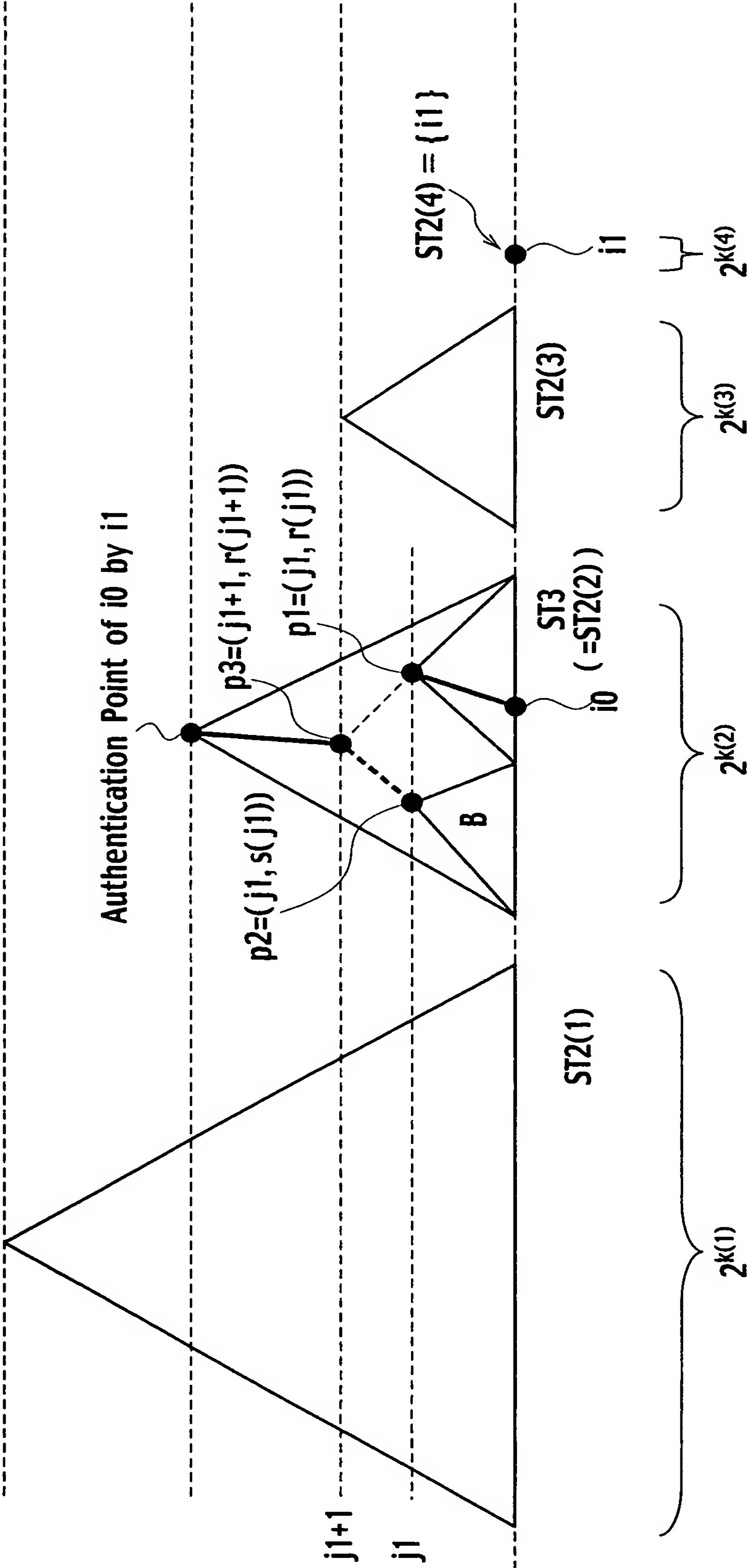


FIG. 84



$$k(1) > k(2) > k(3) > k(4) = 0$$

FIG. 85



$$k(1) > k(2) > k(3) > k(4) = 0$$